

COMPOSITIONAL VERIFICATION
AND SPECIFICATION OF
REFINEMENT FOR REACTIVE
SYSTEMS IN A DENSE TIME
TEMPORAL LOGIC

Dissertation
zur Erlangung des Doktorgrades
der Technischen Fakultät
der Christian-Albrechts-Universität
zu Kiel

vorgelegt von
Antonio Cau

Kiel,
im August 1995

Berichterstatter: Prof. Dr. Willem-Paul de Roever
.....

Tag der mündlichen Prüfung : 14 Dezember 1995

Zum Druck genehmigt: Kiel, den 14 Dezember 1995

.....

(Dekan)

Abstract

This thesis introduces a compositional dense time temporal logic for the composition and refinement of reactive systems. A reactive system is specified by a pair consisting of a machine and a condition on the computations of this machine. In order to compose reactive systems, each step in a computation has additionally composition information such as “this is a system step”, or “this is an environment step” or “this is a communication step”. By defining a merge operator that merges two steps into one step compositionality is achieved. Because a dense time temporal logic is used refinement can be expressed easily in this logic. Existing proof rules for refinement are reformulated in our formalism. The notion of relative refinement is introduced to handle refinement of systems that only under certain conditions are considered to be correct refinements. The proof rules for “normal” refinement are extended to handle relative refinement of systems. Relative refinement is used to formalize Dijkstra’s development strategy for the solution of the readers/writers problem and to formalize a development strategy for certain fault tolerant systems. This development strategy is applied to the development of a fault tolerant storage system.

Acknowledgements

I would like to thank my advisor Willem-Paul de Roever for his guidance and his scientific support especially during the first two years of writing this thesis. Without his support this thesis would be just “spaghetti code”. Special thanks go to Pierre Collette, the collaboration with whom was very agreeable and fruitful and led to many insights. I would like to thank all my colleagues of the group “Software Technology” at the Christian-Albrecht-University for their support, especially Yassine Lakhneche, who made many invaluable comments during the last stage of the thesis, Kai Engelhardt, who acted as a “finite variability condition” with respect to the changes I wanted to make in my thesis, and Qiwen Xu with whom I started to collaborate after he left the group. Also members of the other groups at the Christian Albrechts University are thanked for their comments, especially Thomas Wilke. I also want to thank Ruurd Kuiper for his help and comments during the first and last stage of the thesis. Finally I would like to thank the Personalrat and the Technical Faculty of the Christian Albrechts University in the persons of Reimer Hansen and Frank Paul for their efforts in extending my contract for 4 months, thus enabling me to finish this thesis. Last but not least I want to thank Anne Straßner for her (moral) support during the last stage of the thesis.

Contents

Abstract	iii
Acknowledgements	v
1 Introduction	1
2 A Dense Model Formalism	3
2.1 Introduction	3
2.2 Specification of Reactive Systems	6
2.2.1 Semantic Specification of Reactive Systems	6
2.2.2 DTL Specification of Reactive Systems	16
2.3 Refinement and Composition of Reactive System Specifications	29
2.3.1 Semantic Refinement and Composition of Specifications	29
2.3.2 Refinement and Composition of DTL Specifications	31
2.4 Proving Refinement of Reactive System Specifications	37
2.4.1 Proving Semantic Refinement of Specifications	37
2.4.2 Proving Refinement of DTL Specifications	38
2.5 Relative Refinement and Composition of Reactive System Specifications	43
2.5.1 Semantic Relative Refinement and Composition of Specifications	43
2.5.2 Relative Refinement and Composition of DTL Specifications	44
2.5.3 Proving Semantic Relative Refinement of Specifications	45
2.5.4 Proving Relative Refinement of DTL Specifications	46
3 Readers/Writers Example	47
3.1 Introduction	47
3.2 The abstract specification	48
3.2.1 Specification $\mathcal{S}_{r_i^0}$	48
3.2.2 Specification $\mathcal{S}_{w_j^0}$	49
3.2.3 Requirement W_0	51
3.3 The first development step	51
3.3.1 Specification $\mathcal{S}_{r_i^1}$	52
3.3.2 Specification $\mathcal{S}_{w_j^1}$	53
3.3.3 Requirement W_1	56
3.3.4 \mathcal{S}_1 relatively refines \mathcal{S}_0	56
3.4 The second development step	59
3.4.1 Specification $\mathcal{S}_{r_i^2}$	60
3.4.2 Specification $\mathcal{S}_{w_j^2}$	62

3.4.3	Requirement W_2	65
3.4.4	\mathcal{S}_2 relatively refines \mathcal{S}_1	66
3.5	The third development step	71
3.5.1	Specification $\mathcal{S}_{r_i^3}$	72
3.5.2	Specification $\mathcal{S}_{w_j^3}$	74
3.5.3	Requirement W_3	78
3.5.4	\mathcal{S}_3 relatively refines \mathcal{S}_2	78
3.6	The fourth development step	83
3.6.1	Specification $\mathcal{S}_{r_i^4}$	84
3.6.2	Specification $\mathcal{S}_{w_j^4}$	87
3.6.3	\mathcal{S}_4 relatively refines \mathcal{S}_3	90
4	Stable Storage Example	95
4.1	The General Methodology	95
4.2	Application: Introduction	96
4.3	First Step: Stable Storage	97
4.3.1	Introduction	97
4.3.2	Specification	97
4.4	Second Step: Physical Disk	99
4.4.1	Introduction	99
4.4.2	Specification	99
4.4.3	Requirement W_P	101
4.4.4	\mathcal{S}_P relatively refines \mathcal{S}	101
4.5	Third Step: Fail-Stop Detection Layer	104
4.5.1	Introduction	104
4.5.2	Specification	104
4.5.3	Requirement W_{D_s}	109
4.5.4	$\mathcal{S}_{D_s} \parallel \mathcal{S}_P$ relatively refines \mathcal{S}_P	109
4.6	Fourth Step: Error Recovery Layer	114
4.6.1	Introduction	114
4.6.2	Specification of the Recovery Layer	115
4.6.3	Specification of the Detection Layer	119
4.6.4	Requirement W_R	123
4.6.5	$\parallel_{i=1}^N (\mathcal{S}_{D_i} \parallel \mathcal{S}_{P_i}) \parallel \mathcal{S}_R$ relatively refines $\mathcal{S}_{D_s} \parallel \mathcal{S}_P$	124
	Bibliography	134
A	Proofs of Dense Model Theorems	139
A.1	Proof of Theorem 1	139
A.2	Proof of Lemma 1	140
A.3	Proof of Theorem 2	140
A.4	Proof of Lemma 2	149
A.5	Proof of Lemma 3	166
A.6	Proof of Lemma 4	166
A.7	Proof of Lemma 5	167
A.8	Proof of Theorem 3	168
A.9	Proof of Lemma 6	169
A.10	Proof of Theorem 4	169

A.11 Proof of Theorem 5	170
A.12 Proof of Theorem 6	171
A.13 Proof of Lemma 7	171
A.14 Proof of Lemma 8	171
A.15 Proof of Theorem 7	172
A.16 Proof of Lemma 9	173
A.17 Proof of Lemma 10	174
A.18 Proof of Lemma 11	175
A.19 Proof of Theorem 8	175
A.20 Proof of Theorem 9	176
A.21 Proof of Lemma 12	176
A.22 Proof of Lemma 13	177

List of Tables

2.1	Syntax of DTL	16
2.2	Used abbreviations	18

List of Figures

2.1	Computation of a machine.	4
2.2	Concrete computation.	5
2.3	Collapsed history.	11
2.4	Abstract machine	34
2.5	Concrete machine 1	35
2.6	Concrete machine 2	36
2.7	Transitions of $\mathcal{S}_{c1} \parallel \mathcal{S}_{c2}$	40
3.1	Transitions of reader $_i^0$	49
3.2	Transitions of writer $_i^0$	50
3.3	Transitions of reader $_i^1$	54
3.4	Transitions of writer $_j^1$	55
3.5	Transitions of reader $_i^2$	63
3.6	Transitions of writer $_j^2$	65
3.7	Transitions of reader $_i^3$	75
3.8	Transitions of writer $_j^3$	77
3.9	Transitions of reader $_i^4$	86
3.10	Transitions of writer $_j^4$	89
4.1	Transitions of stable storage.	98
4.2	Transitions of the physical disk.	101
4.3	Transitions of the fail-stop detection layer.	108
4.4	Transitions of the relative composed system.	111
4.5	Transitions of the error recovery layer.	120
4.6	Transitions of the detection layer.	124
4.7	Transitions of the final implementation of stable storage.	129

Chapter 1

Introduction

Current formal methods are far from solving the problems in software development. The simplest view of the formal paradigm is that one starts with a formal specification and subsequently decomposes this specification in subspecifications which composed together form a correct refinement. These subspecifications are decomposed into “finer” subspecifications. This refinement process is continued until one gets subspecifications for which an implementation can easily be given. This view is too idealistic in a number of respects. First of all, most specifications of software are wrong (certainly most informal ones, unless they have been formally analyzed) and contain inconsistencies [PWT90]. Secondly, even if a formal specification is produced, this is only after a number of approximation steps because writing a correct specification is an even more difficult process than producing a correct implementation, and should therefore be structured, resulting in a number of increasingly less abstract layers with specifications which tend to increase in detail (and therefore become less readable [LGdR79]). Thirdly, even an incorrect refinement step may be useful in the sense that from this incorrect refinement step one can sometimes easier derive the correct refinement step. This is especially the case with intricate algorithms such as those concerning specific strategies for solving the mutual exclusion problem. An interesting illustration of this third view is provided by E.W. Dijkstra’s “Tutorial on the split binary semaphore” [Dij79] in which he solves the readers/writers problem by subsequently improving incorrect refinement steps till they are correct. If this master of style prefers to approximate and finally arrive at his correct solution using formally “incorrect” intermediate stages, one certainly expects that a formally correct development process for that paradigm is difficult to find! The strategy described in [Dij79] is necessarily informal, reflecting the state of the art in 1979.

In Chapter 2 a dense time formalism is introduced for the specification and verification of refinement of systems based on [BKP86, DK90, KMP93, Sta84, Sta85, Sta88]. This formalism will be used to describe above strategy of incorrect intermediate stages. A dense time formalism is used because it allows one to deal with the stutter-problem (explained in section 2.1) and it enables one to express hiding of “internal” variables by existential quantification. Instead of using the assumption/commitment approach of [AL93a, AL93b, Jon83, MC81, PJ91, Pnu85, Sti88, Stø91, WD88, ZdBdR84, ZdRvEB84], unified in [XCC94, CC94], in order to achieve compositionality an event variable is used

that stores “compositionality information” like “this is a system step” or “this is an environment step” or “this is a communication step”. A merging operator, first version defined in [Acz83], based on the one defined in [CC94] is introduced to merge this “compositionality information” of the components into “compositionality information” of the composed system. The use of event variables has as second advantage that existing proof rules for refinement like those in [Lam91, KMP93] can easily be extended to our framework. The notion of relative refinement is introduced to handle “incorrect” development steps. The system specification is therefore extended by a requirement that extracts the “good” computations of the system. The refinement proof rules are extended to handle relative refinement so that the correct part of incorrect development steps can be proven correct.


In Chapter 3 we present Dijkstra’s development strategy of the readers/writers problem [Dij79] in our formalism. A preliminary version of this formalization, without proofs, appeared in [CKdR92] using the original formalism of [Sta84]. Our formalism preserves the flavour of the informal strategy in that it formalises Dijkstra’s argumentation in terms of incorrect approximations to a correct program and provides a formal criterion for recognising when a formally correct end product, the correct program, has finally been reached.

In Chapter 4 we present a formal development strategy for the development of certain fault tolerant systems using our notion of relative refinement. A preliminary version of this strategy appeared in [CdR93b, CdR93a] using the original formalism of [Sta84]. The formal strategy is as follows: one starts with an implementation for a specified fault tolerant system. This implementation contains some faults, i.e., the refinement step is incorrect because of these faults. It is however relative correct because when these faults don’t occur it is a correct implementation. In the next step we try to detect these faults, i.e., we construct a detection layer upon the previous implementation that stops that implementation when it detects an error caused by these faults. This is called a fail-stop implementation [LA90] and represents an improvement over the previous implementation because now at least the implementation stops on the occurrence of such a fault. The second implementation is also relatively correct because no occurrence of faults and the detection layer doesn’t detect any error due to a fault then the second implementation is correct. In the third approximation we recover these errors, i.e., we don’t stop anymore upon the detection of an error but merely recover the error by executing some special program that neutralizes that error. This third approximated refinement step is correct under the assumption that certain conditions are fulfilled, which exclude the occurrence of faults different from those whose errors are neutralized, i.e., it is again relative correct. This strategy is used for the development of a fault tolerant storage system, a so called stable storage.

Chapter 2

A Dense Model Formalism

2.1 Introduction

 In this chapter we present a refinement method for reactive systems. A system is called reactive if it maintains some ongoing interaction with its environment, for example an operating system. This contrasts with transformational systems where from some input without further interaction output is produced. Because of this characteristic reactive systems should be described as sets of behaviours (histories). The underlying model for these behaviours is dense. The method which we present is based on the work of E.W. Stark [Sta84, Sta85, Sta88]. Here we present a framework which can model both CSP based and shared variable based concurrency, using the work of [BKP86, DK90, KMP93].

In section 2.2 reactive systems are specified by sets of histories together with a basis. A history is pair consisting of an event and a state function. The domains of these functions are the non-negative real numbers (the underlying dense model). The event function maps each non-negative real number to an event (an action occurring during the operation of the system and its environment) and the state function maps each real number to a state of the system and its environment. The intuition is that an occurrence of an action causes (potentially) a state change as illustrated in Figure 2.1. The basis is a pair consisting of an action basis and a process basis, where the action basis specifies the input and output channels over which the system communicates with its environment and the process basis specifies the local (only accessible by the system) and shared (accessible by both system and its environment) variables. Due to this basis composition of reactive systems corresponds to conjunction. Note that in for instance Lamport's work on TLA [Lam91, Lam94, Lam] this is not always the case: $x := 1 \parallel x := 1$ must be modeled as disjunction because conjunction leads to a "one process" specification $x := 1$. In our model however, it can be modeled as conjunction because the specification of one component also contains environmental information, especially about the other component. With a "conjoining" operator the histories of both components are merged into a history of the composite one. This conjoining operator based on [CC94] corresponds in our model almost to conjunction and is actually an extended version of Aczel's one [Acz83] because it also can handle CSP based concurrency whereas Aczel's one can only handle shared variable

based concurrency.

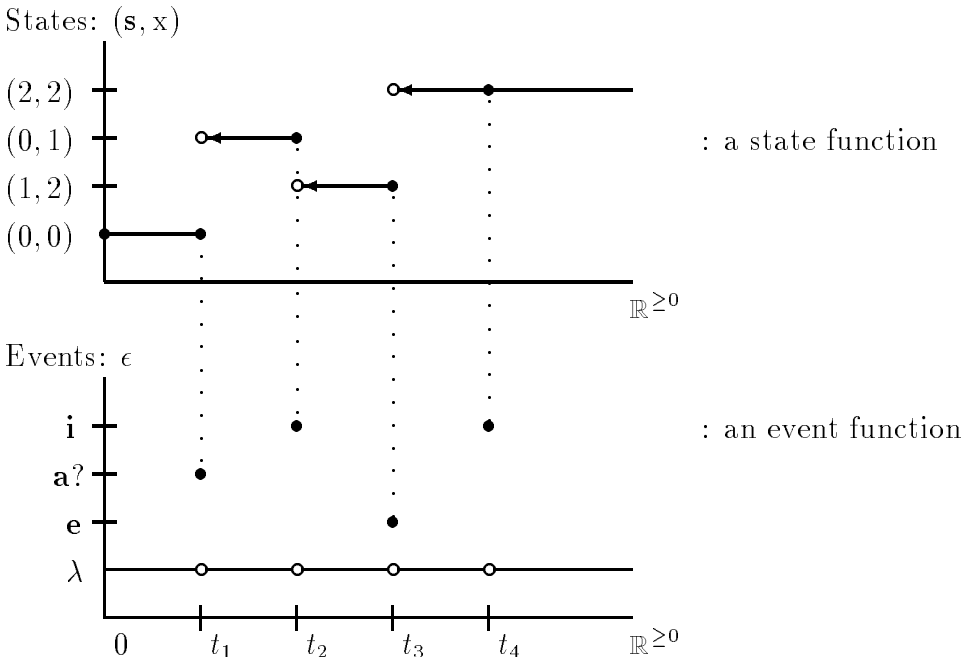


Figure 2.1: This picture illustrates the notion of state and event function, which together characterize the notion of computation of a machine. It illustrates the following computation: initially $(s, x) = (0, 0)$, the event $a?$ changes x into 1, i.e., s doesn't change. In the interval $[0, t_1]$ there are only λ events. The event i at point t_2 changes (s, x) into $(1, 2)$, at point t_3 the event e changes s into 2 and at point t_4 the event i doesn't change s or x .

A notion of a machine is introduced for generating these histories, i.e., a history is a computation generated by a machine. With this machine notion only safety properties, i.e., sets of histories generated by a machine, of a system can be specified, so an extra condition on the computations of this machine is introduced for specifying liveness properties of the system.

The use of real numbers as domain for the event and state function handles the stutter problem. This problem, first observed by Lamport [Lam83, Lam89], is as follows. Given two behaviours of a system, let the first behaviour contain only consecutive snap-shots of the system that differ from each other whereas the second behaviour contains the same snap-shots but also some consecutive ones that are identical. This is called stuttering. From the viewpoint of an observer these behaviours are considered as equal. Consequently, any formalism that allows to distinguish between these behaviours is not abstract enough and has a power of discrimination which is too strong. An example of such a formalism is linear temporal logic with a next operator \bigcirc . In the present formalism this excessive expressive power is avoided as follows: state changes caused by events happen only now and then, so that in between each two consecutive changes there are uncountably many instants of time at which *nothing* happens. Consequently, it is impossible to count, or express, stutter steps. Furthermore the use of real numbers for defining the event and state function enables us to express hiding of variables as existential quantification and consider refinement as implication, even if there are more "states" on the abstract level than on the concrete level: let the history illustrated in Figure 2.1 be a history at the

2.1 Introduction

abstract level where x is the variable that should be hidden and let the history illustrated in Figure 2.2 be a history at the concrete level. The history of Figure 2.2 is a refinement of the history of Figure 2.1.

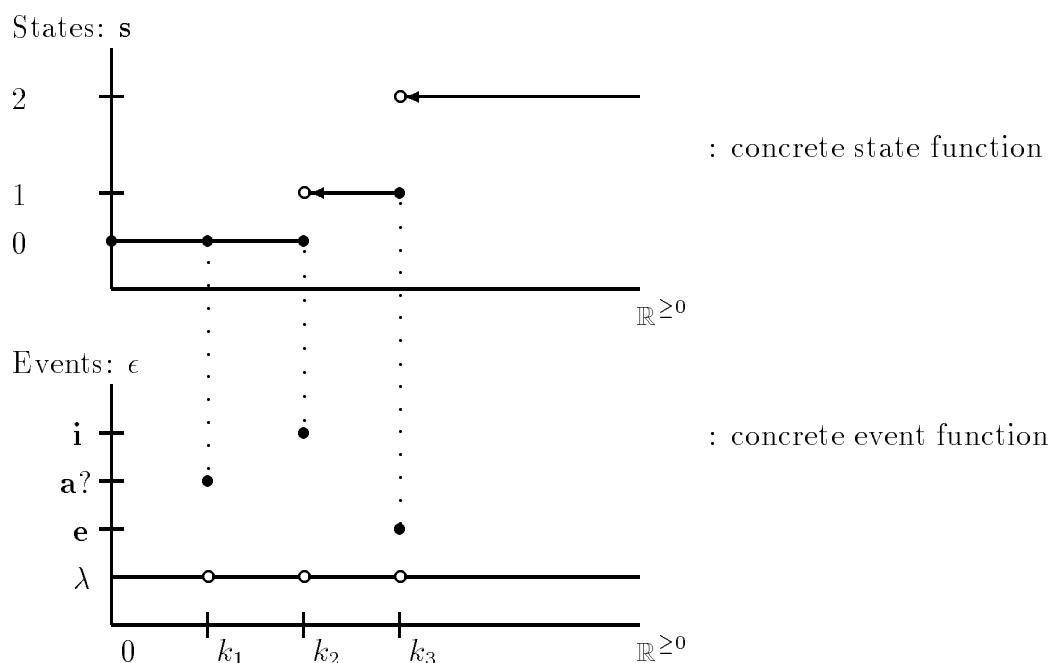


Figure 2.2: This picture illustrates the following concrete computation: initially $s = (0)$, the event $a?$ doesn't change s , the event i changes s into 1, and the event e changes s into 2.

A dense time temporal logic DTL based on histories is introduced in section 2.2.2. This logic is based on [Sta84, Sta85, BKP86, DK90, KMP93]. A salient feature of the dense time temporal logic is the “immediately after” operator $'$, in a version which Lamport [Lam83] approves of, i.e., it is stutter insensitive. In this logic the notion of a machine and the condition on the computations of that machine will be expressed. It is also possible to express in this logic whether a system refines another system, i.e., the set of histories of the first system is a subset of the histories of the second one and the “observable” part of the abstract basis (i.e., observable from outside of the component) is equal to the “observable” part of the concrete basis. In our model initial stuttering is incorporated by default (cf. [DK90]) and refinement can be expressed using implication and existential quantification.

In section 2.3 the notions of composition and refinement of systems are defined. Firstly in terms of histories (semantically) and secondly in the dense time temporal logic DTL (syntactically). It is also investigated how composition relates to refinement, i.e., the notion of compositional refinement [ZCdR92] is given. Compositional refinement means intuitively that if the components of an abstract composed system are refined by the components of a concrete composed system then the abstract composed system is refined by the concrete composed system, i.e., refinement is preserved under composition.

Section 2.4 gives proof rules for refinement based on those given in [Lam91, KMP93]. These proof rules split the proof of refinement of systems into (1) a proof of refinement of the safety parts of the systems and (2) a proof of refinement of the liveness part of the systems.

Section 2.5 explains how the formalism can be used to describe relative (incorrect) refinement steps as discussed in Chapter 1. Also the notion of relative composition is introduced which intuitively means that only restricted parts of the components are composed together. The notion of compositional refinement of section 2.3 is extended to compositional relative refinement. The proof rules for refinement of section 2.4 are extended to handle relative refinement. These proof rules are used extensively in the readers/writers example of Chapter 3 and the stable storage example of Chapter 4.

2.2 Specification of Reactive Systems

This section explains how reactive systems can be specified. Firstly they will be specified at the semantical level, i.e., by sets of histories. A history intuitively specifies which event occurs at a particular point and in what state the system is at that particular point. Secondly reactive systems are specified using the dense time temporal logic DTL.

2.2.1 Semantic Specification of Reactive Systems

In [Sta84] a method for specifying reactive systems is introduced. Such systems are characterized by sets of histories. A history is a pair consisting of an event function and a state function. An event function records at each point (i.e., element of the positive reals, including zero) which *event* occurs. An event is an instantaneous occurrence of an action during the operation of a system, that can be generated by that system or its environment and that is of interest at the given level of abstraction. Four kinds of actions are distinguished:

1. *communication* actions $\mathbf{a?}, \mathbf{b!}$, i.e., actions that transmit information over a channel. A channel is a connection between the system and its environment.
2. *system* actions \mathbf{i} , i.e., non-communication actions of the system.
3. *environment* actions \mathbf{e} , i.e., non-communication actions of the environment.
4. *silent* actions λ , i.e., actions that don't influence the status of the system.

Event states are introduced in order to record which event occurs during the operation of the system. An event state is like the usual notion of state with the exception that instead of normal program variables *event variables* are used. An event state is defined formally in the following definition.

Definition 1 (Event variable and event state)

Let Chan denote the set of all channels. Let \mathfrak{E} denote the set of event variables with typical elements $\epsilon, \epsilon_0, \epsilon_1, \dots$. Event variable ϵ will record which action occurs during the operation of the system, and the event variables $\epsilon_0, \epsilon_1, \dots$ are auxiliary event variables recording which

2.2 Specification of Reactive Systems

actions occur in components of the system. Let \mathfrak{A} denote the set of actions, with typical elements \mathbf{i} (denoting system actions), \mathbf{e} (denoting environment actions), $\mathbf{a}?, \mathbf{b}!$ ¹ ... denoting respectively an input communication action over channel \mathbf{a} and an output communication action over channel \mathbf{b} , and λ denoting the silent action. An event state is a mapping δ from \mathfrak{C} to \mathfrak{A} . Let Δ denote the set of all event states.

An state function records at each point (a non-negative real number) the *process state*, i.e. the usual notion of state of a system and its environment. In order to distinguish the normal variables from the event variables the normal variables are called here *process variables*. Three kind of process variables are distinguished:

1. *shared* process variables which are “shared” between a system and its environment, and
2. *local* process variables which are only accessible by a system.
3. *rigid* variables which are not changed by the system and its environment, i.e., which are used for specification purposes.

The process state is defined formally in the following definition.

Definition 2 (Process variable and process state)

A process state is a mapping from variables to values. Let \mathfrak{S} denote the set of shared variables with typical elements \mathbf{s}, \dots , and \mathfrak{X} the set of local variables ($\mathfrak{S} \cap \mathfrak{X} = \emptyset$) with typical elements \mathbf{x}, \dots , and \mathfrak{R} the set of rigid variables with typical elements \mathbf{n}, \dots . A state is a mapping σ from $\mathfrak{S} \cup \mathfrak{X} \cup \mathfrak{R}$ to the set of values Val . Let Σ denote the set of all process states.

As already said above, event and state functions are mappings from the non-negative reals to, respectively event and process states. Because of this some requirements are needed in order to specify “reasonable” histories. Here reasonable is used in the sense that in a bounded interval only a finite number of non-silent actions and process state changes can occur. This requirement is called the finite variability condition [BKP86]. Next several notions for functions from $\mathbb{R}^{\geq 0}$ (the positive reals including 0) to some domain D are introduced in order to define this requirement and to formally define the event and state functions.

Definition 3 (Left and right constant, limit)

Given function $f : \mathbb{R}^{\geq 0} \rightarrow D$.

f is called **left constant** at $t \in \mathbb{R}^{\geq 0}$, if there exists a real number t_0 , $0 < t_0 < t$, such that $f(t_1) = d$ for all $t_1 \in (t_0, t)$. d is called the **left limit** of f at t , and is denoted by $\lim_{t_1 \rightarrow t} f(t_1)$.

f is called **right constant** at $t \in \mathbb{R}^{\geq 0}$, if there exists a real number t_0 , $t_0 > t$, such that $f(t_1) = d$ for all $t_1 \in (t, t_0)$. d is called the **right-limit** of f at t , and is denoted by $\lim_{t \leftarrow t_1} f(t_1)$.

¹In this chapter we omit the value part of the communication, i.e., which value is transmitted, in order to ease the formalism a little bit. In the example of the stable storage we will use this value part although it is not formally introduce in this chapter

Definition 4 (Left and right continuous, discontinuous)

Given function $f : \mathbb{R}^{\geq 0} \rightarrow D$.

f is called **left continuous**, if $f(t) = \lim_{t_1 \rightarrow t} f(t_1)$ for every $t > 0$.

f is called **right continuous**, if $f(t) = \lim_{t \leftarrow t_1} f(t_1)$ for every $t \geq 0$.

f is called **discontinuous** at t , if $f(t) \neq \lim_{t_1 \rightarrow t} f(t_1)$ or $f(t) \neq \lim_{t \leftarrow t_1} f(t_1)$.

f is called **strongly discontinuous** at t , if $f(t) \neq \lim_{t_1 \rightarrow t} f(t_1)$ and $f(t) \neq \lim_{t \leftarrow t_1} f(t_1)$.

Definition 5 (Finite variability)

Given function $f : \mathbb{R}^{\geq 0} \rightarrow D$.

f has the **finite variability** property iff f has only finitely many points of discontinuity in any interval $[a, b]$, $0 \leq a \leq b$, $a, b \in \mathbb{R}^{\geq 0}$.

Now *event* and *state* functions can be defined. [DK90] states that initial stuttering is needed in order to express refinement in a logic with the help of existential quantification and implication. We must first define what stuttering, in the sense of [DK90], is in our setting. In our setting a stutter step is a step in which a non-communication action doesn't change the state. So here this initial stuttering can be included by requiring that in the first interval the event function has the constant value λ and the state function remains constant there. Furthermore a state should remain constant for an interval of points in order to be observable. Also non- λ events are considered to be single points. Another possibility would be for the events to remain constant during an interval of points. The intuitive meaning of a history is that the points of non- λ event occurrence mark the state changes. For the non- λ events the question to be answered is: at which point of the interval should the state change take place? Answer: at the last point of the interval of the event. So for events only the last point of the interval is interesting because it marks the state change. So why consider an interval if only its last point is interesting? This is the explanation of the choice made here that the non- λ events occur only at single points. This is captured by the following definitions.

Definition 6 (restriction)

For $g : A_1 \rightarrow A_2$, $A_0 \subseteq A_1$ define $g|_{A_0}^1 : A_0 \rightarrow A_2$ as $g|_{A_0}^1(x) = g(x)$ for $x \in A_0$. If A_0 is a set containing only one element x then we will write $g|_x^1$ instead of $g|_{\{x\}}^1$.

For $g : A_1 \rightarrow (A_2 \rightarrow A_3)$, $A_0 \subseteq A_1$ define $g|_{A_0}^2 : A_0 \rightarrow (A_2 \rightarrow A_3)$ as $g|_{A_0}^2(t)(x) = g(t)(x)$ for $x \in A_2$. Again if A_0 is a set containing only one element x then we will write $g|_x^2$ instead of $g|_{\{x\}}^2$.

Definition 7 (Event function)

An **event function** ψ is a function from $\mathbb{R}^{\geq 0}$ to Δ , such that $\psi|_e^2$ has the finite variability condition, $\psi(0)(\epsilon) = \lambda$ (i.e. initial stuttering) and for all points t , ψ is strongly discontinuous at t iff $\psi(t)(\epsilon) \neq \lambda$ (i.e. an event function is almost constant λ). Let Ψ denote the set of all event functions.

Figure 2.1 illustrates the notion of event function. At point t_1 event **a?** occurs, at point t_2 event **i** occurs, at point t_3 event **e** occurs, and at all other points event λ occurs. Points t_1 , t_2 and t_3 are here the strongly discontinuous points.

2.2 Specification of Reactive Systems

Definition 8 (State function)

A **state function** θ is a left continuous function from $\mathbb{R}^{\geq 0}$ to Σ such that for all $n \in \mathfrak{N}$ and $t \in \mathbb{R}^{\geq 0}$, $\theta(t)(n) = \theta(0)(n)$ (i.e., the rigid variables don't change at all), and for all $x \in \mathfrak{V} \cup \mathfrak{X}$, $\theta|_x^2$ satisfies the finite variability property and $\theta|_x^2(0)(x) = \lim_{0 \leftarrow t_1} \theta|_x^2(t_1)(x)$ (i.e. initial stuttering). Let Θ denote the set of all state functions.

Figure 2.1 illustrates the notion of state function. In interval $[0, t_1]$ the system is in state $(\mathbf{s}, \mathbf{x}) = (0, 0)$, in interval $(t_1, t_2]$ in state $(\mathbf{s}, \mathbf{x}) = (0, 1)$, in interval $(t_2, t_3]$ in state $(\mathbf{s}, \mathbf{x}) = (1, 2)$ and in interval $(t_3, \infty]$ in state $(\mathbf{s}, \mathbf{x}) = (2, 2)$. The event \mathbf{i} at t_4 is an illustration of a non- λ stutter step.

The following definition combines the notions of state function and event function into the notion of history. Two requirements are imposed on the combination of event and state function in order to be a history. The first requirement is that silent actions don't give rise to process state changes. The second requirement is that communication actions don't change the shared variables; this requirement is imposed in order to model CSP [Hoa84] like processes.

Definition 9 (History)

A **history** h is a pair $\langle \psi, \theta \rangle$, where ψ is an event function and θ is a state function s.t. a λ action doesn't change the values of variables from $\mathfrak{V} \cup \mathfrak{X}$, i.e.:

$$\forall t : \psi(t)(\epsilon) = \lambda \rightarrow \theta(t) = \lim_{t \leftarrow t_1} \theta(t_1)$$

and a communication action doesn't change the values of shared variables, i.e.:

$$\begin{aligned} \forall t : \psi(t)(\epsilon) = \mathbf{a}^? \rightarrow \theta(t)|_{\mathfrak{S}}^1 &= \lim_{t \leftarrow t_1} \theta(t_1)|_{\mathfrak{S}}^1 \\ \forall t : \psi(t)(\epsilon) = \mathbf{a}! \rightarrow \theta(t)|_{\mathfrak{S}}^1 &= \lim_{t \leftarrow t_1} \theta(t_1)|_{\mathfrak{S}}^1 \end{aligned}$$

Let \mathcal{H} denote the set of all histories.

The following definition defines when a history is stutter equivalent to another history. A *history collapse* function is introduced that takes a history and collapses it in such a way that the non-stutter steps only occur at discrete points (elements of \mathbb{N}) and at all remaining points stutter steps occur. Also a restricted version of the history stutter equivalence relation is defined, namely, restricted to the process state information. The last one will be used to define a “process state history stutter insensitive” logic DTL. This logic will be restricted to a special kind of formulae in order to obtain the “history stutter insensitive” logic.

Definition 10 (History collapse, stutter equivalent)

Given history $h \in \mathcal{H}$, the **history collapse** denoted $\mathfrak{h}_b(h)$ is a function from \mathcal{H} to \mathcal{H} defined as $\mathfrak{h}_b(h) \triangleq h \circ \text{di}(h)$ where $\text{di}(h)$ is the **discretization** bijection for h from $\mathbb{R}^{\geq 0}$ to $\mathbb{R}^{\geq 0}$ and is defined as follows:

Let $\text{tt}(h, k)$ be the function from $\mathcal{H} \times \mathbb{N}$ to $\mathbb{R}^{\geq 0}$ that gives the point in $\mathbb{R}^{\geq 0}$ of the k -th change in h , formally:

$$\begin{aligned} \text{tt}(h, 0) &\triangleq 0 \\ \text{for } k > 0, \\ \text{tt}(h, k) &\triangleq \min(t : t > \text{tt}(h, k-1) \wedge (\psi(t)(\epsilon) \notin \{\lambda, \mathbf{i}, \mathbf{e}\} \vee \theta(t) \neq \lim_{\text{tt}(h, k-1) \leftarrow t_1} \theta(t_1))) \end{aligned}$$

Let $nn(h)$ denote the number of non-stutter points of h . Then the discretization bijection $di(h)$ for h is defined as follows:

$$di(h)(t) \stackrel{\Delta}{=} \begin{cases} tt(h, k) + (t - k) * (tt(h, k + 1) - tt(h, k)) & nn(h) < \infty \wedge 0 \leq k < nn(h) \\ & \wedge k \leq t \leq k + 1 \\ tt(h, k) + (t - k) & nn(h) < \infty \wedge k = nn(h) \wedge k \leq t \\ tt(h, k) + (t - k) * (tt(h, k + 1) - tt(h, k)) & nn(h) = \infty \wedge 0 \leq k \wedge k \leq t \leq k + 1 \end{cases}$$

The inverse discretization of h is denoted $di^{-1}(h)$.

Given histories $h_0, h_1 \in \mathcal{H}$, h_0 is **history stutter equivalent** to h_1 denoted $h_0 \simeq_h h_1$ iff

$$\begin{aligned} nn(h_0) &= nn(h_1), & \text{and} \\ \theta_{\natural_b(h_0)} &= \theta_{\natural_b(h_1)}, & \text{and} \\ \psi_{\natural_b(h_0)}(k) &= \psi_{\natural_b(h_1)}(k), & k \leq nn(h_0) \end{aligned}$$

i.e., the number of non-stutter steps should be equal, the state information should be equal in both collapsed histories and the event information should be equal in the points of non-stuttering. A restricted version of the history stutter equivalence relation is the one that considers only the process state information, i.e., h_0 is **history process state stutter equivalent** to h_1 denoted $h_0 \simeq_{\theta_h} h_1$ iff

$$\begin{aligned} nn(h_0) &= nn(h_1), & \text{and} \\ \theta_{\natural_b(h_0)} &= \theta_{\natural_b(h_1)}, \end{aligned}$$

Application of above definition to the history of Figure 2.1 results in: $tt(h, 0) = 0$, $tt(h, 1) = t_1$, $tt(h, 2) = t_2$, $tt(h, 3) = t_3$, and $tt(h, k) = \infty$ for $k > 3$ and $nn(h) = 3$. The discretization function $di(h)t$ is as follows:

$$\begin{cases} t * t_1 & 0 \leq t \leq 1 \\ t_1 + (t - 1) * (t_2 - t_1) & 1 \leq t \leq 2 \\ t_2 + (t - 2) * (t_3 - t_2) & 2 \leq t \leq 3 \\ t_3 + (t - 3) & 3 \leq t \end{cases}$$

The collapsed history $\natural_b(h_0)$ is illustrated in Figure 2.3.

The following theorem relates histories to a special kind of infinite sequences of pairs of event and process states, in which sequences start with an λ action, followed by possibly stuttering actions, then followed by exactly one non-stuttering action etc. Furthermore should every non- λ event be surrounded by λ -events. These kind of sequences are inspired by those defined in [KMP93]. For these kind of sequences a *sequence collapse* is defined that removes all finite stuttering; with the help of this collapse operator the sequence stutter equivalence operator is defined.

Definition 11 (Infinite sequences)

Define a sequence element as a pair (δ, σ) of an event and a process state. Let sel_i ($i \geq 0$) be

2.2 Specification of Reactive Systems

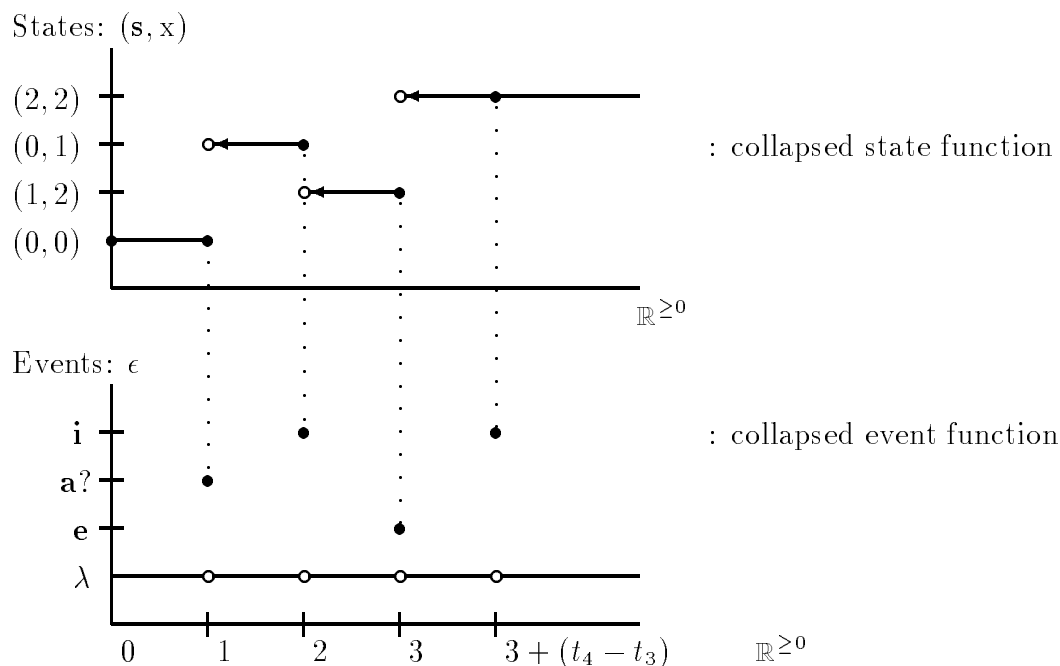


Figure 2.3: This picture illustrates the collapsed history of Figure 2.1

the pair (δ_i, σ_i) then the sequence seq is a infinite sequence of the form $seq_0 sel_0 seq_1 sel_1 \dots$ where $sel_i = (\delta_i, \sigma_i)$ is such that

$$\begin{aligned} \delta_i(\epsilon) &\neq \lambda, \\ (\delta_i(\epsilon) = \mathbf{a?} \vee \delta_i(\epsilon) = \mathbf{a!}) &\rightarrow \sigma_i|_{\mathbb{S}}^1 = \sigma_{i+1}|_{\mathbb{S}}^1 \\ \delta_i(\epsilon) \in \{\mathbf{i}, \mathbf{e}\} &\rightarrow \sigma_i \neq \sigma_{i+1} \end{aligned}$$

and seq_i is a sequence of the form $(\delta_{i1}, \sigma_i)^{n_i} ((\delta_{i2}, \sigma_i)(\delta_{i1}, \sigma_i)^{k_i})^{l_i}$ where $n_i > 0$, $k_i > 0$ and $l_i \geq 0$, and $\delta_{i1}(\epsilon) = \lambda$ and $\delta_{i2}(\epsilon) \in \{\mathbf{i}, \mathbf{e}\}$.

Let SEQ denote the set of all such sequences. Let seq be a sequence of the above form then $\mathfrak{h}_s(seq) = (sel'_i)_{i \geq 0}$ is the stutter free sequence obtained from seq by deleting all finite stuttering from seq . Formally: Let $ns(seq)$ denote the number of non-stutter steps in seq , if $ns(seq) = \infty$:

$$\begin{aligned} sel'_{2 \times i + 1} &= sel_i \quad 0 \leq i \\ sel'_{2 \times i} &= (\delta_{i1}, \sigma_i) \quad 0 \leq i \end{aligned}$$

if $ns(seq) < \infty$:

$$\begin{aligned} sel'_{2 \times i + 1} &= sel_i \quad 0 \leq i < ns(seq) \\ sel'_{2 \times i + 1} &= (\delta_{k1}, \sigma_k) \quad k = ns(seq) \wedge k \leq i \\ sel'_{2 \times i} &= (\delta_{i1}, \sigma_i) \quad 0 \leq i \leq ns(seq) \\ sel'_{2 \times i} &= (\delta_{k1}, \sigma_k) \quad k = ns(seq) \wedge k \leq i \end{aligned}$$

Let seq_0 and seq_1 be sequences then seq_0 is stutter equivalent to seq_1 denoted $seq_0 \simeq_s seq_1$ iff: let $\mathfrak{h}_s(seq_0) = (sel_i^0)_{i \geq 0}$ and $\mathfrak{h}_s(seq_1) = (sel_i^1)_{i \geq 0}$,

$$\begin{aligned} ns(seq_0) &= ns(seq_1) \quad \text{and} \\ \sigma_i^0 &= \sigma_i^1 \quad \text{and} \\ \delta_i^0(\epsilon) &= \delta_i^1(\epsilon) \end{aligned}$$

The relationship between the sequences and histories is that there exists a function from the stutter equivalence classes of histories to the stutter equivalence classes of sequences and a function from the stutter equivalence classes of sequences to the stutter equivalence classes of histories.

Theorem 1 (Relationship between histories and infinite sequences)

Let $h \in \mathcal{H}/ \simeq_h$ then $(sel_i)_{i \geq 0} \in SEQ/ \simeq_s$ where sel_i is as follows:

if $nn(h) < \infty$:

$$\begin{aligned} sel_{2^*i+1} &= h(i) & 0 \leq i < nn(h) \\ sel_{2^*i} &= \lim_{i \leftarrow t_1} h(t_1) & 0 \leq i \leq nn(h) \\ sel_{2^*i+1} &= \lim_{k \leftarrow t_1} h(t_1) & k = nn(h) \wedge k \leq i \\ sel_{2^*i} &= \lim_{k \leftarrow t_1} h(t_1) & k = nn(h) \wedge k \leq i \end{aligned}$$

if $nn(h) = \infty$:

$$\begin{aligned} sel_{2^*i+1} &= h(i) & 0 \leq i \\ sel_{2^*i} &= \lim_{i \leftarrow t_1} h(t_1) & 0 \leq i \end{aligned}$$

Let $seq = (sel_i)_{i \geq 0} \in SEQ/ \simeq_s$ then $h \in \mathcal{H}/ \simeq_h$ where h is as follows:

if $ns(seq) < \infty$:

$$\begin{aligned} h(0) &= sel_0 \\ h(t) &= sel_{2^*t-1} & t \in \mathbb{N} \wedge 0 < t \leq ns(seq) \\ h(t) &= sel_{2^*t} & t \in \mathbb{N} \wedge t > ns(seq) \\ h(t) &= sel_{2^*i} & i < t < i + 1 \end{aligned}$$

if $ns(seq) = \infty$:

$$\begin{aligned} h(0) &= sel_0 \\ h(t) &= sel_{2^*t-1} & t \in \mathbb{N} \\ h(t) &= sel_{2^*i} & i < t < i + 1 \end{aligned}$$

The following sequence corresponds to the history of figure 2.3:

$$\begin{aligned} seq &= (\delta_i, \sigma_i)_{i \geq 0}, \\ \delta_0(\epsilon) &= \lambda & \sigma_0(\mathbf{s}) &= 0 & \sigma_0(\mathbf{x}) &= 0 \\ \delta_1(\epsilon) &= \mathbf{a} ? & \sigma_1(\mathbf{s}) &= 0 & \sigma_1(\mathbf{x}) &= 0 \\ \delta_2(\epsilon) &= \lambda & \sigma_2(\mathbf{s}) &= 0 & \sigma_2(\mathbf{x}) &= 1 \\ \delta_3(\epsilon) &= \mathbf{i} & \sigma_3(\mathbf{s}) &= 0 & \sigma_3(\mathbf{x}) &= 1 \\ \delta_4(\epsilon) &= \lambda & \sigma_4(\mathbf{s}) &= 1 & \sigma_4(\mathbf{x}) &= 2 \\ \delta_5(\epsilon) &= \mathbf{e} & \sigma_5(\mathbf{s}) &= 1 & \sigma_5(\mathbf{x}) &= 2 \\ \delta_i(\epsilon) &= \lambda & \sigma_i(\mathbf{s}) &= 2 & \sigma_i(\mathbf{x}) &= 2 & i > 5 \end{aligned}$$

The basis is a pair consisting of a *process basis*, specifying the local and shared variables of the system, and a *action basis* which specifies the input and output communication channels of a system. The following definition introduces basis and history sets that constrain a specific process basis, i.e., specific sets of shared variables and local variables are constrained to change in specific ways, the variables outside this process basis can change without restriction, with exception of the rigid variables which do not change at all.

2.2 Specification of Reactive Systems

Definition 12 (Basis, history set constraining a basis)

A **basis** (denoted by B) is a pair (B^A, B^P) , where B^A (called **action basis**) is a pair (In, Out) where In is a set of input communication channels and Out is a set of output communication channels, and where B^P (called **process basis**) is a tuple (V, X) where V is a finite set of shared variables and X a finite set of local variables.

Given a history $h \in \mathcal{H}$ and process basis B^P then the **process basis restriction** of h denoted $h|_{B^P}^2$ is defined as $\langle \psi, \theta|_{V \cup X}^2 \rangle$.

Given a set of histories H and process basis B^P then H is **constrained by B^P** iff $\forall h_1, h_2 \in \mathcal{H} : h_1|_{B^P}^2 = h_2|_{B^P}^2 \rightarrow (h_1 \in H \leftrightarrow h_2 \in H)$.

The following definition introduces the notion of *history specification* which is a pair consisting of a basis and a set of histories constraining the process basis.

Definition 13 (History specification of a system)

A **history specification** of a system (denoted \mathcal{S}) is a pair (B, H) where B is a basis and H is a set of histories constraining process basis B^P such that an environment action \mathbf{e} doesn't change the local variables of the system:

$$\forall t : \psi(t)(\mathbf{e}) = \mathbf{e} \rightarrow \theta(t)|_X^1 = \lim_{t \leftarrow t_1} \theta(t_1)|_X^1$$

The following definition introduces several notions from topology ([Wri87]) needed for the definition of safety and liveness sets of histories. These definitions of safety and liveness are based on those of [AS85]. Informally a safety set of histories consists of histories where nothing "bad" happens and a liveness set of histories consists of histories where something "good" eventually happens.

Definition 14 (Safety and liveness set)

Let H be a set of histories and $h \in \mathcal{H}$.

- The **prefix** of h of length t denoted $h \upharpoonright_t$ is defined as

$$h \upharpoonright_t (t_0) \triangleq \begin{cases} \langle \psi(t_0), \theta(t_0) \rangle & 0 \leq t_0 \leq t \\ \langle \psi(0), \theta(t) \rangle & t_0 > t \end{cases}$$

Thus for $t_0 > t$ only stutter actions occur in $h \upharpoonright_t$.

- The **distance function** d from $\mathcal{H} \times \mathcal{H} \rightarrow \mathbb{R}^{\geq 0}$ is defined as:

$$d(h_1, h_2) \triangleq \begin{cases} 0 & \text{if } h_1 = h_2 \\ 1 & \text{if } h_1(0) \neq h_2(0) \\ 2^{-\sup\{t \in \mathbb{R}^{\geq 0} \mid h_1 \upharpoonright_t = h_2 \upharpoonright_t\}} & \text{otherwise} \end{cases}$$

(\mathcal{H}, d) is a metric space.

- H is called **d -open** iff

$$\forall h \in H : \exists \varepsilon > 0 : \forall h_1 : d(h, h_1) < \varepsilon \rightarrow h_1 \in H$$

- The topology with $\{H \subseteq \mathcal{H} \mid H \text{ is } d\text{-open}\}$ as its basis is called the **d induced topology** of (\mathcal{H}, d) denoted τ_d .

- H is called a **τ_d -environment** of h iff

$$\exists H_1 \in \tau_d : h \in H_1 \wedge H_1 \subseteq H$$

- The **interior** of H denoted $in(H)$ is defined as

$$\{h \in \mathcal{H} \mid H \text{ is a } \tau_d \text{ environment of } h\}$$

- The **closure** of H denoted $cl(H)$ is defined as $\mathcal{H} \setminus (in(\mathcal{H} \setminus H))$.
- H is a **safety set** iff $cl(H) = H$.
- H is a **liveness set** iff $cl(H) = \mathcal{H}$.

Note: the only set that is both a safety and a liveness set is \mathcal{H} [AS85].

A specification method for systems that uses only sets of histories is not attractive. Therefore the notion of machine is introduced. A machine consists of a set of states and a state-transition relation. The intention is that the set of computations (i.e. histories) of a machine associated to a system should correspond to the history specification of this system. A machine however can only generate safety sets of histories [AS87]. Therefore, a liveness set is specified as a condition on the set of computations (histories) of a machine. Next the formal definition of a machine is given.

Definition 15 (Machine)

The machine specification M of a system is a triple (B, I, T) where:

- B : the basis of M ; a tuple $((In, Out), (V, X))$. Note: the shared variables will be printed in bold faced style in order to distinguish them from the local variables.
- I : a non-empty subset of Σ , the set of initial states, such that
 - $\forall \sigma_0, \sigma_1 \in \Sigma : (\sigma_0|_{V \cup X}^1 = \sigma_1|_{V \cup X}^1) \rightarrow (\sigma_0 \in I \leftrightarrow \sigma_1 \in I)$, i.e., it constrains the variables from $V \cup X$ only.
- T : the state-transition relation (finite), $T \subseteq \Delta \times \Sigma^2$, such that
 - $\forall \sigma_0, \sigma_1 \in \Sigma, \delta \in \Delta : \langle \delta, \sigma_0, \sigma_1 \rangle \in T \rightarrow \sigma_0|_{\mathfrak{R}}^1 = \sigma_1|_{\mathfrak{R}}^1$, i.e., the rigid variables don't change at all.
 - $\forall \sigma_0, \sigma_1, \sigma_2, \sigma_3 \in \Sigma, \delta \in \Delta : (\sigma_0|_{V \cup X}^1 = \sigma_2|_{V \cup X}^1 \wedge \sigma_1|_{V \cup X}^1 = \sigma_3|_{V \cup X}^1) \rightarrow (\langle \delta, \sigma_0, \sigma_1 \rangle \in T \leftrightarrow \langle \delta, \sigma_2, \sigma_3 \rangle \in T)$, i.e. T constrains B^P only.
 - $\forall \sigma_0, \sigma_1 \in \Sigma, \delta \in \Delta : (\langle \delta, \sigma_0, \sigma_1 \rangle \in T \wedge (\delta(\epsilon) = \mathbf{a?} \vee \delta(\epsilon) = \mathbf{a!})) \rightarrow \sigma_0|_V^1 = \sigma_1|_V^1$, i.e., a communication action doesn't change the values of shared variables, and
 - $\forall \sigma_0, \sigma_1 \in \Sigma, \delta \in \Delta : (\langle \delta, \sigma_0, \sigma_1 \rangle \in T \wedge \delta(\epsilon) = \mathbf{e}) \rightarrow \sigma_0|_X^1 = \sigma_1|_X^1$, i.e., an environment action doesn't change the values of local variables of the system.

2.2 Specification of Reactive Systems

- $\forall \sigma_0, \sigma_1 \in \Sigma, \delta \in \Delta : \langle \delta, \sigma_0, \sigma_1 \rangle \in T \rightarrow (\delta(\epsilon) \notin \{\lambda, \mathbf{i}, \mathbf{e}\} \vee \sigma_0 \neq \sigma_1)$, i.e., no stutter transitions are specified.

The following example is an illustration of the notion of machine.

Example 1

$M = (B, I, T)$ where:

1. **Basis:** $B = ((\text{In}, \text{Out}), (\text{V}, \text{X}))$ where

$$\begin{aligned} \text{In} &\stackrel{\Delta}{=} \{\mathbf{a}\} \\ \text{Out} &\stackrel{\Delta}{=} \emptyset \\ \text{V} &\stackrel{\Delta}{=} \{\mathbf{v}\} \\ \text{X} &\stackrel{\Delta}{=} \{\mathbf{u}\} \end{aligned}$$

2. **Initial States:**

$$I : \{\sigma \in \Sigma \mid \sigma(\mathbf{u}) = 0 \text{ and } \sigma(\mathbf{v}) = 0\}$$

3. **Transitions:**

$T:$

$$\{\langle \delta, \sigma_0, \sigma_1 \rangle \in \Delta \times \Sigma^2 \mid$$

- (a) $(\delta(\epsilon) = \mathbf{a}?$ and $\sigma_0(\mathbf{u}) = 0$ and $\sigma_1(\mathbf{u}) = 1$ and $\sigma_1(\mathbf{v}) = \sigma_0(\mathbf{v}))$ or
- (b) $(\delta(\epsilon) = \mathbf{i}$ and $\sigma_0(\mathbf{u}) = 1$ and $\sigma_0(\mathbf{v}) = 1$ and $\sigma_1(\mathbf{u}) = 2$ and $\sigma_1(\mathbf{v}) = 0)$ or
- (c) $(\delta(\epsilon) = \mathbf{e}$ and $\sigma_1(\mathbf{u}) = \sigma_0(\mathbf{u})$ and $\sigma_1(\mathbf{v}) = \sigma_0(\mathbf{v}) + 1)$

The concepts of event and state functions are related by the notion of *computation* of a machine M . A computation of M intuitively expresses that an event function and a state function fit together in that at any point t any triple consisting of (1) the event occurring at t , (2) the state just before and including t , and (3) the state just after t , belongs to the state transition relation of M (see fig. 2.1). Because a state-transition relations don't contain stutter steps but histories do, a set of stutter transitions should be defined in order to relate machine computations to histories.

Definition 16 (Computation)

Let $h = \langle \psi, \theta \rangle \in \mathcal{H}$ and $t \in \mathbb{R}^{\geq 0}$, then define the **step** occurring at t in h by:

$$\text{Step}_h(t) = \langle \psi(t), \theta(t), \lim_{t_1 \leftarrow t} \theta(t_1) \rangle.$$

Define the set of **stutter steps** denoted STU as $\{\langle \delta, \sigma_0, \sigma_1 \rangle \mid \delta(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\} \wedge \sigma_0 = \sigma_1\}$.

A **computation of a machine** $M = (B, I, T)$ is a history $h = \langle \psi, \theta \rangle \in \mathcal{H}$ such that:

$$\begin{aligned} \theta(0) &\in I \text{ and} \\ \forall t : \text{Step}_h(t) &\in T \vee \text{Step}_h(t) \in \text{STU}. \end{aligned}$$

Let the set of all computations of M be defined as:

$$\text{Comp}(M) \stackrel{\Delta}{=} \{h \in \mathcal{H} \mid h \text{ is a computation of } M\}.$$

Lemma 1 (Machine is safety)

Given machine $M = (B, I, T)$ then

$Comp(M)$ is a safety set.

A proof of this lemma is given in [AL91] (it is also repeated in the appendix). The *machine specification* of a system now consists of a machine M and a set of histories L constraining the basis of this machine such that the closure of the intersection of $Comp(M)$ and L equals $Comp(M)$. This is the *machine closedness* property of a system specification introduced in [AFK88, AL91]. Let $A \rightarrow B$ denote $\bar{A} \cup B$. By a result of [AS85] every set of histories can be written as the intersection of a safety set and a liveness set namely $cl(Comp(M) \cap L) \cap cl(Comp(M) \cap L) \rightarrow (Comp(M) \cap L)$. By the machine closedness property this can be written as $Comp(M) \cap Comp(M) \rightarrow L$. This means that $Comp(M)$ specifies the safety properties and $Comp(M) \rightarrow L$ the liveness properties of the system.

Definition 17 (Machine specification of a system)

A **machine specification** \mathcal{S} of a system is a pair $(B, Comp(M) \cap L)$ where M is a machine with basis B and L a set of histories constraining only B^P such that $cl(Comp(M) \cap L) = Comp(M)$. The set of computations of \mathcal{S} , denoted $Comp(\mathcal{S})$, is defined as $Comp(M) \cap L$.

2.2.2 DTL Specification of Reactive Systems

As mentioned above, the local properties are described by a machine and the liveness properties are described as a set of histories. The dense time temporal logic DTL is introduced to describe both kind of properties. The one used here is a mixture of dense time temporal logics defined in [Sta84, Sta85, BKP86, DK90, KMP93].

Definition 18 (Syntax of DTL)

The syntax of DTL is defined in Table 2.1 where value $\mu \in Val$, rigid variable $n \in \mathfrak{R}$, observable variable $\mathbf{v} \in \mathfrak{B}$, local variable $x \in \mathfrak{X}$, event variable $\epsilon \in \mathfrak{E}$ and channel $\mathbf{a} \in \text{Chan}$.

Table 2.1: Syntax of DTL

<i>Rigid Expressions</i> $rexp ::= \mu \mid n \mid n' \mid \dot{n} \mid rexp_1 + rexp_2 \mid \dots$
<i>Expressions</i> $exp ::= rexp \mid \mathbf{v} \mid \mathbf{v}' \mid \dot{\mathbf{v}} \mid x \mid x' \mid \dot{x} \mid exp_1 + exp_2 \mid \dots$
<i>Event Expressions</i> $evexp ::= \mathbf{a}? \mid \mathbf{a}! \mid \mathbf{i} \mid \mathbf{e} \mid \lambda \mid \epsilon \mid \epsilon' \mid \dot{\epsilon}$
<i>Temporal formulae</i> $p ::= \mathbf{true} \mid exp_1 = exp_2 \mid exp_1 < exp_2 \mid evexp_1 = evexp_2 \mid \neg p \mid p_1 \vee p_2$ $p_1 \hat{U} p_2 \mid p_1 \hat{S} p_2 \mid \exists x.p \mid \exists \epsilon.p \mid \exists n.p$

The informal semantics of the most interesting constructs are as follows:

- \dot{x} denotes the *previous value* of x ,

2.2 Specification of Reactive Systems

- x denotes the *current value* of x ,
- x' denotes the *next value* of x ,
- ϵ denotes the *current action* value of ϵ ,
- ϵ' denotes the *next action* value of ϵ ,
- $\hat{\epsilon}$ denotes the *previous action* value of ϵ ,
- ϵ' denotes the *next action* value of ϵ ,
- $p_1 \hat{U} p_2$ denotes strict (present not included in the future) until operator from temporal logic,
- $p_1 \hat{S} p_2$ denotes strict (present not included in the past) since operator from temporal logic,
- $\exists x.p$ denotes *existential quantification over local variable* x of p , i.e., hiding,
- $\exists \epsilon.p$ denotes *existential quantification over event variable* ϵ of p , i.e., hiding.

A *state expression* is an expression without any primed variables. A *state formula* is a formula build from state expressions without \hat{U} and \hat{S} operators.

Table 2.2 lists some frequently used abbreviations: The following example 2 gives some DTL formulae

Example 2 (Some DTL formulae)

$(\epsilon = \mathbf{a}_0 \wedge x = 0 \wedge x' = 1)$ (a *state-transition*),
 $\square x > 0$ (a *safety property*),
and $\square(x = 0 \rightarrow \diamond x > 0)$ (a *liveness property*).

Before we give the semantics of DTL formulae we define for a variable x (local process or event) the x -variant of a history.

Definition 19 (x -variant, ϵ -variant and n -variant of a history)

Let $h, h_1 \in \mathcal{H}$.

Let $x \in \mathfrak{X}$ then h_1 is a x -variant of h if $\psi_1 = \psi$ and $\theta_1|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus \{x\}} = \theta|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus \{x\}}$.

Let $X \subseteq \mathfrak{X}$ then h_1 is a X -variant of h if $\psi_1 = \psi$ and $\theta_1|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus X} = \theta|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus X}$.

Let $\epsilon \in \mathfrak{E}$ then h_1 is a ϵ -variant of h if $\psi_1|_{\mathfrak{E} \setminus \{\epsilon\}} = \psi|_{\mathfrak{E} \setminus \{\epsilon\}}$ and $\theta_1 = \theta$.

Let $n \in \mathfrak{N}$ then h_1 is a n -variant of h if $\psi_1 = \psi$ and $\theta_1|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus \{n\}} = \theta|_{(\mathfrak{U} \cup \mathfrak{X} \cup \mathfrak{N}) \setminus \{n\}}$.

In the following definition the semantics of DTL is given without using valuation functions for expressions, i.e., this valuation function is implicitly defined by \models . By convention, boolean values are not explicitly denoted, i.e., we shall write $(h, t) \models \mathbf{true}$ rather than $(h, t) \models \mathbf{true} \triangleq tt$.

Definition 20 (Semantics of DTL)

Let $h \in \mathcal{H}$, $t \in \mathbb{R}^{\geq 0}$, $n \in \mathfrak{N}$, $\mathbf{v} \in \mathfrak{V}$, $x \in \mathfrak{X}$, and $\epsilon \in \mathfrak{E}$.

- $(h, t) \models \mu \triangleq \mu$,

Table 2.2: Used abbreviations

$\mathbf{false} \triangleq \neg \mathbf{true}$ $p_1 \rightarrow p_2 \triangleq \neg p_1 \vee p_2$ $p_1 \wedge p_2 \triangleq \neg(\neg p_1 \vee \neg p_2)$ $p_1 \leftrightarrow p_2 \triangleq (p_1 \rightarrow p_2) \wedge (p_1 \leftarrow p_2)$ $\forall x.p \triangleq \neg \exists x.\neg p$ $\exists X.p \triangleq \exists x_0.\dots.\exists x_n.p$	p_1 implies p_2 p_1 and p_2 p_1 equivalent p_2 for all x p hiding over $X = \{x_0, \dots, x_n\}$
$\hat{\diamond}p \triangleq \mathbf{true} \hat{U} p$ $\hat{\square}p \triangleq \neg \hat{\diamond} \neg p$ $\bigcirc p \triangleq p \hat{U} \mathbf{true}$ $\diamond p \triangleq p \vee \hat{\diamond}p$ $\square p \triangleq p \wedge \hat{\square}p$ $p_1 \mathcal{U} p_2 \triangleq p_2 \vee (p_1 \wedge (p_1 \hat{U} p_2))$	strict eventually p , strict always p , is for some time going to be uninterruptedly p , non-strict eventually, non-strict always, non-strict until,
$\hat{\diamond}p \triangleq \mathbf{true} \hat{S} p$ $\hat{\square}p \triangleq \neg \hat{\diamond} \neg p$ $\ominus p \triangleq p \hat{S} \mathbf{true}$ $\tilde{\ominus} p \triangleq \neg \ominus \neg p$ $\mathbf{first} \triangleq \tilde{\ominus} \mathbf{false}$ $\diamond p \triangleq p \vee \hat{\diamond}p$ $\square p \triangleq p \wedge \hat{\square}p$ $p_1 \mathcal{S} p_2 \triangleq p_2 \vee (p_1 \wedge (p_1 \hat{S} p_2))$	strict once p , strict has-always-been p , has for some time been uninterruptedly p , has arbitrarily recently been p , first position in a history, non-strict once, non-strict has-always-been, non-strict since,
$p_1 \Rightarrow p_2 \triangleq \square(p_1 \rightarrow p_2)$ $p_1 \Leftrightarrow p_2 \triangleq \square(p_1 \leftrightarrow p_2)$	p_1 entails p_2 p_1 is congruent p_2

- $(h, t) \models n \triangleq \theta(0)(n)$,
- $(h, t) \models n' \triangleq \theta(0)(n)$,
- $(h, t) \models \backslash n \triangleq \theta(0)(n)$,
- $(h, t) \models x \triangleq \theta(t)(x)$,
- $(h, t) \models \mathbf{v} \triangleq \theta(t)(\mathbf{v})$,
- $(h, 0) \models \backslash x \triangleq \theta(0)(x)$
 $t > 0: (h, t) \models \backslash x \triangleq \lim_{t_1 \rightarrow t} \theta(t_1)(x)$
- $(h, 0) \models \backslash \mathbf{v} \triangleq \theta(0)(\mathbf{v})$
 $t > 0: (h, t) \models \backslash \mathbf{v} \triangleq \lim_{t_1 \rightarrow t} \theta(t_1)(\mathbf{v})$
- $(h, t) \models x' \triangleq \lim_{t \leftarrow t_1} \theta(t_1)(x)$

2.2 Specification of Reactive Systems

- $(h, t) \models \mathbf{v}' \stackrel{\Delta}{=} \lim_{t \leftarrow t_1} \theta(t_1)(\mathbf{v})$
- $(h, t) \models \text{exp}_1 + \text{exp}_2 \stackrel{\Delta}{=} (h, t) \models \text{exp}_1 + (h, t) \models \text{exp}_2,$
- $(h, t) \models \text{exp}_1 - \text{exp}_2 \stackrel{\Delta}{=} (h, t) \models \text{exp}_1 - (h, t) \models \text{exp}_2,$
- $(h, t) \models \mathbf{a}^? \stackrel{\Delta}{=} \mathbf{a}^?,$
- $(h, t) \models \mathbf{a}! \stackrel{\Delta}{=} \mathbf{a}!,$
- $(h, t) \models \mathbf{i} \stackrel{\Delta}{=} \mathbf{i},$
- $(h, t) \models \mathbf{e} \stackrel{\Delta}{=} \mathbf{e},$
- $(h, t) \models \lambda \stackrel{\Delta}{=} \lambda,$
- $(h, t) \models \epsilon \stackrel{\Delta}{=} \psi_h(t)(\epsilon),$
- $(h, 0) \models \epsilon \stackrel{\Delta}{=} \psi(0)(\epsilon)$
 $t > 0: (h, t) \models \epsilon \stackrel{\Delta}{=} \lim_{t_1 \rightarrow t} \psi(t_1)(\epsilon),$
- $(h, t) \models \epsilon' \stackrel{\Delta}{=} \lim_{t \leftarrow t_1} \psi(t_1)(\epsilon),$
- $(h, t) \models \mathbf{true},$
- $(h, t) \models \text{exp}_1 = \text{exp}_2 \text{ iff } (h, t) \models \text{exp}_1 = (h, t) \models \text{exp}_2,$
- $(h, t) \models \text{evexp}_1 = \text{evexp}_2 \text{ iff } (h, t) \models \text{evexp}_1 = (h, t) \models \text{evexp}_2,$
- $(h, t) \models \text{exp}_1 < \text{exp}_2 \text{ iff } (h, t) \models \text{exp}_1 < (h, t) \models \text{exp}_2,$
- $(h, t) \models \neg p \text{ iff } (h, t) \not\models p,$
- $(h, t) \models p_1 \vee p_2 \text{ iff } (h, t) \models p_1 \text{ or } (h, t) \models p_2,$
- $(h, t) \models p_1 \widehat{U} p_2 \text{ iff there exists a } t_0 > t, (h, t_0) \models p_2 \text{ and for all } t_1 \in (t, t_0), (h, t_1) \models p_1,$
- $(h, t) \models p_1 \widehat{S} p_2 \text{ iff there exists a } t_0 < t, (h, t_0) \models p_2 \text{ and for all } t_1 \in (t_0, t), (h, t_1) \models p_1.$
- $(h, t) \models \exists x.p \text{ iff } (h_1, t) \models p, \text{ for some } h_1, \text{ a } x\text{-variant of } h.$
- $(h, t) \models \exists \epsilon.p \text{ iff } (h_1, t) \models p, \text{ for some } h_1, \text{ a } \epsilon\text{-variant of } h.$
- $(h, t) \models \exists n.p \text{ iff } (h_1, t) \models p, \text{ for some } h_1, \text{ a } n\text{-variant of } h.$

Definition 21 (Satisfiability, validity)

For a DTL formula p and a history $h \in \mathcal{H}$, h **satisfies** p denoted $h \models p$ iff $(h, 0) \models p$.

A DTL formula p is **satisfiable** iff $h \models p$ for some history $h \in \mathcal{H}$.

A DTL formula p is **valid**, denoted $\models p$, iff $h \models p$ for all histories $h \in \mathcal{H}$.

Given a system \mathcal{S} with basis B and set of computations $\text{Comp}(\mathcal{S})$ then a DTL formula is **\mathcal{S} -valid**, denoted $\mathcal{S} \models p$ iff $h \models p$ for all histories $h \in \text{Comp}(\mathcal{S})$.

Given a temporal formula p then the set of histories satisfying p denoted $\text{Hist}(p)$ is defined as $\{h \mid h \models p\}$.

The following theorem states that the logic DTL is history process state stutter insensitive. Later on a restricted version of DTL is considered in order to make it history stutter insensitive.

Theorem 2 (DTL is history process state stutter insensitive)

Let rexp be a rigid expression, exp be an expression, evexp an event expression and p a temporal formula then

- a $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{rexp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp})$
- b $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{exp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{exp})$
- c $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{evexp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{evexp})$
- d $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models p \text{ iff } (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p)$

The following definitions introduce substitution.

Definition 22 (Non-rigid process variable substitution in expressions)

Define substitution of $w \in \mathfrak{X} \cup \mathfrak{X}$ by state expression exp in expression exp_0 denoted $\text{exp}_0[\text{exp}/w]$ as follows, using \equiv for syntactic equality:

- $\text{rexp}[\text{exp}/w] \equiv \text{rexp}$

- $\mathbf{v}[\text{exp}/w] \equiv \begin{cases} \text{exp} & \text{if } \mathbf{v} \equiv w \\ \mathbf{v} & \text{if } \mathbf{v} \not\equiv w \end{cases}$

- $\mathbf{v}'[\text{exp}/w] \equiv \begin{cases} \text{exp}' & \text{if } \mathbf{v} \equiv w \\ \mathbf{v}' & \text{if } \mathbf{v} \not\equiv w \end{cases}$

where exp' denotes the operation of “priming after” all occurrences of variables in exp (note: exp is a state expression so all variables in exp are unprimed).

- $\backslash \mathbf{v}[\text{exp}/w] \equiv \begin{cases} \backslash \text{exp} & \text{if } \mathbf{v} \equiv w \\ \backslash \mathbf{v} & \text{if } \mathbf{v} \not\equiv w \end{cases}$

where $\backslash \text{exp}$ denotes the operation of “priming before” all occurrences of variables in exp .

- $\mathbf{x}[\text{exp}/w] \equiv \begin{cases} \text{exp} & \text{if } \mathbf{x} \equiv w \\ \mathbf{x} & \text{if } \mathbf{x} \not\equiv w \end{cases}$

- $\mathbf{x}'[\text{exp}/w] \equiv \begin{cases} \text{exp}' & \text{if } \mathbf{x} \equiv w \\ \mathbf{x}' & \text{if } \mathbf{x} \not\equiv w \end{cases}$

2.2 Specification of Reactive Systems

- $\backslash x [exp/w] \equiv \begin{cases} \backslash exp & \text{if } x \equiv w \\ \backslash x & \text{if } x \not\equiv w \end{cases}$
- $(exp_1 + exp_2) [exp/w] \equiv exp_1 [exp/w] + exp_2 [exp/w]$
- ...

Definition 23 (Rigid variable substitution in expressions)

Define substitution of $n \in \mathfrak{R}$ by state rigid expression $rexp$ in expression exp_0 denoted $exp_0 [rexp/n]$ as follows, using \equiv for syntactic equality:

- $\mu [rexp/n] \equiv \mu,$
- $n_0 [rexp/n] \equiv \begin{cases} rexp & \text{if } n \equiv n_0 \\ n_0 & \text{if } n \not\equiv n_0 \end{cases}$
- $n'_0 [rexp/n] \equiv \begin{cases} rexp' & \text{if } n \equiv n_0 \\ n'_0 & \text{if } n \not\equiv n_0 \end{cases}.$
- $\backslash n_0 [rexp/n] \equiv \begin{cases} \backslash rexp & \text{if } n \equiv n_0 \\ \backslash n_0 & \text{if } n \not\equiv n_0 \end{cases}.$
- $w [rexp/n] \equiv w, w \in \mathfrak{V} \cup \mathfrak{X},$
- $w' [rexp/n] \equiv w', w \in \mathfrak{V} \cup \mathfrak{X}$
- $\backslash w [rexp/n] \equiv \backslash w, w \in \mathfrak{V} \cup \mathfrak{X}$
- $(exp_1 + exp_2) [rexp/n] \equiv exp_1 [rexp/n] + exp_2 [rexp/n],$
- ...

Definition 24 (Event variable substitution in event expressions)

Define substitution of $\epsilon \in \mathfrak{E}$ by state event expression $evexp$ in $evexp_0$ denoted $evexp_0 [evexp/\epsilon]$ as follows, using \equiv for syntactic equality:

- $\lambda [evexp/\epsilon] \equiv \lambda,$
- $\mathbf{a}^? [evexp/\epsilon] \equiv \mathbf{a}^?,$
- $\mathbf{a}! [evexp/\epsilon] \equiv \mathbf{a}!,$
- $\mathbf{i} [evexp/\epsilon] \equiv \mathbf{i},$
- $\mathbf{e} [evexp/\epsilon] \equiv \mathbf{e},$
- $\epsilon_0 [evexp/\epsilon] \equiv \begin{cases} evexp & \text{if } \epsilon_0 \equiv \epsilon \\ \epsilon_0 & \text{if } \epsilon_0 \not\equiv \epsilon \end{cases}$
- $\epsilon'_0 [evexp/\epsilon] \equiv \begin{cases} evexp' & \text{if } \epsilon_0 \equiv \epsilon \\ \epsilon'_0 & \text{if } \epsilon_0 \not\equiv \epsilon \end{cases}$

$$\bullet \text{`}\epsilon_0[evexp/\epsilon] \equiv \begin{cases} \text{`}evexp & \text{if } \epsilon_0 \equiv \epsilon \\ \text{`}\epsilon_0 & \text{if } \epsilon_0 \not\equiv \epsilon \end{cases}$$

Definition 25 (Process and event variable substitution in temporal formulae)
Define substitution for a non-rigid process variable $w \in \mathfrak{W} \cup \mathfrak{X}$ by state expression exp in a temporal formula p , denoted $p[exp/w]$, as follows:

- $\mathbf{true}[exp/w] \equiv \mathbf{true}$
- $(exp_1 = exp_2)[exp/w] \equiv exp_1[exp/w] = exp_2[exp/w]$
- $(evexp_1 = evexp_2)[exp/w] \equiv (evexp_1 = evexp_2)$
- $(exp_1 < exp_2)[exp/w] \equiv exp_1[exp/w] < exp_2[exp/w]$
- $(\neg p)[exp/w] \equiv \neg(p[exp/w])$
- $(p_1 \vee p_2)[exp/w] \equiv p_1[exp/w] \vee p_2[exp/w]$
- $(p_1 \hat{\mathcal{U}} p_2)[exp/w] \equiv (p_1[exp/w]) \hat{\mathcal{U}} (p_2[exp/w])$
- $(p_1 \hat{\mathcal{S}} p_2)[exp/w] \equiv (p_1[exp/w]) \hat{\mathcal{S}} (p_2[exp/w])$
- $(\exists x.p)[exp/w] \equiv \exists x.(p[exp/w])$ if $x \notin \text{var}(exp) \cup \{w\}$.
- $(\exists \epsilon.p)[exp/w] \equiv \exists \epsilon.(p[exp/w])$,
- $(\exists n.p)[exp/w] \equiv \exists n.(p[exp/w])$

Define substitution of rigid process variable $n \in \mathfrak{X}$ by state rigid expression $rexp$ in temporal formula p denoted $p[rexp/n]$ as follows:

- $\mathbf{true}[rexp/n] \equiv \mathbf{true}$
- $(exp_1 = exp_2)[rexp/n] \equiv exp_1[rexp/n] = exp_2[rexp/n]$
- $(evexp_1 = evexp_2)[rexp/n] \equiv (evexp_1 = evexp_2)$
- $(exp_1 < exp_2)[rexp/n] \equiv exp_1[rexp/n] < exp_2[rexp/n]$
- $(\neg p[rexp/n]) \equiv \neg(p[rexp/n])$
- $(p_1 \vee p_2)[rexp/n] \equiv p_1[rexp/n] \vee p_2[rexp/n]$
- $(p_1 \hat{\mathcal{U}} p_2)[rexp/n] \equiv (p_1[rexp/n]) \hat{\mathcal{U}} (p_2[rexp/n])$
- $(p_1 \hat{\mathcal{S}} p_2)[rexp/n] \equiv (p_1[rexp/n]) \hat{\mathcal{S}} (p_2[rexp/n])$
- $(\exists x.p)[rexp/n] \equiv \exists x.(p[rexp/n])$,
- $(\exists \epsilon.p)[rexp/n] \equiv \exists \epsilon.(p[rexp/n])$,
- $(\exists n_0.p)[rexp/n] \equiv \exists n_0.(p[rexp/n])$, if $n_0 \notin \text{var}(rexp) \cup \{n\}$.

2.2 Specification of Reactive Systems

Define substitution of event variable $\epsilon \in \mathfrak{E}$ by state event expression $evexp$ in temporal formula p denoted $p[evexp/\epsilon]$ as follows:

- $\mathbf{true}[evexp/\epsilon] \equiv \mathbf{true}$
- $(exp_1 = exp_2)[evexp/\epsilon] \equiv (exp_1 = exp_2)$
- $(evexp_1 = evexp_2)[evexp/\epsilon] \equiv (evexp_1[evexp/\epsilon] = evexp_2[evexp/\epsilon])$
- $(exp_1 < exp_2)[evexp/\epsilon] \equiv (exp_1 < exp_2)$
- $(\neg p)[evexp/\epsilon] \equiv \neg(p[evexp/\epsilon])$
- $(p_1 \vee p_2)[evexp/\epsilon] \equiv (p_1[evexp/\epsilon]) \vee (p_2[evexp/\epsilon])$
- $(p_1 \hat{\mathcal{U}} p_2)[evexp/\epsilon] \equiv (p_1[evexp/\epsilon]) \hat{\mathcal{U}} (p_2[evexp/\epsilon])$
- $(p_1 \hat{\mathcal{S}} p_2)[evexp/\epsilon] \equiv (p_1[evexp/\epsilon]) \hat{\mathcal{S}} (p_2[evexp/\epsilon])$
- $(\exists x.p)[evexp/\epsilon] \equiv \exists x.(p[evexp/\epsilon])$
- $(\exists \epsilon_0.p)[evexp/\epsilon] \equiv \exists \epsilon_0.(p[evexp/\epsilon])$ where $\epsilon_0 \notin \mathit{evar}(evexp) \cup \{\epsilon\}$.

The following introduces the *history variant* of a history.

Definition 26 (History variant)

The **history variant** of a history with respect to non-rigid process variable $w \in \mathfrak{W} \cup \mathfrak{X}$, and a state expression exp , denoted by $(h : w \rightsquigarrow exp)$, is defined for $w_1 \in \mathfrak{W} \cup \mathfrak{X}$ as follows: Let $\mu \in Val$ and $\sigma \in \Sigma$ then

$$(\sigma : w \mapsto \mu)(w_1) \triangleq \begin{cases} \mu & \text{if } w_1 \equiv w \\ \sigma(w_1) & \text{if } w_1 \not\equiv w \end{cases}$$

then

$$(h : w \rightsquigarrow exp)(t_0) \triangleq \langle \psi_h(t_0), (\theta_h(t_0) : w \mapsto (h, t_0) \models exp) \rangle$$

The **history variant** of a history with respect to rigid process variable $n \in \mathfrak{R}$, and a state rigid expression $rexp$, denoted by $(h : n \rightsquigarrow rexp)$, is defined for $n_1 \in \mathfrak{R}$ as follows: Let $\mu \in Val$ and $\sigma \in \Sigma$ then

$$(\sigma : n \mapsto \mu)(n_1) \triangleq \begin{cases} \mu & \text{if } n_1 \equiv n \\ \sigma(n_1) & \text{if } n_1 \not\equiv n \end{cases}$$

then

$$(h : n \rightsquigarrow rexp)(t_0) \triangleq \langle \psi_h(t_0), (\theta_h(t_0) : n \mapsto (h, t_0) \models rexp) \rangle$$

The **history variant** of a history with respect to event variable $\epsilon \in \mathfrak{E}$, and a state event expression $evexp$, denoted by $(h : \epsilon \rightsquigarrow evexp)$, is defined for $\epsilon_1 \in \mathfrak{E}$ as follows: Let $\mathbf{a} \in \mathfrak{A}$ and $\delta \in \Delta$ then

$$(\delta : \epsilon \mapsto \mathbf{a})(\epsilon_1) \triangleq \begin{cases} \mathbf{a} & \text{if } \epsilon_1 \equiv \epsilon \\ \delta(\epsilon_1) & \text{if } \epsilon_1 \not\equiv \epsilon \end{cases}$$

then

$$(h : \epsilon \rightsquigarrow \text{evexp})(t_0) \triangleq \langle (\psi_h(t_0) : \epsilon \mapsto (h, t_0) \models \text{evexp}), \theta_h(t_0) \rangle$$

The following substitution lemma holds.

Lemma 2 (Substitution lemma)

Let exp_0 be an expression, exp be a state expression, $w \in \mathfrak{W} \cup \mathfrak{X}$, rexp be a state rigid expression, $n \in \mathfrak{N}$, evexp_0 an event expression, evexp a state event expression, $\epsilon \in \mathfrak{E}$, and p a temporal formula. Then the following holds:

- a $(h, t) \models \text{exp}_0[\text{exp}/w] = ((h : w \rightsquigarrow \text{exp}), t) \models \text{exp}_0$
- b $(h, t) \models \text{exp}_0[\text{rexp}/n] = ((h : n \rightsquigarrow \text{rexp}), t) \models \text{exp}_0$
- c $(h, t) \models \text{evexp}_0[\text{evexp}/\epsilon] = ((h : \epsilon \rightsquigarrow \text{evexp}), t) \models \text{evexp}_0$
- d $(h, t) \models p[\text{exp}/w]$ iff $((h : w \rightsquigarrow \text{exp}), t) \models p$
- e $(h, t) \models p[\text{rexp}/n]$ iff $((h : n \rightsquigarrow \text{rexp}), t) \models p$
- f $(h, t) \models p[\text{evexp}/\epsilon]$ iff $((h : \epsilon \rightsquigarrow \text{evexp}), t) \models p$

The following proof system for DTL is inspired on [Bur82, Bur84, BKP86, MP89]. An erroneous variant of it appeared in [BKP86] where these authors state that it is “an almost verbatim copy of [Bur84]” indeed “almost” their axiom F5 was not copied well. Furthermore a link with the proof system of [KMP93] is established via axioms AX7b–AX7f, i.e., these axioms are needed for deriving their proof system. Note: because the models of [Bur82, Bur84] need not to satisfy the finite variability condition, and the persistency condition (once in an interval “going back or forward” doesn’t bring you outside that interval, and the induction axiom. This is the crucial difference between the model of [KMP93] and ours and the one in [Bur82, Bur84]. The difference between the model of [KMP93] and our model is that we have additional compositionality information as reflected in axioms AX0, AX5 and AX6.

The proof system is for the pure logic, i.e., it is not meant for a specific reactive system. Axioms AX0–AX9 characterize our notion of histories; they should follow from the definition of history (Def. 9), and, because a history is a pair consisting of a event and a state function, also from Definition 7 and 8. Ax10 and Ax11 are the axioms for substitution and quantification. Axioms F1–F7 are the axioms of the future part of DTL and P1–P7 the past part. As rules we take standard ones, i.e., the modus ponus, generalization, specialization, instantiation and universal generalization.

Definition 27 (Proof system for DTL)

Let $n \in \mathfrak{N}$, $\mathbf{v} \in \mathfrak{V}$, $w \in \mathfrak{W} \cup \mathfrak{X}$, $\mathbf{x} \in \mathfrak{X}$ and $\epsilon \in \mathfrak{E}$.

Axioms All the axioms for state formulae.

$$\text{AX0} : (\epsilon = \mathbf{a} ? \vee \epsilon = \mathbf{a} ! \vee \epsilon = \mathbf{i} \vee \epsilon = \mathbf{e}) \Rightarrow (\epsilon' = \lambda \wedge \epsilon = \lambda)$$

Non- λ actions are points surrounded by λ actions conform Definition 7.

$$\text{AX1} : \mathbf{first} \rightarrow \epsilon = \lambda \wedge \mathbf{v}' = \mathbf{v} \wedge \mathbf{x}' = \mathbf{x}$$

2.2 Specification of Reactive Systems

The initially stuttering requirement conform Definition 7 and 8.

$$AX2 : \Box(\dot{x} = x \wedge \dot{v} = v)$$

The process variables are left continuous variables conform Definition 8.

$$AX3 : \bigcirc(x' = x \wedge v' = v) \wedge (((x' \neq x \vee v' \neq v) \Rightarrow \bigcirc(x'' = x' \wedge v'' = v')))$$

The value of process variables are maintained during an interval, conform Definition 8.

$$AX4 : \Box(n = n' \wedge n = \dot{n})$$

The rigid variables don't change at all conform Definition 8.

$$AX5 : (\epsilon = a? \vee \epsilon = a!) \Rightarrow v' = v$$

Communication actions don't change the shared variables conform Definition 9.

$$AX6 : \epsilon = \lambda \Rightarrow (v' = v \wedge x' = x)$$

A λ action causes no state change.

$$AX7a : \hat{\Diamond}p \Rightarrow \hat{\Diamond}\hat{\Diamond}p$$

$$AX7b : \neg\bigcirc p \Rightarrow \bigcirc\neg p$$

$$AX7c : \neg\Theta p \Rightarrow \Theta\neg p$$

$$AX7d : \bigcirc\Theta p \Rightarrow \bigcirc p$$

$$AX7e : \Theta\bigcirc p \Rightarrow \Theta p$$

$$AX7f : (p \wedge p \Rightarrow \bigcirc p \wedge \Theta p \Rightarrow p) \rightarrow \Box p$$

The underlying structure is dense (a), and satisfies the finite variability condition (b & c), and is persistent (d & e). Axiom (f) is the induction axiom. For an explanation of d-f see [KMP93].

$$AX8 : \Box\hat{\Diamond}\mathbf{true}$$

There is no last element, i.e., the future is unbounded.

$$AX9 : \Box\hat{\Diamond}\hat{\Diamond}\Box\mathbf{false}$$

There exists a first element.

$$AX10 : \begin{aligned} & (exp_1 = exp_2) \Rightarrow (p[exp_1/w] \leftrightarrow p[exp_2/w]) \\ & \wedge (rexp_1 = rexp_2) \Rightarrow (p[rexp_1/n] \leftrightarrow p[rexp_2/n]) \\ & \wedge (evexp_1 = evexp_2) \Rightarrow (p[evexp_1/\epsilon] \leftrightarrow p[evexp_2/\epsilon]) \end{aligned}$$

where p is a state formula and none of the variables appearing in respectively exp_1 , exp_2 , $rexp_1$, $rexp_2$, $evexp_1$ and $evexp_2$ is quantified in p .

Replacement of equal expressions.

$$\begin{aligned} AX11 : \quad & (\forall x.p) \Rightarrow p[exp/x] \\ & \wedge (\forall n.p) \Rightarrow p[rexp/n] \\ & \wedge (\forall \epsilon.p) \Rightarrow p[evexp/\epsilon] \end{aligned}$$

where none of the variables appearing in exp , $rexp$ and $evexp$ is quantified in p .
Quantifier instantiation.

$$F1 : \widehat{\Box}(p \rightarrow q) \Rightarrow (r \widehat{U} p \rightarrow r \widehat{U} q)$$

\widehat{U} is monotonic in its second argument.

$$F2 : \widehat{\Box}(p \rightarrow q) \Rightarrow (p \widehat{U} r \rightarrow q \widehat{U} r)$$

\widehat{U} is monotonic in its first argument.

$$F3 : (p \wedge r \widehat{U} q) \Rightarrow (r \widehat{U} (q \wedge r \widehat{S} p))$$

The relation of reflection holding between past and future.

$$F4 : (q \widehat{U} p \wedge \neg(r \widehat{U} p)) \Rightarrow q \widehat{U} (q \wedge \neg r)$$

$$F5 : q \widehat{U} p \Rightarrow (q \wedge q \widehat{U} p) \widehat{U} p$$

$$F6 : q \widehat{U} (q \wedge q \widehat{U} p) \Rightarrow q \widehat{U} p$$

$$F7 : (q \widehat{U} p \wedge s \widehat{U} r) \Rightarrow (q \wedge s) \widehat{U} (p \wedge r) \vee (q \wedge s) \widehat{U} (p \wedge s) \vee (q \wedge s) \widehat{U} (q \wedge r)$$

The underlying structure is linear.

$$P1 : \widehat{\Box}(p \rightarrow q) \Rightarrow (r \widehat{S} p \rightarrow r \widehat{S} q)$$

\widehat{S} is monotonic in its second argument.

$$P2 : \widehat{\Box}(p \rightarrow q) \Rightarrow (p \widehat{S} r \rightarrow q \widehat{S} r)$$

\widehat{S} is monotonic in its first argument.

$$P3 : (p \wedge r \widehat{S} q) \Rightarrow (r \widehat{S} (q \wedge r \widehat{U} p))$$

2.2 Specification of Reactive Systems

The relation of reflection holding between past and future.

$$P4 : (q \widehat{S} p \wedge \neg(r \widehat{S} p)) \Rightarrow q \widehat{S} (q \wedge \neg r)$$

$$P5 : q \widehat{S} p \Rightarrow (q \wedge q \widehat{S} p) \widehat{S} p$$

$$P6 : q \widehat{S} (q \wedge q \widehat{S} p) \Rightarrow q \widehat{S} p$$

$$P7 : (q \widehat{S} p \wedge s \widehat{S} r) \Rightarrow (q \wedge s) \widehat{S} (p \wedge r) \vee (q \wedge s) \widehat{S} (p \wedge s) \vee (q \wedge s) \widehat{S} (q \wedge r)$$

The underlying structure is linear.

Rules

$$\frac{p, p \rightarrow q}{q}$$

The Modus Ponus.

$$\frac{p}{\Box p} \text{ for state formula } p \text{ in which all occurrences of}$$

$\Box p$ parameterized sentence symbols in p are rigid

Generalization.

$$\frac{\Box p}{p} \text{ for state formula } p$$

Specialization.

$$\frac{p}{p[p_1/p_0]} \text{ where } p_1 \text{ doesn't contain variables which are bound in } p$$

Instantiation.

$$\frac{p_0 \Rightarrow p_1}{p_0 \Rightarrow \forall x.p_1} \text{ for } x \text{ not free in } p_0$$

$$\frac{p_0 \Rightarrow p_1}{p_0 \Rightarrow \forall n.p_1} \text{ for } n \text{ not free in } p_0$$

$$\frac{p_0 \Rightarrow p_1}{p_0 \Rightarrow \forall \epsilon.p_1} \text{ for } \epsilon \text{ not free in } p_0$$

Universal Generalization.

The following definition characterizes a machine M in DTL. This kind of DTL formulae is history stutter insensitive.

Definition 28 (Machine in DTL)

Given basis $B = ((\text{In}, \text{Out}), (V, X))$. Let $\text{In}?$ be defined as $\{\mathbf{a}^? \mid a \in \text{In}\}$ and let $\text{Out}!$ be defined as $\{\mathbf{a}! \mid a \in \text{Out}\}$. Let I be a DTL formula over $V \cup X$ without the \widehat{S} , \widehat{U} and \exists operators. Let \mathcal{T} be a finite set of DTL formulae τ of the form $(\text{event}_\tau \wedge \text{trans}_\tau)$ where event_τ is of the form $\epsilon = a_\tau$ where $a_\tau \in \{\mathbf{i}, \mathbf{e}\} \cup \text{In}^? \cup \text{Out}!$, and trans_τ a DTL formula over $V \cup X$ and $V' \cup X'$ (variables primed with ') without the \widehat{S} , \widehat{U} and \exists operators such that $(\epsilon = \mathbf{e} \Rightarrow \bigwedge_{x \in X} x' = x)$, i.e., an environment action doesn't change the local variables of the system. Define the stutter step, denoted by **stut**, as $\epsilon = \lambda \vee (\epsilon = \mathbf{i} \wedge (V, X)' = (V, X)) \vee (\epsilon = \mathbf{e} \wedge (V, X)' = (V, X))$. Let T be the DTL formula $\mathbf{stut} \vee \bigvee_{\tau \in \mathcal{T}} \tau$. A machine in DTL is defined as $(B, I \wedge \square T)$.

Lemma 3

Given a machine in DTL $(B, I \wedge \square T)$ then there exists a semantic machine $M = (B, I, T)$ such that $\text{Comp}(M) = \text{Hist}(I \wedge \square T)$.

The following example is an illustration of a machine in DTL.

Example 3

Machine M in example 1 as DTL-formula:

1. **Basis:** $B = ((\text{In}, \text{Out}), (V, X))$ where

$$\begin{aligned} \text{In} &\stackrel{\Delta}{=} \{a\}, \\ \text{Out} &\stackrel{\Delta}{=} \emptyset, \\ V &\stackrel{\Delta}{=} \{\mathbf{v}\}, \\ X &\stackrel{\Delta}{=} \{\mathbf{u}\} \end{aligned}$$

2. **Initial States:**

$$I \stackrel{\Delta}{=} (\mathbf{v}, \mathbf{u}) = (0, 0)$$

3. **Transitions:**

$$\begin{aligned} T &\stackrel{\Delta}{=} \\ &\left(\epsilon = \mathbf{a}^? \wedge \mathbf{u} = 0 \wedge (\mathbf{v}, \mathbf{u})' = (\mathbf{v}, 1) \right) \vee \\ &\left(\epsilon = \mathbf{i} \wedge (\mathbf{v}, \mathbf{u}) = (1, 1) \wedge (\mathbf{v}, \mathbf{u})' = (0, 2) \right) \vee \\ &\left(\epsilon = \mathbf{e} \wedge (\mathbf{v}, \mathbf{u})' = (\mathbf{v} + 1, \mathbf{u}) \right) \vee \\ &\mathbf{stut} \end{aligned}$$

The machine specification of a system in DTL is as follows.

Definition 29 (Machine specification of a system in DTL)

Given a machine $(B, I \wedge \square T)$ in DTL. Let $\text{WF} \subseteq \mathcal{T}$ be the set of weak fair transitions and $\text{SF} \subseteq \mathcal{T}$ be the set of strong fair transitions. For $\tau \in \mathcal{T}$ define the enabledness condition for τ denoted $\text{En}(\tau)$ as $\exists \bar{v}_0. \tau [\bar{v}_0 / \bar{v}']$ where $\tau [\bar{v}_0 / \bar{v}']$ denotes the substitution of \bar{v}_0 (a list of variables not in $V \cup X$) for \bar{v}' (the list of primed variables in τ). Let L be the DTL formula $\bigwedge_{\tau \in \text{WF}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$. The machine specification of a system in DTL is then a tuple $(B, I \wedge \square T \wedge L)$.

2.3 Refinement and Composition of Reactive System Specifications

Note: in above definition L is such that $cl(Hist(I \wedge \Box T) \cap Hist(L)) = Hist(I \wedge \Box T)$, i.e., it satisfies the machine closedness property. With this the following lemma is straight forward.

Lemma 4

Given DTL machine specification $(B, I \wedge \Box T \wedge L)$ of a system, there exists a semantic machine specification $\mathcal{S} = (B, Comp(M) \cap L)$ such that $Comp(M) \cap L = Hist(I \wedge \Box T \wedge L)$.

2.3 Refinement and Composition of Reactive System Specifications

In this section the notion of *refinement* and *composition* of reactive systems is introduced. Intuitively refinement means that the set of histories of a concrete system is a subset of the set of histories of an abstract system. Composition means that the histories of the component systems are “merged” into composite histories, i.e., the histories of the composed system. Our merge operator is based on the merge operator of Aczel [Acz83]. Both are first defined at the semantic level and then for the DTL specifications.

2.3.1 Semantic Refinement and Composition of Specifications

In this section refinement and composition of reactive systems is defined at the semantical level. Refinement means that the set of histories of a concrete system is a subset of the set of histories of an abstract system. Because histories also contains local information the subset relation doesn't correspond directly with refinement. The local information should first be projected away. The following definition captures this projection of local information.

Definition 30 (Observable system specification)

Given system specification $\mathcal{S} = (B, H)$ where $B = ((In, Out), (V, X))$. The **observable system specification** is defined as $(\mathfrak{D}(B), \mathcal{O}_X(H))$ where $\mathfrak{D}(B)$ denotes the observable basis and is defined as $\mathfrak{D}(B) \triangleq ((In, Out), V, \emptyset)$ and $\mathcal{O}_X(H)$ denotes the set of observable histories corresponding to H and is defined as

$$\{h \in \mathcal{H} \mid \exists h_1 \in H : h \text{ is an } X\text{-variant of } h_1\}$$

Definition 31 (Refinement of systems)

Given concrete system $\mathcal{S}_c \triangleq (B_c, H_c)$ and abstract system $\mathcal{S}_a \triangleq (B_a, H_a)$. \mathcal{S}_c **refines** \mathcal{S}_a denoted by $\mathcal{S}_c \text{ ref } \mathcal{S}_a$ iff $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$ and $\mathcal{O}_{X_c}(H_c) \subseteq \mathcal{O}_{X_a}(H_a)$.

A more general definition of refinement would be one wherein both the abstract and concrete system are composed of subsystems. Therefore the notion of *composition* is introduced. Intuitively the composition of two systems is that *matching* histories are merged into one history. A history of one system matches a history of the other system if for all time points t

- (1) the state information of the two histories at time t are same and

- (2a) in both histories the λ -action occurs at time t or
- (2b) in both histories the environment action \mathbf{e} occurs at time t or
- (2c) in one history at time t a process action \mathbf{i} occurs and in the other one an environment action \mathbf{e} occurs at time t or
- (2d) in both histories at time t a communication action \mathbf{a} occurs which is an input action in one of them and an output action in the other one
- (2e) in one history at time t a communication action occurs which is not an communication action in the other one and in the other history an environment action \mathbf{e} occurs.

So if the two components each perform an \mathbf{i} action this prohibited because we want to model interleaving where only communication actions can possible occur simultaneously. Two matching histories are then merged into one history by (1) “copying” the state-information of the two histories; and in case (2a) the resulting event becomes λ , and in case (2b) the resulting event becomes \mathbf{e} , and in case (2c) the resulting event becomes \mathbf{i} , and in case (2d) the resulting event becomes \mathbf{i} , and in case (2e) the resulting event becomes the communication action.

Definition 32 (Composition of two systems)

Given systems $\mathcal{S}_i = (B_i, H_i)$ with $B_i = ((\text{In}_i, \text{Out}_i), (V_i, X_i))$ ($i = 1, 2$) such that $\text{In}_1 \cap \text{In}_2 = \emptyset$, $\text{Out}_1 \cap \text{Out}_2 = \emptyset$ and $X_1 \cap X_2 = \emptyset$. The **composed system** $\mathcal{S} = \mathcal{S}_1 \parallel \mathcal{S}_2$ is defined as (B, H) with $B \triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2))$ and $H \triangleq H_1 \otimes H_2$. The \otimes is the **merge operator** which merges the histories $h_1 \in H_1$ and $h_2 \in H_2$ into one history h and which is defined as follows:

$$H_1 \otimes H_2 \triangleq \{h \in \mathcal{H} \mid \exists h_1 \in H_1, h_2 \in H_2. \otimes(h, h_1, h_2)\}$$

where for $h = \langle \psi, \theta \rangle$ and $h_j = \langle \psi_j, \theta_j \rangle$ ($j = 1, 2$),
 $\otimes(h, h_1, h_2)$ iff

- $\theta = \theta_1 \wedge \theta = \theta_2$
- $\forall t :$
 - $\vee \psi(t)(\epsilon) = \lambda \wedge \psi_1(t)(\epsilon) = \lambda \wedge \psi_2(t)(\epsilon) = \lambda$
 - $\vee \psi(t)(\epsilon) = \mathbf{e} \wedge \psi_1(t)(\epsilon) = \mathbf{e} \wedge \psi_2(t)(\epsilon) = \mathbf{e}$
 - $\vee \psi(t)(\epsilon) = \mathbf{i} \wedge \psi_1(t)(\epsilon) = \mathbf{i} \wedge \psi_2(t)(\epsilon) = \mathbf{e}$
 - $\vee \psi(t)(\epsilon) = \mathbf{i} \wedge \psi_1(t)(\epsilon) = \mathbf{e} \wedge \psi_2(t)(\epsilon) = \mathbf{i}$
 - $\vee \exists \mathbf{a} \in \text{In}_1 \cap \text{Out}_2 : \psi(t)(\epsilon) = \mathbf{i} \wedge \psi_1(t)(\epsilon) = \mathbf{a} ? \wedge \psi_2(t)(\epsilon) = \mathbf{a} !$
 - $\vee \exists \mathbf{a} \in \text{In}_2 \cap \text{Out}_1 : \psi(t)(\epsilon) = \mathbf{i} \wedge \psi_1(t)(\epsilon) = \mathbf{a} ! \wedge \psi_2(t)(\epsilon) = \mathbf{a} ?$
 - $\vee \exists \mathbf{a} \in \text{In}_1 \setminus \text{Out}_2 : \psi(t)(\epsilon) = \mathbf{a} ? \wedge \psi_1(t)(\epsilon) = \mathbf{a} ? \wedge \psi_2(t)(\epsilon) = \mathbf{e}$
 - $\vee \exists \mathbf{a} \in \text{Out}_1 \setminus \text{In}_2 : \psi(t)(\epsilon) = \mathbf{a} ! \wedge \psi_1(t)(\epsilon) = \mathbf{a} ! \wedge \psi_2(t)(\epsilon) = \mathbf{e}$
 - $\vee \exists \mathbf{a} \in \text{In}_2 \setminus \text{Out}_1 : \psi(t)(\epsilon) = \mathbf{a} ? \wedge \psi_1(t)(\epsilon) = \mathbf{e} \wedge \psi_2(t)(\epsilon) = \mathbf{a} ?$
 - $\vee \exists \mathbf{a} \in \text{Out}_2 \setminus \text{In}_1 : \psi(t)(\epsilon) = \mathbf{a} ! \wedge \psi_1(t)(\epsilon) = \mathbf{e} \wedge \psi_2(t)(\epsilon) = \mathbf{a} !$

The following Lemma expresses that the “making observable”-operation and the merge operator are monotonic and that the “making observable”-operation on the composed system is equal to the “making observable”-operation on the components.

2.3 Refinement and Composition of Reactive System Specifications

Lemma 5 (Properties of \mathcal{O} and \otimes)

Given systems (B_1, H_0) , (B_1, H_1) , (B_2, H_2) and (B_2, H_3) then

- (a) $H_0 \subseteq H_1$ implies $H_0 \otimes H_2 \subseteq H_1 \otimes H_2$
- (b) $\mathcal{O}_{X_{12}}(H_1 \otimes H_2) = \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2)$
- (c) $H_0 \subseteq H_1$ implies $\mathcal{O}_{X_1}(H_0) \subseteq \mathcal{O}_{X_1}(H_1)$
- (d) $(H_0 \cap H_1) \otimes (H_2 \cap H_3) \subseteq (H_0 \otimes H_2) \cap (H_1 \otimes H_3)$

The following theorem of compositional refinement can be inferred from the above lemma.

Theorem 3 (Compositional refinement)

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) such that $\mathfrak{D}(B_1) = \mathfrak{D}(B_3)$ and $\mathfrak{D}(B_2) = \mathfrak{D}(B_4)$ then $\mathcal{S}_1 \text{ ref } \mathcal{S}_3$ and $\mathcal{S}_2 \text{ ref } \mathcal{S}_4$ implies $\mathcal{S}_1 \parallel \mathcal{S}_2 \text{ ref } \mathcal{S}_3 \parallel \mathcal{S}_4$.

It is very common that a shared variable is only used by the subcomponents of a system and not by the environment of the system. This variable acts then as a local variable for the system. The following definition introduces *encapsulation* which makes certain shared variables local to the system.

Definition 33 (Encapsulation)

Given system $\mathcal{S} = (B, H)$ where $B = ((\text{In}, \text{Out}), (V, X))$ then **encapsulation** of V_1 in \mathcal{S} with $V_1 \subseteq V$ is denoted by $\mathcal{S} \upharpoonright V_1$ and defined by $(B_1, \text{Enc}_{V_1}(H))$ where $B_1 \triangleq ((\text{In}, \text{Out}), (V \setminus V_1, X \cup \text{ren}(V_1)))$ where *ren* is a mapping from the shared variables to the local variables and intuitively “renames” the shared variables of V_1 to fresh local variables (not already in X). The encapsulation operator $\text{Enc}_{V_1}(H)$ is defined as

$$\{h \in \mathcal{H} \mid h \in H \wedge \forall t : \psi(t)(\epsilon) = \mathbf{e} \rightarrow \theta(t)|_{V_1}^1 = \lim_{t \leftarrow t_1} \theta(t_1)|_{V_1}^1\}$$

As *ren* mapping in above definition we usually take the identity mapping (almost it transforms bold variables names to non-bold variables names) because those shared variables that we want to make local are not yet in the set of local variables. In the following when *ren* is not given this identity mapping should be assumed.

2.3.2 Refinement and Composition of DTL Specifications

In this section the refinement and composition notion of the previous section are translated into DTL by defining it for machine specifications (Def. 29). This means that first the *observable* machine specification should be defined in DTL.

Definition 34 (Observable machine specification in DTL)

Given machine specification $(B, I \wedge \Box T \wedge L)$ in DTL and then the corresponding **observable** machine specification is defined as $(\mathfrak{D}(B), (\exists X. (I \wedge \Box T \wedge L)))$.

The following lemma expresses that existential quantification relates to the semantic notion of observable histories.

Lemma 6

Given DTL machine specification $\mathcal{S} = (B, I \wedge \Box T \wedge L)$ then $\mathcal{O}_X(\text{Hist}(I \wedge \Box T \wedge L)) = \text{Hist}((\exists X. (I \wedge \Box T \wedge L)))$

Theorem 4 (Refinement of machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ where $B_c \triangleq (B_c^A, (V_c, X_c))$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ where $B_a \triangleq (B_a^A, (V_a, X_a))$. Then \mathcal{S}_c refines \mathcal{S}_a denoted $\mathcal{S}_c \text{ ref } \mathcal{S}_a$ iff

$$\begin{aligned} \mathfrak{D}(B_c) &= \mathfrak{D}(B_a) \text{ and} \\ (\exists X_c. (I_c \wedge \Box T_c \wedge L_c)) &\rightarrow (\exists X_a. (I_a \wedge \Box T_a \wedge L_a)) \end{aligned}$$

Composition of DTL machine specifications can be defined in the same way as in the previous section.

Definition 35 (Composition of two DTL machine specifications)

Given DTL machine system specifications $\mathcal{S}_i \triangleq (B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq (B_i^A, B_i^P)$, for $i = 1, 2$. Let $_{B_1^A} \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2)$ be defined as

$$\begin{aligned} \Box(& \\ & \vee \epsilon = \lambda \wedge \epsilon_1 = \lambda \wedge \epsilon_2 = \lambda \\ & \vee \epsilon = \mathbf{e} \wedge \epsilon_1 = \mathbf{e} \wedge \epsilon_2 = \mathbf{e} \\ & \vee \epsilon = \mathbf{i} \wedge \epsilon_1 = \mathbf{i} \wedge \epsilon_2 = \mathbf{e} \\ & \vee \epsilon = \mathbf{i} \wedge \epsilon_1 = \mathbf{e} \wedge \epsilon_2 = \mathbf{i} \\ & \vee \bigvee_{\mathbf{a} \in \text{In}_1 \cap \text{Out}_2} \epsilon = \mathbf{i} \wedge \epsilon_1 = \mathbf{a}^? \wedge \epsilon_2 = \mathbf{a}! \\ & \vee \bigvee_{\mathbf{a} \in \text{In}_2 \cap \text{Out}_1} \epsilon = \mathbf{i} \wedge \epsilon_1 = \mathbf{a}! \wedge \epsilon_2 = \mathbf{a}^? \\ & \vee \bigvee_{\mathbf{a} \in \text{In}_1 \setminus \text{Out}_2} \epsilon = \mathbf{a}^? \wedge \epsilon_1 = \mathbf{a}^? \wedge \epsilon_2 = \mathbf{e} \\ & \vee \bigvee_{\mathbf{a} \in \text{Out}_1 \setminus \text{In}_2} \epsilon = \mathbf{a}! \wedge \epsilon_1 = \mathbf{a}! \wedge \epsilon_2 = \mathbf{e} \\ & \vee \bigvee_{\mathbf{a} \in \text{In}_2 \setminus \text{Out}_1} \epsilon = \mathbf{a}^? \wedge \epsilon_1 = \mathbf{e} \wedge \epsilon_2 = \mathbf{a}^? \\ & \vee \bigvee_{\mathbf{a} \in \text{Out}_2 \setminus \text{In}_1} \epsilon = \mathbf{a}! \wedge \epsilon_1 = \mathbf{e} \wedge \epsilon_2 = \mathbf{a}! \\ &) \end{aligned}$$

Then the **composed** machine system specification \mathcal{S} is defined as (B, H) where

$$\begin{aligned} H &\triangleq \exists \epsilon_1, \epsilon_2. \text{ }_{B_1^A} \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2) [\epsilon_2/\epsilon] \\ B &\triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2)). \end{aligned}$$

This definition can be easily extended for n DTL specifications. One has then to define a predicate $\odot_{B^A}(\epsilon, \bar{\epsilon})$ corresponding to the operation of merging n components.

Theorem 5 (Semantic merge is almost conjunction)

Given machine system specifications $(B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq ((\text{In}_i, \text{Out}_i), (V_i, X_i))$, for $i = 1, 2$ and composed machine system specification as in definition 35, i.e., (B, H) where $H \triangleq \exists \epsilon_1, \epsilon_2. \text{ }_{B_1^A} \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2) [\epsilon_2/\epsilon]$ and $B \triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2))$ then

$$\text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \otimes \text{Hist}(I_2 \wedge \Box T_2 \wedge L_2) = \text{Hist}(H)$$

2.3 Refinement and Composition of Reactive System Specifications

Encapsulation of shared variables for DTL specifications is defined as follows.

Definition 36 (Encapsulation)

Given machine specification $\mathcal{S} \triangleq (B, H)$ then **encapsulation** of V_1 in \mathcal{S} with $V_1 \subseteq V$ denoted by $\mathcal{S} \upharpoonright V_1$ is defined as $(B_1, H \wedge (\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1))$ where $B_1 \triangleq (E, V \setminus V_1, X \cup V_1)$.

The following theorem states that above definition indeed captures encapsulation.

Theorem 6

Given machine specification $\mathcal{S} \triangleq (B, H)$ and given set of shared variables $V_1 \subseteq V$ then

$$Enc_{V_1}(Hist(H)) = Hist(H \wedge (\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1))$$

Example 4

Abstract machine specification $\mathcal{S}_a \triangleq (B, I \wedge \Box T \wedge L)$ is refined by the composition of concrete machines specifications $\mathcal{S}_{c_1} \triangleq (B_1, I_1 \wedge \Box T_1 \wedge L_1)$ and $\mathcal{S}_{c_2} \triangleq (B_2, I_2 \wedge \Box T_2 \wedge L_2)$.

The abstract machine specification \mathcal{S}_a is defined as follows:

1. **Basis** $B = ((In, Out), (V, X))$

$$\begin{aligned} In &\triangleq \{\mathbf{b}\}, \\ Out &\triangleq \{\mathbf{a}\}, \\ V &\triangleq \{\mathbf{s}\}, \\ X &\triangleq \{\mathbf{x}\} \end{aligned}$$

2. **Initial States**

$$I \triangleq (\mathbf{s}, \mathbf{x}) = (0, 0)$$

3. **Transitions**

$$T \triangleq$$

$$\begin{aligned} &\vee (\epsilon = \mathbf{a}! \wedge \mathbf{x} = 0 \wedge (\mathbf{s}, \mathbf{x})' = (\mathbf{s}, 1)) \\ &\vee (\epsilon = \mathbf{b}? \wedge \mathbf{x} = 1 \wedge (\mathbf{s}, \mathbf{x})' = (\mathbf{s}, 2)) \\ &\vee (\epsilon = \mathbf{i} \wedge (\mathbf{s}, \mathbf{x}) = (1, 2) \wedge (\mathbf{s}, \mathbf{x})' = (0, \mathbf{x})) \\ &\vee (\epsilon = \mathbf{e} \wedge (\mathbf{s}, \mathbf{x})' = (1, \mathbf{x})) \\ &\vee \mathbf{stut}_a \end{aligned}$$

These transitions are illustrated in figure 2.4. Note: the stutter transitions are not drawn in all subsequent figures in order to minimize the number of edges.

4. **Liveness**

$$L \triangleq \mathbf{true}$$

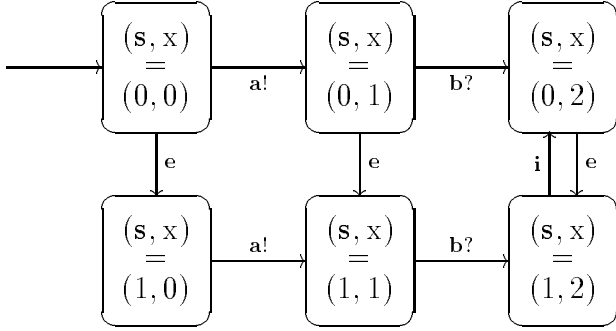


Figure 2.4: Abstract machine

The definition of \mathcal{S}_{c_1} is as follows:

1. **Basis** $B_1 = ((\text{In}_1, \text{Out}_1), (\text{V}_1, \text{X}_1))$

$$\begin{aligned} \text{In}_1 &\triangleq \{\mathbf{c}\}, \\ \text{Out}_1 &\triangleq \{\mathbf{a}\}, \\ \text{V}_1 &\triangleq \{\mathbf{s}\}, \\ \text{X}_1 &\triangleq \{\mathbf{t}\} \end{aligned}$$

2. **Initial States**

$$I_1 \triangleq (\mathbf{s}, \mathbf{t}) = (0, 0)$$

3. **Transitions**

$$T_1 \triangleq$$

$$\vee (\epsilon = \mathbf{a}! \wedge \mathbf{t} = 0 \wedge (\mathbf{s}, \mathbf{t})' = (\mathbf{s}, 1))$$

$$\vee (\epsilon = \mathbf{c}? \wedge \mathbf{t} = 1 \wedge (\mathbf{s}, \mathbf{t})' = (\mathbf{s}, 2))$$

$$\vee (\epsilon = \mathbf{e} \wedge (\mathbf{s}, \mathbf{t})' = (1, \mathbf{t}))$$

$$\vee (\epsilon = \mathbf{e} \wedge (\mathbf{s}, \mathbf{t})' = (0, \mathbf{t}))$$

$$\vee \mathbf{stut}_1$$

These transitions are illustrated in figure 2.5

4. **Liveness**

$$L_1 \triangleq \mathbf{true}$$

The definition of \mathcal{S}_{c_2} is as follows:

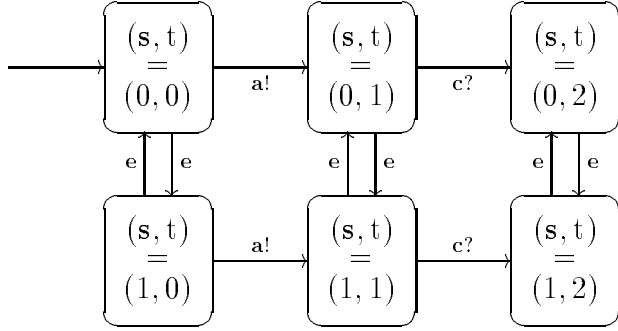


Figure 2.5: Concrete machine 1

1. **Basis** $B_2 = ((In_2, Out_2), (V_2, X_2))$

$$\begin{aligned} In_2 &\stackrel{\Delta}{=} \{\mathbf{b}\}, \\ Out_2 &\stackrel{\Delta}{=} \{\mathbf{c}\}, \\ V_2 &\stackrel{\Delta}{=} \{\mathbf{s}\}, \\ X_2 &\stackrel{\Delta}{=} \{\mathbf{u}\} \end{aligned}$$

2. **Initial States**

$$I_2 \stackrel{\Delta}{=} (\underline{s}, \underline{u}) = (0, 0)$$

3. **Transitions**

$$\begin{aligned} T_2 &\stackrel{\Delta}{=} \\ &\vee (\epsilon = \mathbf{c}! \wedge u = 0 \wedge (\underline{s}, \underline{u})' = (\underline{s}, 1)) \\ &\vee (\epsilon = \mathbf{b}? \wedge u = 1 \wedge (\underline{s}, \underline{u})' = (\underline{s}, 2)) \\ &\vee (\epsilon = \mathbf{i} \wedge (\underline{s}, \underline{u}) = (1, 2) \wedge (\underline{s}, \underline{u})' = (0, \underline{u})) \\ &\vee (\epsilon = \mathbf{e} \wedge (\underline{s}, \underline{u})' = (1, \underline{u})) \\ &\vee \mathbf{stut}_2 \end{aligned}$$

These transitions are illustrated in figure 2.6

4. **Liveness**

$$L_2 \stackrel{\Delta}{=} \mathbf{true}$$

According to definition 35 the composition of \mathcal{S}_{c_1} and \mathcal{S}_{c_2} is as follows:

$$\left(\left((In_1 \setminus Out_2 \cup In_2 \setminus Out_1, Out_1 \setminus In_2 \cup Out_2 \setminus In_1), (V_1 \cup V_2, X_1 \cup X_2) \right), \right. \\ \left. \exists \epsilon_1, \epsilon_2. (B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \square T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \square T_2 \wedge L_2) [\epsilon_2/\epsilon]) \right)$$

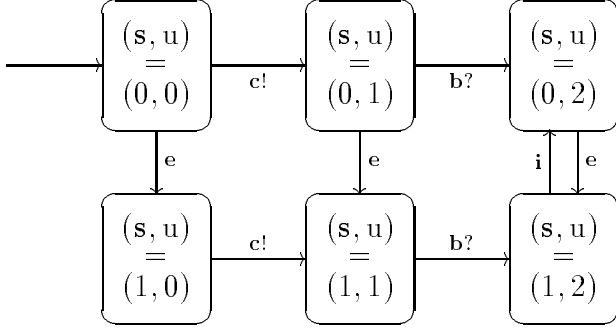


Figure 2.6: Concrete machine 2

where

$$\begin{aligned}
\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1 &= \{\mathbf{a}\} \\
\text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1 &= \{\mathbf{b}\} \\
V_1 \cup V_2 &= \{\mathbf{s}\} \\
X_1 \cup X_2 &= \{\mathbf{t}, \mathbf{u}\} \\
I_1[\epsilon_1/\epsilon] \wedge I_2[\epsilon_2/\epsilon] &= (\mathbf{s}, \mathbf{t}, \mathbf{u}) = (0, 0, 0) \\
T_1[\epsilon_1/\epsilon] \wedge T_2[\epsilon_2/\epsilon] &= \left[\begin{aligned} &\vee \left(\epsilon_1 = \mathbf{a}! \wedge \mathbf{t} = 0 \wedge (\mathbf{s}, \mathbf{t})' = (\mathbf{s}, 1) \right) \\ &\vee \left(\epsilon_1 = \mathbf{c}? \wedge \mathbf{t} = 1 \wedge (\mathbf{s}, \mathbf{t})' = (\mathbf{s}, 2) \right) \\ &\vee \left(\epsilon_1 = \mathbf{e} \wedge (\mathbf{s}, \mathbf{t})' = (1, \mathbf{t}) \right) \\ &\vee \left(\epsilon_1 = \mathbf{e} \wedge (\mathbf{s}, \mathbf{t})' = (0, \mathbf{t}) \right) \\ &\vee \text{stut}_1[\epsilon_1/\epsilon] \end{aligned} \right] \\
&\wedge \\
&\left[\begin{aligned} &\vee \left(\epsilon_2 = \mathbf{c}! \wedge \mathbf{u} = 0 \wedge (\mathbf{s}, \mathbf{u})' = (\mathbf{s}, 1) \right) \\ &\vee \left(\epsilon_2 = \mathbf{b}? \wedge \mathbf{u} = 1 \wedge (\mathbf{s}, \mathbf{u})' = (\mathbf{s}, 2) \right) \\ &\vee \left(\epsilon_2 = \mathbf{i} \wedge (\mathbf{s}, \mathbf{u}) = (1, 2) \wedge (\mathbf{s}, \mathbf{u})' = (0, \mathbf{u}) \right) \\ &\vee \left(\epsilon_2 = \mathbf{e} \wedge (\mathbf{s}, \mathbf{u})' = (1, \mathbf{u}) \right) \\ &\vee \text{stut}_2[\epsilon_2/\epsilon] \end{aligned} \right] \\
L_1[\epsilon_1/\epsilon] \wedge L_2[\epsilon_2/\epsilon] &= \mathbf{true}
\end{aligned}$$

Let $H_c \triangleq \exists \epsilon_1, \epsilon_2. (B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \square T_1 \wedge L_1)[\epsilon_1/\epsilon] \wedge (I_2 \wedge \square T_2 \wedge L_2)[\epsilon_2/\epsilon])$ and $H_a \triangleq I \wedge \square T \wedge L$, then the composition of \mathcal{S}_{c_1} and \mathcal{S}_{c_2} refines \mathcal{S}_a iff

- (1) $\mathfrak{D}(B_c) = \mathfrak{D}(B)$
- (2) $\models (\exists \mathbf{t}, \mathbf{u}. (H_c)) \rightarrow (\exists \mathbf{x}. (H_a))$

The following section will show that both conditions hold. Hence we have refinement.

2.4 Proving Refinement of Reactive System Specifications

This section explains how refinement of reactive systems can be proved. The standard technique of Abadi & Lamport [AL91] is used, i.e., refinement is proven by providing a refinement mapping from the concrete system to the abstract system. Firstly we give its definition at the semantic level and then for DTL specifications.

2.4.1 Proving Semantic Refinement of Specifications

Refinement of reactive systems is proved by means of a *refinement mapping* from the concrete system to the abstract system. A refinement mapping maps a history at the concrete level to a history at the abstract level, more specifically, it maps the states appearing in the concrete history to states appearing in the abstract history.

Definition 37 (Refinement mapping between systems)

Given concrete system $\mathcal{S}_c \triangleq (B_c, H_c)$ and abstract system $\mathcal{S}_a \triangleq (B_a, H_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. A **refinement mapping** from \mathcal{S}_c to \mathcal{S}_a is a mapping f from states appearing in histories of H_c to states appearing in histories of H_a , i.e., f is mapping from with $f : \Sigma \rightarrow \Sigma$ s.t.

- The values of observable variables are not changed, i.e., for all $\sigma \in \Sigma$: $\sigma|_{V_c}^1 = f(\sigma)|_{V_c}^1$.
- For all $h_c \in H_c$ there exists a $h_a \in H_a$ s.t. for all $t \in \mathbb{R}^{\geq 0}$, $\psi_c(t) = \psi_a(t)$ and $\theta_a(t) = f(\theta_c(t))$.

Lemma 7

Given concrete system $\mathcal{S}_c \triangleq (B_c, H_c)$ and abstract system $\mathcal{S}_a \triangleq (B_a, H_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a refinement mapping from \mathcal{S}_c to \mathcal{S}_a , then $\mathcal{S}_c \mathbf{ref} \mathcal{S}_a$.

The concept of refinement mappings can also be applied to machine specifications. A machine specification is of the form $(B, \text{Comp}(M) \cap L)$. Refinement means then that $f(\text{Comp}(M_c) \cap L_c) \subseteq \text{Comp}(M_a) \cap L_a$ for refinement mapping f . This can be split into (1) $f(\text{Comp}(M_c) \cap L_c) \subseteq \text{Comp}(M_a)$ and (2) $f(\text{Comp}(M_c) \cap L_c) \subseteq L_a$. From $f(\text{Comp}(M_c)) \subseteq \text{Comp}(M_a)$ follows (1) because $f(\text{Comp}(M_c) \cap L_c) \subseteq f(\text{Comp}(M_c))$. So the verification condition can be split into a condition on machines and a condition involving machines together with supplementary conditions. This leads to the following definition.

Definition 38 (Refinement mapping between machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, \text{Comp}(M_c) \cap L_c)$, where $M_c \triangleq (B_c, I_c, T_c)$, and abstract machine specification $\mathcal{S}_a \triangleq (B_a, \text{Comp}(M_a) \cap L_a)$, where $M_a \triangleq (B_a, I_a, T_a)$. A **refinement mapping** from machine specification \mathcal{S}_c to machine specification \mathcal{S}_a is a mapping $f : \Sigma \rightarrow \Sigma$ s.t.

- For all $\sigma \in \Sigma$, $\sigma|_{V_c}^1 = f(\sigma)|_{V_c}^1$.
- – For all $\sigma_c \in I_c$, there exist $\sigma_a \in I_a$ s.t. $\sigma_a = f(\sigma_c)$.

- For all $\langle d, \sigma_{c1}, \sigma_{c2} \rangle \in T_c$, $\langle d, f(\sigma_{c1}), f(\sigma_{c2}) \rangle \in T_a$ or $(f(\sigma_{c1}) = f(\sigma_{c2}) \wedge d(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\})$.
- For all $h_c \in \text{Comp}(M_c) \cap L_c$ there exist a $h_a \in L_a$ s.t. for all $t \in \mathbb{R}^{\geq 0}$, $\psi_c(t) = \psi_a(t)$ and $f(\theta_c(t)) = \theta_a(t)$.

The following lemma expresses that refinement mappings are indeed sound for proving refinement of machine specifications.

Lemma 8

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, \text{Comp}(M_c) \cap L_c)$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, \text{Comp}(M_a) \cap L_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a refinement mapping from \mathcal{S}_c to \mathcal{S}_a then $\mathcal{S}_c \mathbf{ref} \mathcal{S}_a$.

2.4.2 Proving Refinement of DTL Specifications

Proving refinement of machine specifications in DTL means according to Theorem 4 that the observable bases are equal and that a formula with two existential quantifications is valid. More specifically:

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$. Then $\mathcal{S}_c \mathbf{refines} \mathcal{S}_a$ is denoted $\mathcal{S}_c \mathbf{ref} \mathcal{S}_a$ and defined by

$$\begin{aligned} \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\ (\exists X_c . (I_c \wedge \Box T_c \wedge L_c)) \rightarrow (\exists X_a . (I_a \wedge \Box T_a \wedge L_a)) \end{aligned}$$

So we must have a rule to prove the following:

$$\exists x_0 . p_0 \rightarrow \exists x_1 . p_1$$

The following rule does the job:

$$\frac{p_0 \rightarrow p_1 [exp/x_1] \text{ for } x_0 \text{ not free in } p_1}{\exists x_0 . p_0 \rightarrow \exists x_1 . p_1} \text{ none of the variables appearing in } exp \text{ is quantified in } p_1$$

as the following derivation shows:

$$\begin{aligned} & p_0 \rightarrow p_1 [exp/x_1] \\ \rightarrow & \quad \% \text{ Generalization, prop.calc.} \\ & p_0 \Rightarrow p_1 [exp/x_1] \\ \rightarrow & \quad \% \text{ contraposition} \\ & \neg p_1 [exp/x_1] \Rightarrow \neg p_0 \\ \rightarrow & \quad \% \text{ Ax11 : } \forall x_1 . \neg p_1 \Rightarrow \neg p_1 [exp/x_1] \text{ where none of the variables} \\ & \quad \text{appearing in } exp \text{ is quantified in } \neg p_1, \text{ Modus Ponus} \\ & \forall x_1 . \neg p_1 \Rightarrow \neg p_0 \\ \rightarrow & \quad \% \text{ Rule } (q_0 \Rightarrow q_1) \rightarrow q_0 \Rightarrow \forall x_0 . q_1, \text{ for } x_0 \text{ not free in } q_0 \\ & \forall x_1 . \neg p_1 \Rightarrow \forall x_0 . \neg p_0 \\ \rightarrow & \quad \% \Box p \rightarrow p, \text{ Modus Ponus} \\ & \forall x_1 . \neg p_1 \rightarrow \forall x_0 . \neg p_0 \\ = & \quad \% \text{ contraposition} \\ & \exists x_0 . p_0 \rightarrow \exists x_1 . p_1 \end{aligned}$$

2.4 Proving Refinement of Reactive System Specifications

From the previous section it should be clear that this *exp* is exactly the refinement mapping f , and that the proof can be split in a safety part and a liveness part (i.e., the proof of $p_0 \rightarrow p_1 [exp/x_1]$ of above rule is split into a safety and a liveness part). This culminates in the following proof rule for refinement based on similar ones in [Lam91, KMP93].

Rule 1 (Proof rule for refinement)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. Let f be a refinement mapping from \mathcal{S}_c to \mathcal{S}_a then

$$\frac{\begin{array}{l} \mathcal{S}_c \models I_c \rightarrow I_a [f/X_a] \\ \mathcal{S}_c \models T_c \rightarrow T_a [f/X_a] \\ \mathcal{S}_c \models L_a [f/X_a] \end{array}}{\models (\exists X_c . (I_c \wedge \Box T_c \wedge L_c)) \rightarrow (\exists X_a . (I_a \wedge \Box T_a \wedge L_a))}$$

When L_a is of the form

$$\bigwedge_{\tau \in \text{WF}_a} (\Diamond \Box En(\tau) \rightarrow \Box \Diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_a} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

(see Def. 29), the last premise of above rule can be split into

$$\begin{array}{l} \mathcal{S}_c \models \bigwedge_{\tau \in \text{WF}_a} (\Diamond \Box En(\tau) \rightarrow \Box \Diamond \tau) [f/X_a] \\ \mathcal{S}_c \models \bigwedge_{\tau \in \text{SF}_a} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau) [f/X_a]. \end{array}$$

This is equal to

$$\begin{array}{l} \mathcal{S}_c \models \bigwedge_{\tau \in \text{WF}_a} (En(\tau) \Rightarrow \Diamond (En(\tau) \rightarrow \tau)) [f/X_a] \\ \mathcal{S}_c \models \bigwedge_{\tau \in \text{SF}_a} (\Box \Diamond En(\tau) \Rightarrow \Diamond \tau) [f/X_a] \end{array}$$

using some temporal logic calculus. So one gets the following proof rule, similar rules appearing in [Lam91, KMP93].

Rule 2 (Proof rule for refinement)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ where L_c is of the form $\bigwedge_{\tau \in \text{WF}_c} (\Diamond \Box En(\tau) \rightarrow \Box \Diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_c} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$. Furthermore given abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ where L_a is of the form $\bigwedge_{\tau \in \text{WF}_a} (\Diamond \Box En(\tau) \rightarrow \Box \Diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_a} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$. Let $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. Let f be a refinement mapping from \mathcal{S}_c to \mathcal{S}_a then

$$\frac{\begin{array}{l} \mathcal{S}_c \models I_c \rightarrow I_a [f/X_a] \\ \mathcal{S}_c \models T_c \rightarrow T_a [f/X_a] \\ \mathcal{S}_c \models \bigwedge_{\tau \in \text{WF}_a} En(\tau) [f/X_a] \Rightarrow \Diamond (En(\tau) [f/X_a] \rightarrow \tau [f/X_a]) \\ \mathcal{S}_c \models \bigwedge_{\tau \in \text{SF}_a} \Box \Diamond En(\tau) [f/X_a] \Rightarrow \Diamond \tau [f/X_a] \end{array}}{\models (\exists X_c . (I_c \wedge \Box T_c \wedge L_c)) \rightarrow (\exists X_a . (I_a \wedge \Box T_a \wedge L_a))}$$

Rule 1 is used in the following example for proving refinement of example 4.

Example 5

From example 4 we have:

Let $H_c \triangleq \exists \epsilon_1, \epsilon_2. (B_1^A \odot B_2^A (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2) [\epsilon_2/\epsilon])$ and $H_a \triangleq I \wedge \Box T \wedge L$ then the composition of \mathcal{S}_{c_1} and \mathcal{S}_{c_2} refines \mathcal{S}_a iff

- (1) $\mathfrak{D}(B_c) = \mathfrak{D}(B)$
- (2) $\models (\exists t, u. (H_c)) \rightarrow (\exists x. (H_a))$

Because the observable bases are equal (1) holds. (2) is proven with rule 1. This means one has to find a refinement mapping f . In order to find such a mapping the picture of the $\mathcal{S}_{c_1} \parallel \mathcal{S}_{c_2}$ is given. (Note only the reachable states are drawn):

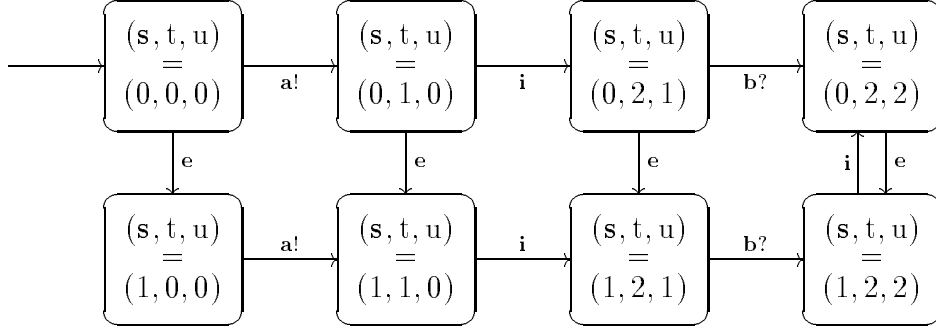


Figure 2.7: Transitions of $\mathcal{S}_{c_1} \parallel \mathcal{S}_{c_2}$

Relating the above figure with figure 2.4 one sees that f is as defined follows:

if

- $t = 0 \wedge u = 0$ then $f(s, t, u) = t$
- $t = 1 \wedge u = 0$ then $f(s, t, u) = t$
- $t = 2 \wedge u = 1$ then $f(s, t, u) = t - u$
- $t = 2 \wedge u = 2$ then $f(s, t, u) = u$

fi

The following premises should be valid in order to apply the rule:

- $\mathcal{S}_c \models (s, t, u) = (0, 0, 0) \rightarrow ((s, x) = (0, 0)) [f/x]$

Substitution means replacing x by t because $t = 0 \wedge u = 0$. This results in:

$$(s, t, u) = (0, 0, 0) \rightarrow (s, t) = (0, 0)$$

This is valid.

- $\mathcal{S}_c \models \left(\epsilon = \mathbf{a}! \wedge t = 0 \wedge (s, t, u)' = (s, 1, u) \right) \rightarrow \left(\epsilon = \mathbf{a}! \wedge x = 0 \wedge (s, x)' = (s, 1) \right) [f/x]$

2.4 Proving Refinement of Reactive System Specifications

Substitution means replacing x by t because $t = 0$, and replacing x' by t' because $t' = 1$. This results in:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{a}! \wedge t = 0 \wedge (\mathbf{s}, t, \mathbf{u})' = (\mathbf{s}, 1, \mathbf{u})) \\ &\rightarrow \\ &(\epsilon = \mathbf{a}! \wedge t = 0 \wedge (\mathbf{s}, t)' = (\mathbf{s}, 1)) \end{aligned}$$

This is valid.

- $\mathcal{S}_c \models (\epsilon = \mathbf{b}? \wedge u = 1 \wedge (\mathbf{s}, t, \mathbf{u})' = (\mathbf{s}, t, 2))$
 \rightarrow
 $(\epsilon = \mathbf{b}? \wedge x = 1 \wedge (\mathbf{s}, x)' = (\mathbf{s}, 2)) [f/x]$

Substitution means replacing x by $t - u$ because $u = 1$, and replacing x' by u' because $u' = 2$. This results in:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{b}? \wedge u = 1 \wedge (\mathbf{s}, t, \mathbf{u})' = (\mathbf{s}, t, 2)) \\ &\rightarrow \\ &(\epsilon = \mathbf{b}? \wedge t - u = 0 \wedge (\mathbf{s}, u)' = (\mathbf{s}, 2)) \end{aligned}$$

This is valid because from figure 2.7 one sees that $u = 1 \Rightarrow t = 2$ holds.

- $\mathcal{S}_c \models (\epsilon = \mathbf{e} \wedge (\mathbf{s}, t, \mathbf{u})' = (1, t, \mathbf{u}))$
 \rightarrow
 $(\epsilon = \mathbf{e} \wedge (\mathbf{s}, x)' = (1, x)) [f/x]$

Substitution means replacing x by f , and replacing x' by f' . This results in:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{e} \wedge (\mathbf{s}, t, \mathbf{u})' = (1, t, \mathbf{u})) \\ &\rightarrow \\ &(\epsilon = \mathbf{e} \wedge (\mathbf{s}, f)' = (\mathbf{s}, f)) \end{aligned}$$

This is valid because $(t, u)' = (t, u) \Rightarrow f' = f$.

- $\mathcal{S}_c \models (\epsilon = \mathbf{i} \wedge (t, u) = (1, 0) \wedge (\mathbf{s}, t, \mathbf{u})' = (\mathbf{s}, 2, 1))$
 \rightarrow
 $\mathbf{stut}_a [f/x]$

Because $\mathbf{stut}_a \triangleq$

$$\begin{aligned} &\epsilon = \lambda \\ \vee &(\epsilon = \mathbf{i} \wedge (\mathbf{s}, x)' = (\mathbf{s}, x)) \\ \vee &(\epsilon = \mathbf{e} \wedge (\mathbf{s}, x)' = (\mathbf{s}, x)) \end{aligned}$$

it suffices to prove:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{i} \wedge (t, u) = (1, 0) \wedge (s, t, u)' = (s, 2, 1)) \\ &\rightarrow \\ &(\epsilon = \mathbf{i} \wedge (s, x)' = (s, x)) [f/x] \end{aligned}$$

Substitution means replacing x by t because $t = 1 \wedge u = 0$, and replacing x' by $t' - u'$ because $t' = 2 \wedge u' = 1$. This results in:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{i} \wedge (t, u) = (1, 0) \wedge (s, t, u)' = (s, 2, 1)) \\ &\rightarrow \\ &(\epsilon = \mathbf{i} \wedge (s, t - u)' = (s, t)) \end{aligned}$$

This is valid.

- $\mathcal{S}_c \models (\epsilon = \mathbf{i} \wedge (s, u) = (1, 2) \wedge (s, t, u)' = (0, t, u))$
 \rightarrow
 $(\epsilon = \mathbf{i} \wedge (s, x) = (1, 2) \wedge (s, x)' = (0, x)) [f/x]$

Substitution means replacing x by u because $u = 2 \wedge t = 2$, and replacing x' by u' because $u' = 2 \wedge t' = 2$. This results in:

$$\begin{aligned} \mathcal{S}_c &\models (\epsilon = \mathbf{i} \wedge (s, u) = (1, 2) \wedge (s, t, u)' = (0, t, u)) \\ &\rightarrow \\ &(\epsilon = \mathbf{i} \wedge (s, u) = (1, 2) \wedge (s, u)' = (s, u)) \end{aligned}$$

This is valid.

- $\mathcal{S}_c \models \mathbf{stut}_c \rightarrow \mathbf{stut}_a [f/x]$

Definition of \mathbf{stut}_a and \mathbf{stut}_c results in

$$\begin{aligned} \mathcal{S}_c &\models \epsilon = \lambda \\ &\vee (\epsilon = \mathbf{i} \wedge (s, t, u)' = (s, t, u)) \\ &\vee (\epsilon = \mathbf{e} \wedge (s, t, u)' = (s, t, u)) \\ &\rightarrow \\ &\epsilon = \lambda \\ &\vee (\epsilon = \mathbf{i} \wedge (s, x)' = (s, x)) [f/x] \\ &\vee (\epsilon = \mathbf{e} \wedge (s, x)' = (s, x)) [f/x] \end{aligned}$$

This is valid.

- $\mathcal{S}_c \models \mathbf{true} [f/x]$

This is valid.

So $\mathcal{S}_{c1} \parallel \mathcal{S}_{c2} \mathbf{ref} \mathcal{S}_a$.

2.5 Relative Refinement and Composition of Reactive System Specifications

In this section the concept of *relative* refinement and composition in the development of systems is explained. Ordinary refinement stipulates that the set of histories generated by the concrete system is included in the set of histories generated by the abstract system. Relative refinement means that this inclusion almost holds, i.e., if one leaves some of the histories generated at the concrete level out of account this inclusion holds. Histories generated by the abstract system can also be left out because a concrete system could be an abstract system in a next refinement step. Ordinary composition means that the histories of two components are merged into the histories of the composed system. Relative composition means that one leaves certain histories out of this merge, i.e., the merge is performed on smaller sets of histories generated by the components. In the first two subsections we consider the sets that extract the good computations as arbitrary, i.e., it can be a safety set, liveness set or neither of them. In the third subsection a condition similar to machine closedness is imposed on a relative system, i.e., the relative system can then be split into a safety part and a liveness part. Using this fact a proof rule for relative refinement is constructed in the last subsection based on rule given in Section 2.4. Again we formulate these concepts first in terms of sets of histories and then in DTL.

2.5.1 Semantic Relative Refinement and Composition of Specifications

Definition 39 (Relative refinement of systems)

Given concrete system $\mathcal{S}_c \triangleq (B_c, H_c)$ and aset W_c of allowed histories for \mathcal{S}_c ($W_c \subseteq \mathcal{H}$ constraining B_c) and abstract system $\mathcal{S}_a \triangleq (B_a, H_a)$ together with a set W_a of allowed histories for \mathcal{S}_a ($W_a \subseteq \mathcal{H}$ constraining B_a). Let $G_c \triangleq H_c \cap W_c$ and $G_a \triangleq H_a \cap W_a$. Then \mathcal{S}_c **relatively refines** \mathcal{S}_a with respect to (W_c, W_a) , denoted by $\mathcal{S}_c \mathop{\text{ref}}^{W_a} \mathcal{S}_a$, iff $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$ and $\mathcal{O}_{X_c}(G_c) \subseteq \mathcal{O}_{X_a}(G_a)$.

Relativizing can also be used for composition, i.e., if during composition one gets unwanted histories these are removed, using a set that characterizes the allowed histories.

Definition 40 (Relative composition of two systems)

Given systems $\mathcal{S}_i = (B_i, H_i)$ where $B_i = ((\text{In}_i, \text{Out}_i), (V_i, X_i))$ and $(i = 1, 2)$ such that $X_1 \cap X_2 = \emptyset$ and given sets $W_i \subseteq \mathcal{H}$ constraining B_i . Let \overline{W} denote (W_1, W_2) . Then the **relative composed system** \mathcal{S} with respect to \overline{W} , denoted $\mathcal{S}_1 \mid \overline{W} \mid \mathcal{S}_2$, is defined as (B, H) with $B \triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2))$, and $H \triangleq H_1 \circledast H_2 \triangleq (H_1 \cap W_1) \otimes (H_2 \cap W_2)$.

The following is a compositional relative refinement theorem.

Theorem 7 (Compositional relative refinement)

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and given set W_c constraining B_{12} (the

basis of $\mathcal{S}_1 \parallel \mathcal{S}_2$). And given abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) and given set W_a constraining B_{34} (the basis of $\mathcal{S}_3 \parallel \mathcal{S}_4$). Then the following holds:

$$\begin{array}{l}
(H_1 \otimes H_2) \cap (W_{c1} \otimes W_{c2}) \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
W_c \subseteq W_{c1} \otimes W_{c2} \\
W_{a3} \otimes W_{a4} \subseteq W_a \\
\mathcal{S}_1 \text{ }_{W_{c1}} \text{ref }^{W_{a3}} \mathcal{S}_3 \\
\mathcal{S}_2 \text{ }_{W_{c2}} \text{ref }^{W_{a4}} \mathcal{S}_4 \\
\hline
\mathcal{S}_1 \parallel \mathcal{S}_2 \text{ }_{W_c} \text{ref }^{W_a} \mathcal{S}_3 \parallel \mathcal{S}_4
\end{array}
\quad
\begin{array}{l}
W_{ci} \text{ constraining } B_i \text{ (} i=1,2 \text{)} \\
W_{aj} \text{ constraining } B_j \text{ (} j=3,4 \text{)}
\end{array}$$

If the extra requirements W don't constrain the ϵ -variables then the following lemma can be used to prove the first premise of above theorem.

Lemma 9

Given systems $\mathcal{S}_i = (B_i, H_i)$ and sets W_i constraining B_i ($i = 1, 2$) with no restrictions on the event variables. Then the following holds:

$$(H_1 \cap W_1) \otimes (H_2 \cap W_2) = H_1 \otimes H_2 \cap W_1 \otimes W_2$$

In case the abstract requirement W_a can't be decomposed into component requirements the following rule can be used.

Lemma 10

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and given set W_c constraining B_{12} . And given abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) and given set W_a constraining B_{34} without restricting the ϵ variables. Then the following holds:

$$\begin{array}{l}
H_1 \otimes H_2 \cap W_{c1} \otimes W_{c2} \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
W_c \subseteq W_{c1} \otimes W_{c2} \\
\mathcal{S}_1 \text{ }_{W_{c1}} \text{ref }^{W_a} \mathcal{S}_3 \\
\mathcal{S}_2 \text{ }_{W_{c2}} \text{ref }^{W_a} \mathcal{S}_4 \\
\hline
\mathcal{S}_1 \parallel \mathcal{S}_2 \text{ }_{W_c} \text{ref }^{W_a} \mathcal{S}_3 \parallel \mathcal{S}_4
\end{array}
\quad
W_{ci} \text{ constraining } B_i \text{ (} i=1,2 \text{)}$$

The following lemma is useful for proving the second premise of the theorem.

Lemma 11

Given sets W_i ($i = 1, 2$) not restricting the ϵ variables then

$$W_1 \otimes W_2 = W_1 \cap W_2.$$

2.5.2 Relative Refinement and Composition of DTL Specifications

Theorem 8 (Relative refinement of DTL machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ and DTL formula W_c over B_c and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ and DTL formula W_a over B_a . Let $G_c \triangleq I_c \wedge \Box T_c \wedge L_c \wedge W_c$ and $G_a \triangleq I_a \wedge \Box T_a \wedge L_a \wedge W_a$. Then $\mathcal{S}_c \text{ }_{Hist(W_c)} \text{ref }^{Hist(W_a)} \mathcal{S}_a$ iff

$$\begin{array}{l}
\mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\
\models (\exists X_c . (G_c)) \rightarrow (\exists X_a . (G_a))
\end{array}$$

2.5 Relative Refinement and Composition of Reactive System Specifications

Definition 41 (Relative composition of two DTL machine specifications)

Given machine system specifications $(B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq ((In_i, Out_i), (V_i, X_i))$, and given DTL formulae W_i over B_i for $i = 1, 2$. Then the **relative composed machine specification** \mathcal{S} w.r.t. \overline{W} is defined as (B, H) where $B \triangleq ((In_1 \setminus Out_2 \cup In_2 \setminus Out_1, Out_1 \setminus In_2 \cup Out_2 \setminus In_1), (V_1 \cup V_2, X_1 \cup X_2))$ and $H \triangleq \exists \epsilon_1, \epsilon_2. (B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1 \wedge W_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2 \wedge W_2) [\epsilon_2/\epsilon])$.

Theorem 9 (Relative composition corresponds to semantic merge)

Given machine system specifications $(B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq ((In_i, Out_i), (V_i, X_i))$, and given DTL formulae W_i over B_i for $i = 1, 2$ and let $\overline{W} \triangleq (Hist(W_c), Hist(W_a))$ and given the relative composed system as in Def. 41, i.e., (B, H) where $B \triangleq ((In_1 \setminus Out_2 \cup In_2 \setminus Out_1, Out_1 \setminus In_2 \cup Out_2 \setminus In_1), (V_1 \cup V_2, X_1 \cup X_2))$ and $H \triangleq \exists \epsilon_1, \epsilon_2. (B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1 \wedge W_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2 \wedge W_2) [\epsilon_2/\epsilon])$ then

$$Hist(I_1 \wedge \Box T_1 \wedge L_1) \overline{W} Hist(I_2 \wedge \Box T_2 \wedge L_2) = Hist(H)$$

2.5.3 Proving Semantic Relative Refinement of Specifications

The above sections explain the purpose of the restricting set W . In order to prove relative refinement we must know how this set W looks like. Is it a safety set, a liveness set or neither of them? A result of [AS85] states that every set of histories can be represented as the intersection of a safety and a liveness set. Now lemma 1 expresses that for a machine M_1 , $Comp(M_1)$ is a safety set. So we will represent W as a machine M_1 and an external set L_1 s.t. W is machine closed, i.e., $cl(Comp(M_1) \cap L_1) = Comp(M_1)$. We also require that $(B, Comp(M) \cap Comp(M_1) \cap L \cap L_1)$ is machine closed, i.e., $cl(Comp(M) \cap Comp(M_1) \cap L \cap L_1) = Comp(M) \cap Comp(M_1)$, because this is the system that is used in the relative refinement relation. We want to use the refinement mappings of Def. 38 to prove relative refinement of systems. This means that $Comp(M) \cap Comp(M_1)$ should be represented as a machine M_2 such that $Comp(M_2) = Comp(M) \cap Comp(M_1)$. The following lemma expresses that this M_2 can be constructed from M and M_1 .

Lemma 12

Given machines $M \triangleq (B, I, T)$ and $M_1 \triangleq (B, I_1, T_1)$. Define machine M_2 as (B, I_2, T_2) where I_2 and T_2 are as follows:

- $I_2 \triangleq I \cap I_1$, and
- $T_2 \triangleq T \cap T_1$.

Then $Comp(M_2) = Comp(M) \cap Comp(M_1)$.

Now the technique of refinement mappings from Section 2.4 can be applied to prove relative refinement of systems. This is expressed in the following definition.

Definition 42 (Relative refinement mapping between machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, Comp(M_c) \cap L_c)$ and set $W_c = Comp(M_{c1}) \cap L_{c1}$, and given abstract machine specification $\mathcal{S}_a \triangleq (B_a, Comp(M_a) \cap L_a)$ and set $W_a = Comp(M_{a1}) \cap L_{a1}$. A **relative refinement mapping** from machine specification \mathcal{S}_c to machine specification \mathcal{S}_a is a mapping $f : \Sigma \rightarrow \Sigma$ s.t.

- For all $\sigma \in \Sigma$, $\sigma|_{V_c}^1 = f(\sigma)|_{V_c}^1$.
- – For all $\sigma_c \in I_c \cap I_{c1}$, exist $\sigma_a \in I_a \cap I_{a1}$ s.t. $\sigma_a = f(\sigma_c)$.
- For all $\langle d, \sigma_{c1}, \sigma_{c2} \rangle \in T_c \cap T_{c1}$, $\langle d, f(\sigma_{c1}), f(\sigma_{c2}) \rangle \in T_a \cap T_{a1}$ or $(f(\sigma_{c1}) = f(\sigma_{c2}) \wedge d(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\})$.
- For all $h_c \in \text{Comp}(M_c) \cap \text{Comp}(M_{c1} \cap L_c)$ there exist a $h_a \in L_a \cap L_{a1}$ s.t. for all $t \in \mathbb{R}^{\geq 0}$, $\langle \psi_c(t), \theta_c(t) \rangle = \langle \psi_a(t), \theta(t)_a \rangle$ and $f(\theta_c(t)) = \theta_a(t)$.

The following lemma expresses that relative refinement mappings are indeed sufficient for proving relative refinement of machine specifications.

Lemma 13

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, \text{Comp}(M_c) \cap L_c)$ and set $W_c = \text{Comp}(M_{c1}) \cap L_{c1}$, and given abstract machine specification $\mathcal{S}_a \triangleq (B_a, \text{Comp}(M_a) \cap L_a)$ and set $W_a = \text{Comp}(M_{a1}) \cap L_{a1}$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a relative refinement mapping from \mathcal{S}_c to \mathcal{S}_a then $\mathcal{S}_c \text{ } W_c \text{ ref }^{W_a} \mathcal{S}_a$.

2.5.4 Proving Relative Refinement of DTL Specifications

Using the results of the previous section and Section 2.4 it is not surprising that following rule can be applied to prove relative refinement of systems.

Rule 3 (Proof rule for relative refinement)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ and $W_c \triangleq I_{c1} \wedge \Box T_{c1} \wedge L_{c1}$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ and $W_a \triangleq I_{a1} \wedge \Box T_{a1} \wedge L_{a1}$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. Let f be a relative refinement mapping from \mathcal{S}_c to \mathcal{S}_a then

$$\begin{array}{l}
\mathcal{S}_c \cap \text{Hist}(W_c) \models (I_c \wedge I_{c1}) \rightarrow (I_a \wedge I_{a1}) [f/X_a] \\
\mathcal{S}_c \cap \text{Hist}(W_c) \models (T_c \wedge T_{c1}) \rightarrow (T_a \wedge T_{a1}) [f/X_a] \\
\mathcal{S}_c \cap \text{Hist}(W_c) \models (L_c \wedge L_{c1}) [f/X_a] \\
\hline
\models (\exists X_c. (I_c \wedge \Box T_c \wedge L_c \wedge W_c)) \rightarrow (\exists X_a. (I_a \wedge \Box T_a \wedge L_a \wedge W_a))
\end{array}$$

Chapter 3

Readers/Writers Example

3.1 Introduction

The relative refinement technique will now be used to formalize Dijkstra's development strategy for the readers/writers problem. The readers/writers problem, described intuitively, is as follows: given N readers and M writers, a reader performs, cyclically, non-critical action **NCS** and critical action **READ**, and a writer performs, again cyclically, non-critical action **NCS** and critical action **WRITE**. These readers and writers must be synchronized in such a way that if a writer performs the **WRITE** action it is the only process that performs a critical action, i.e. mutual exclusion is required (**ME**). Furthermore, it is necessary that any request to execute the critical action is eventually granted, i.e. eventual access should hold (**EA**). It is this synchronizer that has to be developed. But before we give the development we formulate an abstract specification for the problem.

The abstract specification of Dijkstra consists of a program, implementing the above readers and writers, and the requirements **ME** and **EA**. In our formalism this will be represented by system \mathcal{S}_0 and requirement W_0 . The development process has four steps: in the first step Dijkstra gives an implementation by a program that produces undesirable deadlocked computations. In our formalism the first implementation is represented by system \mathcal{S}_1 and a requirement W_1 which removes the deadlocked computations. We will prove that \mathcal{S}_1 relatively refines \mathcal{S}_0 with respect to (W_1, W_0) . In the second step Dijkstra uses the split binary semaphore technique to delete the deadlocked computations from the first implementation; he obtains by this technique a second implementation that introduces as undesirable computations new deadlocked ones. In our formalism the second implementation is represented by the system \mathcal{S}_2 and the requirement W_2 that removes the newly introduced deadlocked computations. We will prove that \mathcal{S}_2 relatively refines \mathcal{S}_1 with respect to (W_2, W_1) . These deadlocked computations are deleted in the third step resulting in a third implementation that contains as undesirable computations unnecessarily blocking ones. These computations are not deadlocking computations but only computations that are inefficient because they suspend a reader or writer unnecessarily. In our formalism the third implementation will be represented by the system \mathcal{S}_3 and the requirement W_3 that removes the unnecessarily blocking computations. It is proved that \mathcal{S}_3 relatively refines \mathcal{S}_2 with respect to (W_3, W_2) . In the fourth step, these unnecessarily

blocking computations are deleted and also the resulting implementation is cleaned up. In our formalism the fourth implementation will be represented by system \mathcal{S}_4 and it is proved that \mathcal{S}_4 relatively refines \mathcal{S}_3 with respect to (\mathbf{true}, W_3) , i.e., in the fourth step no further requirements are imposed.

3.2 The abstract specification

Here Dijkstra's strategy [Dij79] is followed and it is shown how the informal approach used there can be formalized.

Dijkstra rewrites the informal specification as follows: as a first step, he describes readers and writers by programs (he assumes that the semantics of these programs is intuitively clear):

$$\begin{aligned} \mathbf{reader}_i^0: & \quad \mathbf{do\ true\ \rightarrow\ NCS;READ\ od} \\ \mathbf{writer}_j^0: & \quad \mathbf{do\ true\ \rightarrow\ NCS;WRITE\ od} \end{aligned}$$

He then combines these programs into one parallel program \mathbf{Syn}^0 . \mathbf{Syn}^0 denotes the abstract specification and is defined as follows:

$$\mathbf{Syn}^0 : \quad \parallel_{i=1}^N \mathbf{reader}_i^0 \parallel \parallel_{j=1}^M \mathbf{writer}_j^0 ,$$

Where $\parallel_{i=1}^N \mathbf{reader}_i^0$ is a notation for the N -fold parallel composition of \mathbf{reader}_i^0 . Finally he formulates an informal requirement to exclude from \mathbf{Syn}^0 the unwanted sequences. This requirement is the same as in the introduction: **ME** and **EA**. The complete abstract specification is thus \mathbf{Syn}^0 plus this requirement.

Each \mathbf{reader}_i^0 and \mathbf{writer}_j^0 is represented respectively by DTL machine specification $\mathcal{S}_{r_i^0}$ and $\mathcal{S}_{w_j^0}$. We will incorporate the requirement **EA** as a liveness requirement in each machine specification. The parallel composition of all the separate machine specifications $\mathcal{S}_0 \triangleq \parallel_{i=1}^N \mathcal{S}_{r_i^0} \parallel \parallel_{j=1}^M \mathcal{S}_{w_j^0}$ then corresponds to \mathbf{Syn}^0 plus **EA**. **ME** will be incorporated as an extra requirement on \mathcal{S}_0 . The following sections will give in detail the machine specifications $\mathcal{S}_{r_i^0}$ and $\mathcal{S}_{w_j^0}$, and the extra requirement W_0 .

3.2.1 Specification $\mathcal{S}_{r_i^0}$

The formal specification $\mathcal{S}_{r_i^0} = (B_{r_i^0}, H_{r_i^0})$ where $H_{r_i^0} \triangleq I_{r_i^0} \wedge \Box T_{r_i^0} \wedge L_{r_i^0}$ and $B_{r_i^0}, I_{r_i^0}, T_{r_i^0}$ and $L_{r_i^0}$ are as follows:

1. **Basis** $B_{r_i^0} \triangleq ((\mathbf{In}_{r_i^0}, \mathbf{Out}_{r_i^0}), (\mathbf{V}_{r_i^0}, \mathbf{X}_{r_i^0}))$

$$\begin{aligned} \mathbf{Out}_{r_i^0} & \triangleq \emptyset, \\ \mathbf{In}_{r_i^0} & \triangleq \emptyset, \\ \mathbf{V}_{r_i^0} & \triangleq \{\mathbf{s}_{r_i}\}, \\ \mathbf{X}_{r_i^0} & \triangleq \emptyset \end{aligned}$$

3.2 The abstract specification

- $s_{r_i} = 0$: reader $_i^0$ is non critical.
- $s_{r_i} = 1$: reader $_i^0$ is critical.

2. Initial States:

$$I_{r_i^0} \triangleq s_{r_i} = 0$$

Reader $_i^0$ starts in the non critical state.

3. Transitions:

$$T_{r_i^0} \triangleq$$

$$\tau_{r_{i,1}^0} \quad (\epsilon = \mathbf{i} \wedge s_{r_i} = 0 \wedge s'_{r_i} = 1)$$

Reader $_i^0$ becomes critical.

$$\tau_{r_{i,2}^0} \quad \vee (\epsilon = \mathbf{i} \wedge s_{r_i} = 1 \wedge s'_{r_i} = 0)$$

Reader $_i^0$ becomes critical.

$$\tau_{r_{i,0}^0} \quad \vee \mathbf{stut}_{r_i^0}$$

These transitions are illustrated in figure 3.1

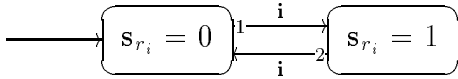


Figure 3.1: Transitions of reader $_i^0$.

4. Liveness

As discussed above $L_{r_i^0}$ should express the EA requirement, i.e., all the transitions are weakly fair.

Let $WF_{r_i^0} \triangleq \{\tau_{r_{i,k}^0} \mid k \in \{1, 2\}\}$ and $SF_{r_i^0} \triangleq \emptyset$ then

$$L_{r_i^0} \triangleq \bigwedge_{\tau \in WF_{r_i^0}} (\diamond \Box En(\tau) \rightarrow \Box \diamond \tau) \wedge \bigwedge_{\tau \in SF_{r_i^0}} (\Box \diamond En(\tau) \rightarrow \Box \diamond \tau)$$

3.2.2 Specification $\mathcal{S}_{w_j^0}$

The formal specification $\mathcal{S}_{w_j^0} = (B_{w_j^0}, H_{w_j^0})$ where $H_{w_j^0} \triangleq I_{w_j^0} \wedge \Box T_{w_j^0} \wedge L_{w_j^0}$ and $B_{w_j^0}$, $I_{w_j^0}$, $T_{w_j^0}$ and $L_{w_j^0}$ are as follows:

1. **Basis** $B_{w_j^0} \triangleq ((\text{In}_{w_j^0}, \text{Out}_{w_j^0}), (V_{w_j^0}, X_{w_j^0}))$

$$\begin{aligned} \text{Out}_{w_j^0} &\triangleq \emptyset, \\ \text{In}_{w_j^0} &\triangleq \emptyset, \\ V_{w_j^0} &\triangleq \{\mathbf{s}_{w_j}\}, \\ X_{w_j^0} &\triangleq \emptyset \end{aligned}$$

- $\mathbf{s}_{w_j} = 0$: writer_j^0 is non critical.
- $\mathbf{s}_{w_j} = 1$: writer_j^0 is critical.

2. **Initial States:**

$$I_{w_j^0} \triangleq \mathbf{s}_{w_j} = 0$$

Writer_j^0 starts in the non critical state.

3. **Transitions:**

$$T_{w_j^0} \triangleq$$

$$\tau_{w_j^0,1} \quad (\epsilon = \mathbf{i} \wedge \mathbf{s}_{w_j} = 0 \wedge \mathbf{s}'_{w_j} = 1)$$

Writer_j^0 becomes critical.

$$\tau_{w_j^0,2} \quad \vee (\epsilon = \mathbf{i} \wedge \mathbf{s}_{w_j} = 1 \wedge \mathbf{s}'_{w_j} = 0)$$

Writer_j^0 becomes non critical.

$$\tau_{w_j^0,0} \quad \vee \mathbf{stut}_{w_j^0}$$

These transitions are illustrated in figure 3.2

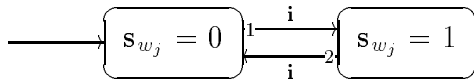


Figure 3.2: Transitions of writer_i^0 .

4. **Liveness**

As discussed above $L_{w_j^0}$ should express the EA requirement, i.e., all the transitions are weakly fair.

Let $\text{WF}_{w_j^0} \triangleq \{\tau_{w_j^0,k} \mid k \in \{1,2\}\}$ and $\text{SF}_{w_j^0} \triangleq \emptyset$ then

$$L_{w_j^0} \triangleq \bigwedge_{\tau \in \text{WF}_{w_j^0}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{w_j^0}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

3.3 The first development step

3.2.3 Requirement W_0

The extra condition on the composed system should express the mutual exclusion property **ME**. A reader (writer) is critical if $\mathbf{s}_{r_i} = 1$ ($\mathbf{s}_{w_j} = 1$). Let $\#(i : 1 \leq i \leq N : \mathbf{s}_{r_i} = 1)$ denote the number of components such that $\mathbf{s}_{r_i} = 1$. The condition is then as follows:

$$W_0 \triangleq \square \left(\#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 0 \vee \right. \\ \left. \#(i : 1 \leq i \leq N : \mathbf{s}_{r_i} = 1) = 0 \wedge \#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 1 \right)$$

As seen in Section 2.5 W_0 should be defined as a machine and a liveness condition in order to apply the proof rule for relative refinement. This can be done quite easily. The liveness condition is **true**. Define p as $\#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 0 \vee (\#(i : 1 \leq i \leq N : \mathbf{s}_{r_i} = 1) = 0 \wedge \#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 1)$ and p' as $\#(j : 1 \leq j \leq M : \mathbf{s}'_{w_j} = 1) = 0 \vee (\#(i : 1 \leq i \leq N : \mathbf{s}'_{r_i} = 1) = 0 \wedge \#(j : 1 \leq j \leq M : \mathbf{s}'_{w_j} = 1) = 1)$. Then $p \wedge \square((p \wedge p') \vee \mathbf{stut}_0)$ is the machine in DTL corresponding to W_0 .

3.3 The first development step

Dijkstra's next step is to translate the informally stated requirement into formal program form, i.e. to transform reader_i^0 and writer_j^0 in such a way that they satisfy the mutual-exclusion requirement **ME**. We discuss this translation informally.

He introduces shared variables **aw** and **ar** and binary semaphore **x**. Shared variable **ar** represents the number of readers which may execute their **READ**, and **aw** represents the number of writers which may execute their **WRITE**. A reader increases **ar** by 1 if it is allowed to execute its **READ** and decreases **ar** by 1 if it is finished with executing its **READ**. Since **ar** will be changed and accessed by several readers, Dijkstra protects the operation of increasing and decreasing **ar** by semaphore operations **P** and **V** on binary semaphore x to ensure that only one reader changes **ar** at a time, i.e. mutual exclusion. The synchronization requirement is brought into reader_i by guarding the increasing operation of **ar** with condition **aw=0**, i.e., the number of writers that may execute their **WRITE** equals zero. The same can be done for writer_j . The initial values of the shared variables are 0 and the initial value of semaphore **x** is 1. This results in the following programs:

```

readeri1:
  do true → NCS;
    P(x); (*) if aw=0 → ar:=ar+1 fi; V(x);
    READ;
    P(x); ar:=ar-1; V(x)
  od

writerj1:
  do true → NCS;
    P(x); (+) if aw=0 ∧ ar=0 → aw:=aw+1 fi; V(x);
    WRITE;
    P(x); aw:=aw-1; V(x)
  od

Syn1 : ||i=1N readeri1 || ||j=1M writerj1

```

This first approximation can deadlock. A deadlocked sequence is for instance:

A writer starts in the initial state and then executes $\text{NCS};\text{P}(\mathbf{x});(+)$, as result of that the value of \mathbf{aw} changes in 1. A reader then executes $\text{NCS};\text{P}(\mathbf{x});(*)$ and blocks in the **if-fi** clause of $(*)$ because $\mathbf{aw}=1$ and the semantics of this **if-fi** is such that when no guard is fulfilled it blocks. Then no reader or writer can then execute $(*)$ or $(+)$ because $\mathbf{x}=0$ and \mathbf{x} holds this value forever. The requirement is thus that these deadlocked sequences are not generated.

Now Syn^1 will be specified in Stark's formalism. Like the abstract specification each reader $_i^1$ and writer $_j^1$ is represented by a separate machine specification $\mathcal{S}_{r_i^1}$ and $\mathcal{S}_{w_j^1}$. The composed system $\mathcal{S}_1 \triangleq \parallel_{i=1}^N \mathcal{S}_{r_i^1} \parallel \parallel_{j=1}^M \mathcal{S}_{w_j^1}$ and corresponds with Syn^1 . For \mathcal{S}_1 the extra requirement W_1 for excluding deadlocked computations is formulated. In the following subsections we give DTL machine specifications $\mathcal{S}_{r_i^1}$ and $\mathcal{S}_{w_j^1}$, and the extra requirement W_1 .

3.3.1 Specification $\mathcal{S}_{r_i^1}$

The formal specification $\mathcal{S}_{r_i^1} \triangleq (B_{r_i^1}, H_{r_i^1})$ where $H_{r_i^1} \triangleq I_{r_i^1} \wedge \square T_{r_i^1} \wedge L_{r_i^1}$ and $B_{r_i^1}, I_{r_i^1}, T_{r_i^1}$ and $L_{r_i^1}$ are as follows:

1. **Basis** $B_{r_i^1} = ((\text{In}_{r_i^1}, \text{Out}_{r_i^1}), (\text{V}_{r_i^1}, \text{X}_{r_i^1}))$

$$\begin{aligned} \text{In}_{r_i^1} &\triangleq \emptyset, \\ \text{Out}_{r_i^1} &\triangleq \emptyset, \\ \text{V}_{r_i^1} &\triangleq \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}\}, \\ \text{X}_{r_i^1} &\triangleq \{\ell_{r_i^1}\} \end{aligned}$$

- $\ell_{r_i^1} = 0$: reader $_i^1$ is non critical.
- $\ell_{r_i^1} = 1$: reader $_i^1$ has passed its first P-operation.
- $\ell_{r_i^1} = 2$: reader $_i^1$ has increased \mathbf{ar} by 1.
- $\ell_{r_i^1} = 3$: reader $_i^1$ is critical.
- $\ell_{r_i^1} = 4$: reader $_i^1$ has passed its second P-operation.
- $\ell_{r_i^1} = 5$: reader $_i^1$ has decreased \mathbf{ar} by 1.

Let $\Psi_1 \triangleq (\mathbf{x}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}, \ell_{r_i^1})$ and $\Psi'_1 \triangleq (\mathbf{x}', \mathbf{ar}', \mathbf{aw}', \mathbf{s}'_{r_i}, \ell'_{r_i^1})$.

2. **Initial States:**

$$I_{r_i^1} \triangleq \Psi_1 = (1, 0, 0, 0, 0)$$

3. **Transitions:**

$$T_{r_i^1} \triangleq$$

3.3 The first development step

$$\tau_{r_i^1,1} \quad \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 0 \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0, 1/\mathbf{x}, \ell_{r_i^1}] \right)$$

Reader_i¹ executes the first P-action.

$$\tau_{r_i^1,2} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 1 \wedge \mathbf{aw} = 0 \wedge \Psi'_1 = \Psi_1 [\mathbf{ar} + 1, 2/\mathbf{ar}, \ell_{r_i^1}] \right)$$

Reader_i¹ can increase the number of active readers by if the number of active writers is zero.

$$\tau_{r_i^1,3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 2 \wedge \Psi'_1 = \Psi_1 [1, 1, 3/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i^1}] \right)$$

Reader_i¹ becomes critical.

$$\tau_{r_i^1,4} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 3 \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0, 4/\mathbf{x}, \ell_{r_i^1}] \right)$$

Reader_i¹ executes its second P-action.

$$\tau_{r_i^1,5} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 4 \wedge \Psi'_1 = \Psi_1 [\mathbf{ar} - 1, 5/\mathbf{ar}, \ell_{r_i^1}] \right)$$

Reader_i¹ decreases the number of active readers by one.

$$\tau_{r_i^1,6} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 5 \wedge \Psi'_1 = \Psi_1 [0, 1, 0/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i^1}] \right)$$

Reader_i¹ becomes non critical.

$$\tau_{r_i^1,7} \quad \vee \left(\epsilon = \mathbf{e} \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0/\mathbf{x}] \right)$$

The environment executes a P-operation on \mathbf{x} .

$$\tau_{r_i^1,8} \quad \vee \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^1} \in \{0, 3\} \wedge \mathbf{x} = 0 \wedge \Psi'_1 = \Psi_1 [1/\mathbf{x}] \right)$$

The environment executes a V-operation on \mathbf{x} .

$$\tau_{r_i^1,0} \quad \vee \mathbf{stut}_{r_i^1}$$

These transitions are illustrated in figure 3.3

4. Liveness:

$L_{r_i^1}$ expresses that the P- and V-operations on the semaphore \mathbf{x} are strongly fair and all the other transitions are weakly fair.

Let $\text{WF}_{r_i^1} \triangleq \{\tau_{r_i^1,k} \mid k \in \{2, 5\}\}$ and $\text{SF}_{r_i^1} \triangleq \{\tau_{r_i^1,k} \mid k \in \{1, 3, 4, 6, 7, 8\}\}$ then

$$L_{r_i^1} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^1}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^1}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

3.3.2 Specification $\mathcal{S}_{w_j^1}$

The formal specification $\mathcal{S}_{w_j^1} \triangleq (B_{w_j^1}, H_{w_j^1})$ where $H_{w_j^1} \triangleq I_{w_j^1} \wedge \square T_{w_j^1} \wedge L_{w_j^1}$ and $B_{w_j^1}, I_{w_j^1}, T_{w_j^1}$ and $L_{w_j^1}$ are as follows:

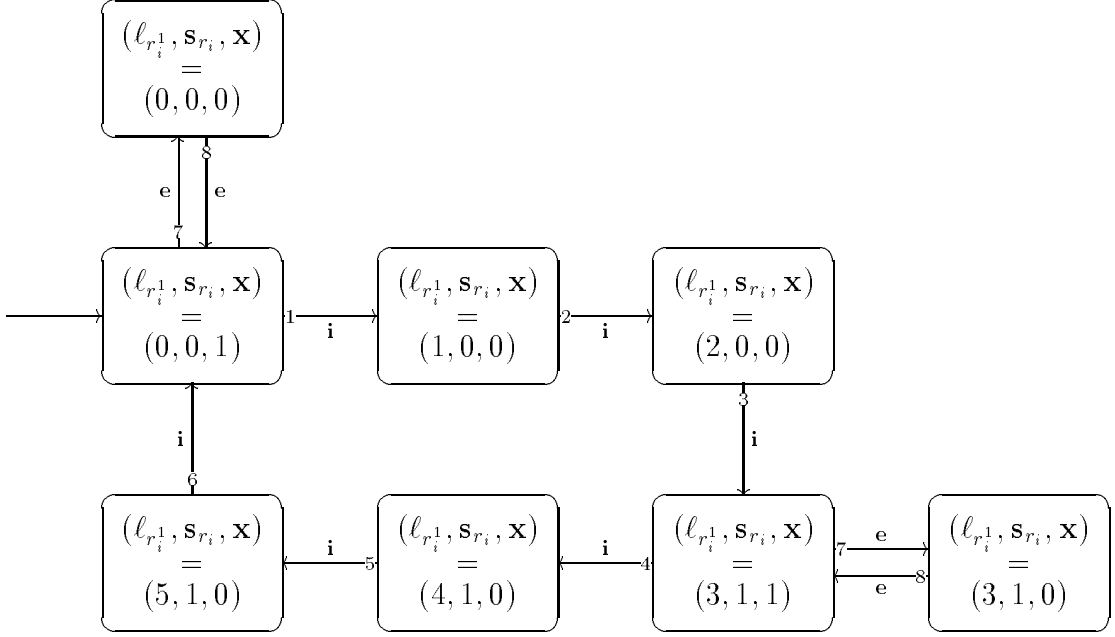
1. **Basis** $B_{w_j^1} = ((\text{In}_{w_j^1}, \text{Out}_{w_j^1}), (\text{V}_{w_j^1}, \text{X}_{w_j^1}))$

$$\text{In}_{w_j^1} \triangleq \emptyset,$$

$$\text{Out}_{w_j^1} \triangleq \emptyset,$$

$$\text{V}_{w_j^1} \triangleq \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{w_j}\},$$

$$\text{X}_{w_j^1} \triangleq \{\ell_{w_j^1}\}$$


 Figure 3.3: Transitions of reader_i¹.

- $\ell_{w_j^1} = 0$: writer_j¹ is non critical.
- $\ell_{w_j^1} = 1$: writer_j¹ has passed its first P-operation.
- $\ell_{w_j^1} = 2$: writer_j¹ has increased **aw** by 1.
- $\ell_{w_j^1} = 3$: writer_j¹ is critical.
- $\ell_{w_j^1} = 4$: writer_j¹ has passed its second P-operation.
- $\ell_{w_j^1} = 5$: writer_j¹ has decreased **aw** by 1.

Let $\Psi_1 \triangleq (\mathbf{x}, \mathbf{ar}, \mathbf{aw}, s_{w_j}, \ell_{w_j^1})$ and $\Psi'_1 \triangleq (\mathbf{x}', \mathbf{ar}', \mathbf{aw}', s'_{w_j}, \ell'_{w_j^1})$.

2. Initial States:

$$I_{w_j^1} \triangleq \Psi_1 = (1, 0, 0, 0)$$

3. Transitions:

$$T_{w_j^1} \triangleq$$

$$\tau_{w_j^1,1} \quad \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 0 \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0, 1/\mathbf{x}, \ell_{w_j^1}] \right)$$

Writer_j¹ executes the first P-action.

$$\tau_{w_j^1,2} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 1 \wedge \mathbf{ar} = 0 \wedge \mathbf{aw} = 0 \wedge \Psi'_1 = \Psi_1 [\mathbf{aw} + 1, 2/\mathbf{aw}, \ell_{w_j^1}] \right)$$

Writer_j¹ can increase the numbers of active writers if the number of active writers and readers is zero.

3.3 The first development step

- $\tau_{w_j^1,3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 2 \wedge \Psi'_1 = \Psi_1 [1, 1, 3/\mathbf{s}_{w_j}, \mathbf{x}, \ell_{w_j^1}] \right)$
 Writer $_j^1$ becomes critical.
- $\tau_{w_j^1,4} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 3 \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0, 4/\mathbf{x}, \ell_{w_j^1}] \right)$
 Writer $_j^1$ executes the second P-action.
- $\tau_{w_j^1,5} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 4 \wedge \Psi'_1 = \Psi_1 [\mathbf{aw} - 1, 5/\mathbf{aw}, \ell_{w_j^1}] \right)$
 Writer $_j^1$ decreases the number of active writers by one.
- $\tau_{w_j^1,6} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^1} = 5 \wedge \Psi'_1 = \Psi_1 [0, 1, 0/\mathbf{s}_{w_j}, \mathbf{x}, \ell_{w_j^1}] \right)$
 Writer j becomes non critical.
- $\tau_{w_j^1,7} \quad \vee \left(\epsilon = \mathbf{e} \wedge \mathbf{x} = 1 \wedge \Psi'_1 = \Psi_1 [0/\mathbf{x}] \right)$
 The environment executes a P-operation on \mathbf{x} .
- $\tau_{w_j^1,8} \quad \vee \left(\epsilon = \mathbf{e} \wedge \ell_{w_j^1} \in \{0, 3\} \wedge \mathbf{x} = 0 \wedge \Psi'_1 = \Psi_1 [1/\mathbf{x}] \right)$
 The environment executes a V-operation on \mathbf{x} .
- $\tau_{w_j^1,0} \quad \vee \mathbf{stut}_{w_j^1}$

These transitions are illustrated in figure 3.4

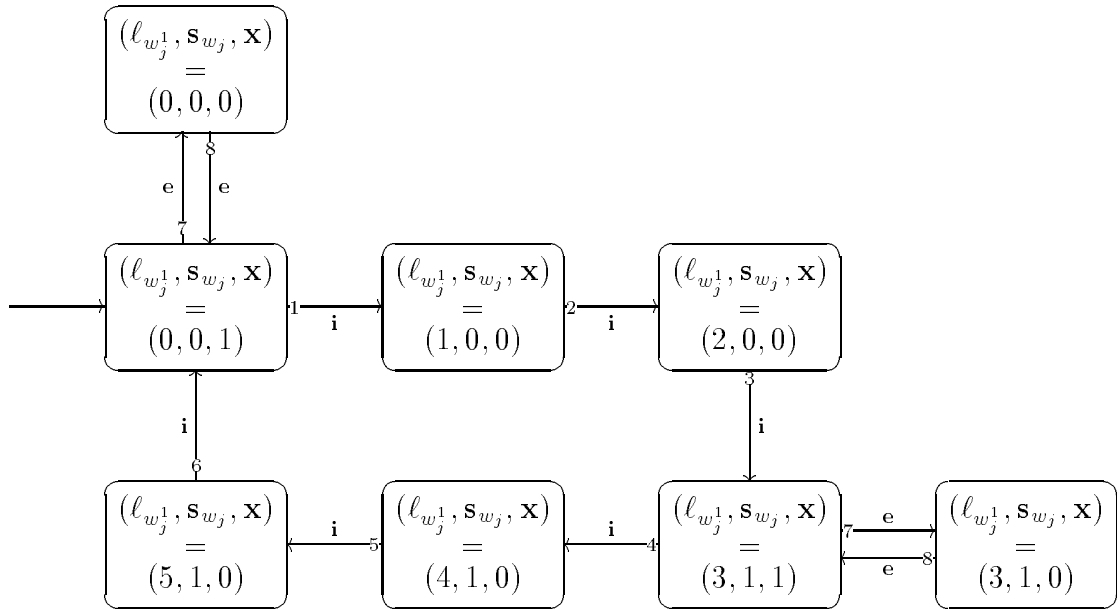


Figure 3.4: Transitions of writer $_j^1$.

4. Liveness

$L_{w_j^1}$ expresses that the P- and V-operations on the semaphore \mathbf{x} are strongly fair and all the other transitions are weakly fair.

Let $\text{WF}_{w_j^1} \triangleq \{\tau_{w_j^1,k} \mid k \in \{2, 5\}\}$ and $\text{SF}_{w_j^1} \triangleq \{\tau_{w_j^1,k} \mid k \in \{1, 3, 4, 6, 7, 8\}\}$ then

$$L_{w_j^1} \triangleq \bigwedge_{\tau \in \text{WF}_{w_j^1}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{w_j^1}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

3.3.3 Requirement W_1

The condition should express that the described deadlocked sequences don't occur, i.e., it when \mathbf{ar} is increased by 1 then $\mathbf{aw} = 0$ and when \mathbf{aw} is increased by 1 then $\mathbf{aw} = 0$ and $\mathbf{ar} = 0$. Formally:

$$W_1 \triangleq \square \left(\left(\bigwedge_{i=1}^N \ell_{r_i^1} = 1 \rightarrow \mathbf{aw} = 0 \right) \wedge \left(\bigwedge_{j=1}^M \ell_{w_j^1} = 1 \rightarrow (\mathbf{ar} = 0 \wedge \mathbf{aw} = 0) \right) \right)$$

This corresponds to the following machine: Let

$$\begin{aligned} p_{ri} &\triangleq \ell_{r_i^1} = 1 \rightarrow \mathbf{aw} = 0 \\ p'_{ri} &\triangleq \ell'_{r_i^1} = 1 \rightarrow \mathbf{aw}' = 0 \\ p_{wj} &\triangleq \ell_{w_j^1} = 1 \rightarrow (\mathbf{ar} = 0 \wedge \mathbf{aw} = 0) \\ p'_{wj} &\triangleq \ell'_{w_j^1} = 1 \rightarrow (\mathbf{ar}' = 0 \wedge \mathbf{aw}' = 0) \end{aligned}$$

then W_1 is the conjunction of the machines $p_{ri} \wedge \square((p_{ri} \wedge p'_{ri}) \vee \text{stut})$ and $p_{wj} \wedge \square((p_{wj} \wedge p'_{wj}) \vee \text{stut})$ for $1 \leq i \leq N$ and $1 \leq j \leq M$.

3.3.4 \mathcal{S}_1 relatively refines \mathcal{S}_0

Since the semaphore \mathbf{x} and the shared variables \mathbf{ar} and \mathbf{aw} are used only by the subcomponents of \mathcal{S}_1 , we should prove $\mathcal{S}_1 \upharpoonright \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}\}$ relatively refines \mathcal{S}_0 instead of \mathcal{S}_1 relatively refines \mathcal{S}_0 . According to definition 35, 36 and theorem 8 $\mathcal{S}_1 \upharpoonright \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}\}$ relatively refines \mathcal{S}_0 with respect to (W_1, W_0) iff the following holds:

$$\begin{aligned} &\mathfrak{D}(B_1) = \mathfrak{D}(B_0) \text{ and} \\ &\models (\exists X_1. (G_1 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{x}, \mathbf{ar}, \mathbf{aw})' = (\mathbf{x}, \mathbf{ar}, \mathbf{aw}))) \rightarrow (\exists X_0. (G_0))) \end{aligned}$$

where X_1 are the local variables from \mathcal{S}_1 , i.e., $X_1 \triangleq \{\ell_{r_i^1} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^1} \mid j = 1, \dots, M\} \cup \{\mathbf{x}, \mathbf{aw}, \mathbf{ar}\}$ and G_1 is the composition of $\mathcal{S}_{r_i^1}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^1}$ ($j = 1, \dots, M$) and W_1 ,

let $\bar{\epsilon}_1 \triangleq \epsilon_{1,1}, \dots, \epsilon_{1,N}, \epsilon_{1,N+1}, \dots, \epsilon_{1,N+M}$, and

let $\bar{B}_1^A \triangleq B_{r_1^1}^A, \dots, B_{r_N^1}^A, B_{w_1^1}^A, \dots, B_{w_M^1}^A$

then $G_1 \triangleq$

$$\left(\exists \bar{\epsilon}_1. \odot_{\bar{B}_1^A} (\epsilon, \bar{\epsilon}_1) \wedge \bigwedge_{i=1}^N \text{H}_{r_i^1} [\epsilon_{1,i}/\epsilon] \wedge \bigwedge_{j=1}^M \text{H}_{w_j^1} [\epsilon_{1,N+j}/\epsilon] \right) \wedge W_1$$

X_0 are the local variables from \mathcal{S}_0 , i.e., $X_0 \triangleq \emptyset$ and G_0 is the composition of $\mathcal{S}_{r_i^0}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^0}$ ($j = 1, \dots, M$) and W_0 ,

3.3 The first development step

let $\bar{\epsilon}_0 \triangleq \epsilon_{1,1}, \dots, \epsilon_{0,N}, \epsilon_{0,N+1}, \dots, \epsilon_{0,N+M}$, and

let $\bar{B}_0^A \triangleq B_{r_1^0}^A, \dots, B_{r_N^0}^A, B_{w_1^0}^A, \dots, B_{w_M^0}^A$

then $G_0 \triangleq$

$$\left(\exists \bar{\epsilon}_0. \odot_{\bar{B}_0^A} (\epsilon, \bar{\epsilon}_0) \wedge \bigwedge_{i=1}^N H_{r_i^0} [\epsilon_{0,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^0} [\epsilon_{0,N+j}/\epsilon] \right) \wedge W_0$$

Since W_0 can't be decomposed into sub-requirements but doesn't constrain the ϵ variables and W_1 can be decomposed into sub-requirements $W_{r_i^1} \triangleq \Box(\ell_{r_i^1} = 1 \rightarrow \mathbf{aw} = 0)$ for reader $_i^1$ and $W_{w_j^1} \triangleq \Box(\ell_{w_j^1} = 1 \rightarrow (\mathbf{aw} = 0 \wedge \mathbf{ar} = 0))$, and doesn't constrain the ϵ variables Lemma 9, 10 and 11 can be used for the proof, i.e., following proof rule can be used

$$\frac{\begin{array}{l} W_1 \subseteq \bigcap_{i=1}^N W_{r_i^1} \bigcap_{j=1}^M W_{w_j^1} \\ \mathcal{S}_{r_i^1} \ W_{r_i^1} \ \mathbf{ref}^{W_0} \ \mathcal{S}_{r_i^0} \qquad W_{r_i^1} \text{ constraining } B_{r_i^1} \\ \mathcal{S}_{w_j^1} \ W_{w_j^1} \ \mathbf{ref}^{W_0} \ \mathcal{S}_{w_j^0} \qquad W_{w_j^1} \text{ constraining } B_{w_j^1} \end{array}}{\mathcal{S}_1 \ W_1 \ \mathbf{ref}^{W_0} \ \mathcal{S}_0}$$

This means we have to prove for $i = 1, \dots, N$ and $j = 1, \dots, M$:

- (1) $(\exists \ell_{r_i^1}. (H_{r_i^1} \wedge W_{r_i^1})) \rightarrow H_{r_i^0} \wedge W_0$
- (2) $(\exists \ell_{w_j^1}. (H_{w_j^1} \wedge W_{w_j^1})) \rightarrow H_{w_j^0} \wedge W_0$
- (3) $W_1 \rightarrow (W_{r_i^1} \wedge W_{w_j^1})$

ad (1) Rule 3 will be used to prove (1). This means one has to prove

- (a) $\mathcal{S}_1 \cap \text{Hist}(W_1) \models (I_{r_i^1} \wedge p_{ri}) \rightarrow I_{r_i^0} \wedge p$
- (b) $\mathcal{S}_1 \cap \text{Hist}(W_1) \models T_{r_i^1} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^1}) \rightarrow T_{r_i^0} \wedge ((p \wedge p') \vee \mathbf{stut}_0)$
- (c) $\mathcal{S}_1 \cap \text{Hist}(W_1) \models L_{r_i^0}$

(a) **Proof 1**

$$\begin{aligned} & I_{r_i^1} \wedge p_{ri} \\ = & \quad \% \text{ Def. } I_{r_i^1}, p_{ri} \\ & (x, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}, \ell_{r_i^1}) = (1, 0, 0, 0, 0) \wedge (\ell_{r_i^1} \rightarrow \mathbf{aw} = 0) \\ \rightarrow & \quad \% \quad 0 \leq \#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) \leq \mathbf{aw} \\ & \quad \quad \quad 0 \leq \#(i : 1 \leq j \leq M : \mathbf{s}_{r_i} = 1) \leq \mathbf{ar} \\ & \mathbf{s}_{r_i} = 0 \\ & \wedge (\#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 0 \\ & \quad \vee (\#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) = 1 \wedge \#(i : 1 \leq j \leq M : \mathbf{s}_{r_i} = 1) = 0)) \\ = & \quad \% \text{ Def. } I_{r_i^0}, p \\ & I_{r_i^0} \wedge p \end{aligned}$$

(b) **Proof 2**

Since $T_{r_i^1}$ is of the form $\mathbf{stut}_{r_i^1} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \text{trans}_{\tau})$ then $T_{r_i^1} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^1})$ is equal to $\mathbf{stut}_{r_i^1} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \text{trans}_{\tau} \wedge p_{ri} \wedge p'_{ri})$. $T_{r_i^0}$ is of the form $\mathbf{stut}_{r_i^0} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \text{trans}_{\tau})$ so $T_{r_i^0} \wedge ((p \wedge p') \vee \mathbf{stut}_0)$ is equal to $\mathbf{stut}_{r_i^0} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \text{trans}_{\tau} \wedge p \wedge p')$.

- $$\begin{aligned}
 & - \quad \tau_{r_i,1}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 0 \wedge p_{ri} \wedge \mathbf{x} = 1 \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 1/\mathbf{x}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,2}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 1 \wedge p_{ri} \wedge \mathbf{aw} = 0 \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [\mathbf{ar} + 1, 2/\mathbf{ar}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,3}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 2 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1, 1, 3/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \left(\epsilon = \mathbf{i} \wedge \mathbf{s}_{r_i} = 0 \wedge p \wedge p' \wedge \mathbf{s}'_{r_i} = 1 \right) \\
 & \quad = \tau_{r_i,1}^0 \wedge p \wedge p' \\
 & \quad \text{because } \ell_{r_i}^1 = 1 \rightarrow \mathbf{aw} = 0 \text{ and } 0 \leq \#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) \leq \mathbf{aw} \text{ and} \\
 & \quad 0 \leq \#(i : 1 \leq i \leq N : \mathbf{s}_{r_i} = 1) \leq \mathbf{ar}.
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,4}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 3 \wedge \mathbf{x} = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 4/\mathbf{x}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,5}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 4 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [\mathbf{ar} - 1, 5/\mathbf{ar}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,6}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad \left(\epsilon = \mathbf{i} \wedge \ell_{r_i}^1 = 5 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 1, 0/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i}^1] \right) \\
 & \quad \rightarrow \left(\epsilon = \mathbf{i} \wedge \mathbf{s}_{r_i} = 1 \wedge p \wedge p' \wedge \mathbf{s}'_{r_i} = 0 \right) \\
 & \quad = \tau_{r_i,2}^0 \wedge p \wedge p' \\
 & \quad \text{because } 0 \leq \#(j : 1 \leq j \leq M : \mathbf{s}_{w_j} = 1) \leq \mathbf{aw} \text{ and } 0 \leq \#(i : 1 \leq i \leq N : \\
 & \quad \mathbf{s}_{r_i} = 1) \leq \mathbf{ar}.
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,7}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{e} \wedge \mathbf{x} = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0/\mathbf{x}] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \tau_{r_i,8}^1 \wedge p_{ri} \wedge p'_{ri} \\
 & \quad = \left(\epsilon = \mathbf{e} \wedge \ell_{r_i}^1 \in \{0, 3\} \wedge \mathbf{x} = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1/\mathbf{x}] \right) \\
 & \quad \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$
- $$\begin{aligned}
 & - \quad \mathbf{stut}_{r_i^1} \rightarrow \mathbf{stut}_{r_i^0} \\
 & \quad \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$

3.4 The second development step

$$\begin{aligned}
(c) \quad & L_{r_i^0} \\
&= (\diamond \square \text{En}(\tau_{r_{i,1}^0}) \rightarrow \square \diamond \tau_{r_{i,1}^0}) \wedge (\diamond \square \text{En}(\tau_{r_{i,2}^0}) \rightarrow \square \diamond \tau_{r_{i,2}^0}) \\
&= (\diamond \square (\epsilon = \mathbf{i} \wedge \mathbf{s}_{r_i} = 0) \rightarrow \square \diamond ((\epsilon = \mathbf{i} \wedge \mathbf{s}_{r_i} = 0 \wedge \mathbf{s}'_{r_i} = 1)))
\end{aligned}$$

From the fact that (see Figure 3.3) transitions $\tau_{r_{i,1}^1}$ and $\tau_{r_{i,3}^1}$ are strongly fair and $\tau_{r_{i,2}^1}$ is weakly fair follows that $(\diamond \square \text{En}(\tau_{r_{i,1}^0}) \rightarrow \square \diamond \tau_{r_{i,1}^0})$, i.e., $\tau_{r_{i,1}^0}$ is weakly fair. From the fact that (see Figure 3.3) transitions $\tau_{r_{i,4}^1}$ and $\tau_{r_{i,6}^1}$ are strongly fair and $\tau_{r_{i,5}^1}$ is weakly fair follows that $(\diamond \square \text{En}(\tau_{r_{i,2}^0}) \rightarrow \square \diamond \tau_{r_{i,2}^0})$, i.e., $\tau_{r_{i,2}^0}$ is weakly fair.

ad (2) Analogue to the proof of (1).

ad (3) This is trivial because $W_1 \leftrightarrow (\bigwedge_{i=1}^N W_{r_i^1} \wedge \bigwedge_{j=1}^M W_{w_j^1})$.

3.4 The second development step

As seen in section 3.3 the first implementation can generate deadlocked sequences. In this step we change the components of the first implementation in such a way that deadlock inside a PV-section is not possible anymore. This is the same as is done by Dijkstra: he massages reader_i^1 and writer_j^1 into reader_j^2 and writer_j^2 so that no deadlocked sequences inside a PV-section are generated any more.

One such deadlocked sequence generated by the first implementation is as follows: suppose reader_i^1 has gained the access-right for the shared variables (first PV-segment) and suppose $\mathbf{aw} = 1$ (a writer is executing WRITE). Then reader_i^1 can never increase \mathbf{ar} by 1, i.e., reader_i^1 has deadlocked.

Dijkstra uses the split binary semaphore technique to prevent programs from becoming deadlocked inside a PV-section. The idea is that we must prevent programs from getting the access-right (get into a PV-section) for the shared variables if we know that they can not give it back (get deadlocked inside a PV-section). For reader_i^1 this means: never let it enter the first PV-section if \mathbf{aw} does not equal zero. For writer_j^1 this means: never let it enter the first PV-section if \mathbf{aw} or \mathbf{ar} does not equal zero. Reader_i^1 and writer_j^1 never block in their second PV-section.

How does one prevent that reader_i^1 gets deadlocked inside a PV-section? This is done as follows: reader_i^1 chooses, when it gives the access-right back, who can have it thereafter. reader_i^1 executes therefore the following piece of program as replacement for $V(\mathbf{mx})$:

CHOOSE: **if true** $\rightarrow V(\mathbf{m})$ **fi** \square **aw=0** $\rightarrow V(\mathbf{r})$ \square **aw=0** \wedge **ar=0** $\rightarrow V(\mathbf{w})$ **fi**

We have to split semaphore \mathbf{mx} in three pieces. If \mathbf{aw} equals zero then a reader is allowed to enter its first PV-section, i.e., this PV-section is not guarded by $P(\mathbf{mx})$ but by $P(\mathbf{r})$. We do this substitution for all PV-sections of reader_i^1 and writer_j^1 . So we have replaced \mathbf{mx} by three other binary semaphores.

What is the initial value of these semaphores? If they all have initial value 1 then more than one program can have access-right to the shared variables, i.e., only one has initial

value 1. Semaphore \mathbf{r} can not have initial value 1 because if no reader wants to execute **READ** then no writer can execute **WRITE**. The same holds for semaphore \mathbf{w} . Thus \mathbf{m} has initial value 1. But then no reader or writer can enter the first PV-section. The solution of this problem is that we insert a PV-section ($\mathbf{P}(\mathbf{m}); \mathbf{CHOOSE}$) at front of the first one. This is in short what Dijkstra does to prevent that reader $_i^1$ and writer $_j^1$ get deadlocked inside a PV-section. The result of this transformation is:

```

reader $_i^2$ :
    do true  $\rightarrow$  NCS;
        P(m); CHOOSE;
        P(r); ar := ar + 1; CHOOSE;
        READ;
        P(m); ar := ar - 1; CHOOSE
    od

writer $_j^2$ :
    do true  $\rightarrow$  NCS;
        P(m); CHOOSE;
        P(w); aw := aw + 1; CHOOSE;
        WRITE;
        P(m); aw := aw - 1; CHOOSE
    od

Syn $^2$  :  $\parallel_{i=1}^N$  reader $_i^2$   $\parallel$   $\parallel_{j=1}^M$  writer $_j^2$ 

```

Syn 2 generates no sequences that can deadlock inside a PV-section. But Syn 2 can generate sequences that can deadlock outside these sections, e.g. initially a reader $_i^2$ can choose for a $\mathbf{V}(\mathbf{w})$ operation, and get blocked by a $\mathbf{P}(\mathbf{r})$ operation. Then no other reader or writer can enter the first PV-section because semaphore \mathbf{m} equals zero.

In the following sections the DTL machine specifications $\mathcal{S}_{r_i^2}$ (corresponding to program reader $_i^2$) and $\mathcal{S}_{w_j^2}$ (corresponding to program writer $_j^2$), and the extra requirement \mathbf{W}_2 , excluding computations that deadlock outside PV-sections, are given.

3.4.1 Specification $\mathcal{S}_{r_i^2}$

The formal specification $\mathcal{S}_{r_i^2} \triangleq (B_{r_i^2}, H_{r_i^2})$ where $H_{r_i^2} \triangleq I_{r_i^2} \wedge \square T_{r_i^2} \wedge L_{r_i^2}$ and $B_{r_i^2}, I_{r_i^2}, T_{r_i^2}$ and $L_{r_i^2}$ are as follows:

1. **Basis** $B_{r_i^2} = ((\text{In}_{r_i^2}, \text{Out}_{r_i^2}), (\text{V}_{r_i^2}, \text{X}_{r_i^2}))$

$$\begin{aligned}
 \text{In}_{r_i^2} &\triangleq \emptyset, \\
 \text{Out}_{r_i^2} &\triangleq \emptyset, \\
 \text{V}_{r_i^2} &\triangleq \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}\}, \\
 \text{X}_{r_i^2} &\triangleq \{\ell_{r_i^2}\}
 \end{aligned}$$

- $\ell_{r_i^2} = 0$: reader $_i^2$ is non critical.

3.4 The second development step

- $\ell_{r_i^2} = 6$: reader $_i^2$ has executed first P-action on \mathbf{m} .
- $\ell_{r_i^2} = 7$: reader $_i^2$ has executed first CHOOSE.
- $\ell_{r_i^2} = 1$: reader $_i^2$ has executed P-action on \mathbf{r} .
- $\ell_{r_i^2} = 2$: reader $_i^2$ has increased \mathbf{ar} by 1.
- $\ell_{r_i^2} = 3$: reader $_i^2$ is critical.
- $\ell_{r_i^2} = 4$: reader $_i^2$ has executed second P-action on \mathbf{m} .
- $\ell_{r_i^2} = 5$: reader $_i^2$ has decreased \mathbf{ar} by 1.

Let $\Psi_2 \triangleq (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}, \ell_{r_i^2})$ and $\Psi'_2 \triangleq (\mathbf{m}', \mathbf{r}', \mathbf{w}', \mathbf{ar}', \mathbf{aw}', \mathbf{s}'_{r_i}, \ell'_{r_i^2})$.

2. Initial States:

$$I_{r_i^2} \triangleq \Psi_2 = (1, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\begin{aligned} \text{Let } CHO(X) \triangleq & \vee \Psi'_2 = \Psi_2 [1, X/\mathbf{m}, \ell_{r_i^2}] \\ & \vee (\mathbf{aw} = 0 \wedge \Psi'_2 = \Psi_2 [1, X/\mathbf{r}, \ell_{r_i^2}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \Psi'_2 = \Psi_2 [1, X/\mathbf{w}, \ell_{r_i^2}]) \end{aligned}$$

$$T_{r_i^2} \triangleq$$

$$\tau_{r_{i,1}^2} \quad (\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{m}) = (0, 1) \wedge \Psi'_2 = \Psi_2 [0, 6/\mathbf{m}, \ell_{r_i^2}])$$

Reader $_i^2$ executes its first P-action on \mathbf{m} .

$$\tau_{r_{i,2}^2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 6 \wedge CHO(7))$$

Reader $_i^2$ executes the first CHOOSE.

$$\tau_{r_{i,3}^2} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{r}) = (7, 1) \wedge \Psi'_2 = \Psi_2 [0, 1/\mathbf{r}, \ell_{r_i^2}])$$

Reader $_i^2$ executes P-action on \mathbf{r} .

$$\tau_{r_{i,4}^2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 1 \wedge \Psi'_2 = \Psi_2 [\mathbf{ar} + 1, 2/\mathbf{ar}, \ell_{r_i^2}])$$

Reader $_i^2$ increases the number of active readers by one.

$$\tau_{r_{i,5}^2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 2 \wedge CHO(3) \wedge \Psi'_2 = \Psi_2 [1/\mathbf{s}_{r_i}])$$

Reader $_i^2$ becomes critical.

$$\tau_{r_{i,6}^2} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{m}) = (3, 1) \wedge \Psi'_2 = \Psi_2 [0, 4/\mathbf{m}, \ell_{r_i^2}])$$

Reader $_i^2$ executes the second P-action on \mathbf{m} .

$$\tau_{r_{i,7}^2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 4 \wedge \Psi'_2 = \Psi_2 [\mathbf{ar} - 1, 5/\mathbf{ar}, \ell_{r_i^2}])$$

Reader $_i^2$ decreases the number of active readers by one.

$$\tau_{r_{i,8}^2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 5 \wedge CHO(0) \wedge \Psi'_2 = \Psi_2 [0/\mathbf{s}_{r_i}])$$

Reader $_i^2$ becomes non critical.

- $\tau_{r_i^2,9} \quad \vee \left(\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{m}] \right)$
 The environment executes a P-operation on \mathbf{m} .
- $\tau_{r_i^2,10} \quad \vee \left(\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{r}] \right)$
 The environment executes a P-operation on \mathbf{r} .
- $\tau_{r_i^2,11} \quad \vee \left(\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{w}] \right)$
 The environment executes a P-operation on \mathbf{w} .
- $\tau_{r_i^2,12} \quad \vee \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{m}] \right)$
 The environment executes a V-operation on \mathbf{m} .
- $\tau_{r_i^2,13} \quad \vee \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{r}] \right)$
 The environment executes a V-operation on \mathbf{r} .
- $\tau_{r_i^2,14} \quad \vee \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{w}] \right)$
 The environment executes a V-operation on \mathbf{w} .
- $\tau_{r_i^2,0} \quad \vee \mathbf{stut}_{r_i^2}$

These transitions are illustrated in figure 3.5

4. Liveness

$L_{r_i^2}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are strongly fair and all the other transitions are weakly fair.

Let $\text{WF}_{r_i^2} \triangleq \{\tau_{r_i^2,k} \mid k \in \{4, 7\}\}$ and

$\text{SF}_{r_i^2} \triangleq \{\tau_{r_i^2,k} \mid k \in \{1, 2, 3, 5, 6, 8, 9, 10, 11, 12, 13, 14\}\}$ then

$$L_{r_i^2} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^2}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^2}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

3.4.2 Specification $\mathcal{S}_{w_j^2}$

The formal specification $\mathcal{S}_{w_j^2} \triangleq (B_{w_j^2}, H_{w_j^2})$ where $H_{w_j^2} \triangleq I_{w_j^2} \wedge \square T_{w_j^2} \wedge L_{w_j^2}$ and $B_{w_j^2}$, $I_{w_j^2}$, $T_{w_j^2}$ and $L_{w_j^2}$ are as follows:

1. **Basis** $B_{w_j^2} = ((\text{In}_{w_j^2}, \text{Out}_{w_j^2}), (\text{V}_{w_j^2}, \text{X}_{w_j^2}))$

$$\begin{aligned}
 \text{In}_{w_j^2} &\triangleq \emptyset, \\
 \text{Out}_{w_j^2} &\triangleq \emptyset, \\
 \text{V}_{w_j^2} &\triangleq \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{w_j}\}, \\
 \text{X}_{w_j^2} &\triangleq \{\ell_{w_j^2}\}
 \end{aligned}$$

- $\ell_{w_j^2} = 0$: writer_j^2 is non critical.
- $\ell_{w_j^2} = 6$: writer_j^2 has executed first P-action on \mathbf{m} .

3.4 The second development step

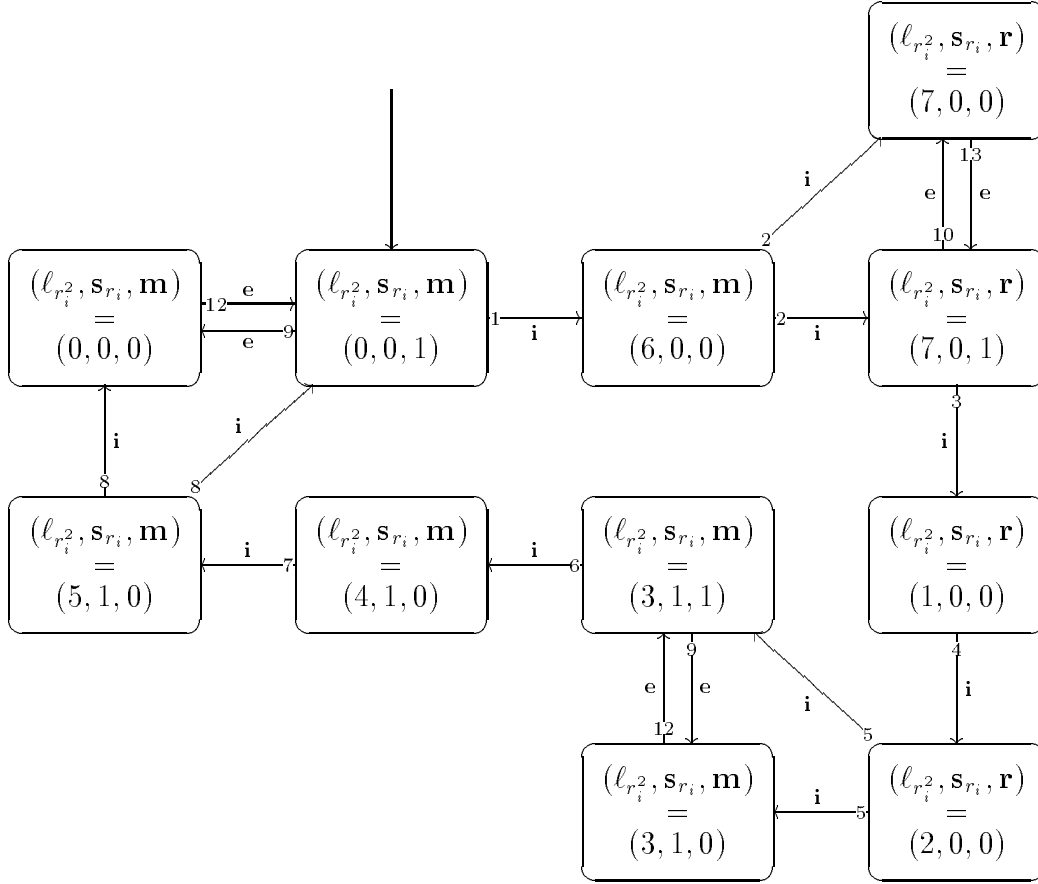


Figure 3.5: Transitions of reader_i².

- $\ell_{w_j^2} = 7$: writer_i² has executed first CHOOSE.
- $\ell_{w_j^2} = 1$: writer_i² has executed P-action on **w**.
- $\ell_{w_j^2} = 2$: writer_i² has increased **aw** by 1.
- $\ell_{w_j^2} = 3$: writer_i² is critical.
- $\ell_{w_j^2} = 4$: writer_i² has executed second P-action on **m**.
- $\ell_{w_j^2} = 5$: writer_i² has decreased **aw** by 1.

Let $\Psi_2 \triangleq (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, s_{w_j}, \ell_{w_j^2})$ and $\Psi_2' \triangleq (\mathbf{m}', \mathbf{r}', \mathbf{w}', \mathbf{ar}', \mathbf{aw}', s'_{w_j}, \ell'_{w_j^2})$.

2. Initial States:

$$I_{w_j^2} \triangleq \Psi_2 = (1, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\begin{aligned} \text{Let } CHO(X) \triangleq & \vee \Psi'_2 = \Psi_2 [1, X/\mathbf{m}, \ell_{w_j^2}] \\ & \vee (\mathbf{aw} = 0 \wedge \Psi'_2 = \Psi_2 [1, X/\mathbf{r}, \ell_{w_j^2}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \Psi'_2 = \Psi_2 [1, X/\mathbf{w}, \ell_{w_j^2}]) \end{aligned}$$

$$\mathbb{T}_{w_j^2} \triangleq$$

$$\tau_{w_j^2,1} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{w_j^2}, \mathbf{m}) = (0, 1) \wedge \Psi'_2 = \Psi_2 [0, 6/\mathbf{m}, \ell_{w_j^2}])$$

Writer_j² executes its first P-action on \mathbf{m} .

$$\tau_{w_j^2,2} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^2} = 6 \wedge CHO(7))$$

Writer_j² executes the first CHOOSE.

$$\tau_{w_j^2,3} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{w_j^2}, \mathbf{w}) = (7, 1) \wedge \Psi'_2 = \Psi_2 [0, 1/\mathbf{w}, \ell_{w_j^2}])$$

Writer_j² executes P-action on \mathbf{w} .

$$\tau_{w_j^2,4} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^2} = 1 \wedge \Psi'_2 = \Psi_2 [\mathbf{aw} + 1, 2/\mathbf{aw}, \ell_{w_j^2}])$$

Writer_j² increases the number of active writers by one.

$$\tau_{w_j^2,5} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^2} = 2 \wedge CHO(3) \wedge \Psi'_2 = \Psi_2 [1/\mathbf{s}_{w_j}])$$

Writer_j² becomes critical.

$$\tau_{w_j^2,6} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{w_j^2}, \mathbf{m}) = (3, 1) \wedge \Psi'_2 = \Psi_2 [0, 4/\mathbf{m}, \ell_{w_j^2}])$$

Writer_j² executes the second P-action on \mathbf{m} .

$$\tau_{w_j^2,7} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^2} = 4 \wedge \Psi'_2 = \Psi_2 [\mathbf{aw} - 1, 5/\mathbf{aw}, \ell_{w_j^2}])$$

Writer_j² decreases the number of active writers by one.

$$\tau_{w_j^2,8} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^2} = 5 \wedge CHO(0) \wedge \Psi'_2 = \Psi_2 [0/\mathbf{s}_{w_j}])$$

Writer_j² becomes non critical.

$$\tau_{w_j^2,9} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{m}])$$

The environment executes a P-operation on \mathbf{m} .

$$\tau_{w_j^2,10} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{r}])$$

The environment executes a P-operation on \mathbf{r} .

$$\tau_{w_j^2,11} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{w}])$$

The environment executes a P-operation on \mathbf{w} .

$$\tau_{w_j^2,12} \quad \vee (\epsilon = \mathbf{e} \wedge \ell_{w_j^2} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{m}])$$

The environment executes a V-operation on \mathbf{m} .

$$\tau_{w_j^2,13} \quad \vee (\epsilon = \mathbf{e} \wedge \ell_{w_j^2} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{r}])$$

The environment executes a V-operation on \mathbf{r} .

$$\tau_{w_j^2,14} \quad \vee (\epsilon = \mathbf{e} \wedge \ell_{w_j^2} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_2 = \Psi_2 [1/\mathbf{w}])$$

The environment executes a V-operation on \mathbf{w} .

3.4 The second development step

$$\tau_{w_j^2,0} \quad \vee \quad \mathbf{stut}_{w_j^2}$$

These transitions are illustrated in figure 3.6

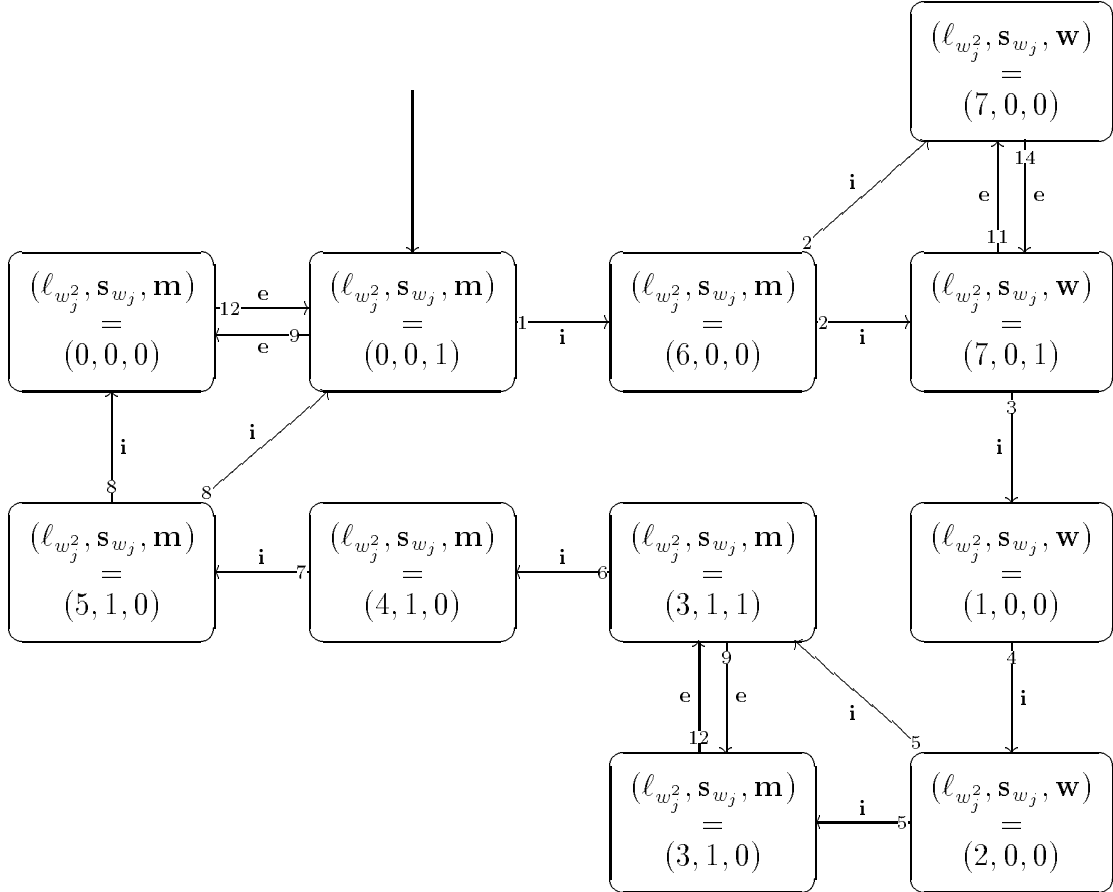


Figure 3.6: Transitions of $writer_j^2$.

4. Liveness:

$L_{w_j^2}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are strongly fair and all the other transitions are weakly fair.

Let $WF_{w_j^2} \triangleq \{\tau_{w_j^2,k} \mid k \in \{4,7\}\}$ and

$SF_{w_j^2} \triangleq \{\tau_{w_j^2,k} \mid k \in \{1,2,3,5,6,8,9,10,11,12,13,14\}\}$ then

$$L_{w_j^2} \triangleq \bigwedge_{\tau \in WF_{w_j^2}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{w_j^2}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

3.4.3 Requirement W_2

W_2 should express that $reader_i^2$ and $writer_j^2$ executes CHOOSE in such a way that no dead-locked computations are generated, i.e.,

- a $V(\mathbf{m})$ is executed if the number of readers and writers that are bound to execute a $P(\mathbf{m})$ is greater than zero,
- a $V(\mathbf{r})$ is executed if the number of readers that are bound to execute a $P(\mathbf{r})$ is greater than zero,
- a $V(\mathbf{w})$ is executed if the number of writers that are bound to execute a $P(\mathbf{w})$ is greater than zero.

Let q be defined as

$$\begin{aligned} & (\mathbf{m} = 1 \wedge \#(k : 1 \leq k \leq N : \ell_{r_k^2} \in \{0, 1, 2, 3, 4, 5\}) + \\ & \quad \#(n : 1 \leq n \leq M : \ell_{w_n^2} \in \{0, 1, 2, 3, 4, 5\}) > 0) \\ \vee & (\mathbf{r} = 1 \wedge \mathbf{aw} = 0 \wedge \#(k : 1 \leq k \leq N : \ell_{r_k^2} \in \{6, 7\}) > 0) \\ \vee & (\mathbf{w} = 1 \wedge \mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \#(n : 1 \leq n \leq M : \ell_{w_n^2} \in \{6, 7\}) > 0) \end{aligned}$$

Then W_2 is as follows

$$W_2 \triangleq \square \left(\left(\bigwedge_{i=1}^N \ell_{r_i^2} \in \{0, 7, 3\} \rightarrow q \right) \wedge \left(\bigwedge_{j=1}^M \ell_{w_j^2} \in \{0, 7, 3\} \rightarrow q \right) \right)$$

The same construction as in the previous development step is used to write this down as a machine. Let $p_2 \triangleq (\bigwedge_{i=1}^N \ell_{r_i^2} \in \{0, 7, 3\} \rightarrow q) \wedge (\bigwedge_{j=1}^M \ell_{w_j^2} \in \{0, 7, 3\} \rightarrow q)$ then $W_2 = p_2 \wedge \square((p_2 \wedge p_2') \vee \mathbf{stut}_2)$. Again the liveness part of W_2 equals **true**.

3.4.4 \mathcal{S}_2 relatively refines \mathcal{S}_1

Since the semaphore \mathbf{x} and the shared variables \mathbf{ar} and \mathbf{aw} are used only by the sub-components of \mathcal{S}_1 and the semaphores \mathbf{m} , \mathbf{w} and \mathbf{r} and the shared variables \mathbf{ar} and \mathbf{aw} only by the sub-components of \mathcal{S}_2 , we should prove $\mathcal{S}_2 \uparrow \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}\}$ relatively refines $\mathcal{S}_1 \uparrow \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}\}$. According to definition 35, 36 and theorem 8 $\mathcal{S}_2 \uparrow \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}\}$ relatively refines $\mathcal{S}_1 \uparrow \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}\}$ with respect to (W_2, W_1) iff the following holds:

$$\begin{aligned} & \mathfrak{D}(B_2) = \mathfrak{D}(B_1) \text{ and} \\ & \models (\exists X_2. (G_2 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw})' = (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw})))) \\ & \rightarrow \\ & (\exists X_1. (G_1 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{x}, \mathbf{ar}, \mathbf{aw})' = (\mathbf{x}, \mathbf{ar}, \mathbf{aw})))) \end{aligned}$$

where X_2 are the local variables from \mathcal{S}_2 , i.e., $X_2 \triangleq \{\ell_{r_i^2} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^2} \mid j = 1, \dots, M\} \cup \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{aw}, \mathbf{ar}\}$ and G_2 is the composition of $\mathcal{S}_{r_i^2}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^2}$ ($j = 1, \dots, M$) and W_2 ,

let $\bar{\epsilon}_2 \triangleq \epsilon_{2,1}, \dots, \epsilon_{2,N}, \epsilon_{2,N+1}, \dots, \epsilon_{2,N+M}$, and

let $\bar{B}_2^A \triangleq B_{r_1^2}^A, \dots, B_{r_N^2}^A, B_{w_1^2}^A, \dots, B_{w_M^2}^A$

then $G_2 \triangleq$

$$\left(\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A} (\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N H_{r_i^2} [\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^2} [\epsilon_{2,N+j}/\epsilon] \right) \wedge W_2$$

3.4 The second development step

X_1 are the local variables from \mathcal{S}_1 , i.e., $X_1 \triangleq \{\ell_{r_i^1} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^1} \mid j = 1, \dots, M\} \cup \{\mathbf{x}, \mathbf{ar}, \mathbf{aw}\}$ and G_1 is the composition of $\mathcal{S}_{r_i^1}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^1}$ ($j = 1, \dots, M$) and W_1 ,

let $\bar{\epsilon}_1 \triangleq \epsilon_{1,1}, \dots, \epsilon_{1,N}, \epsilon_{1,N+1}, \dots, \epsilon_{1,N+M}$, and

let $\bar{B}_1^A \triangleq B_{r_1^1}^A, \dots, B_{r_N^1}^A, B_{w_1^1}^A, \dots, B_{w_M^1}^A$

then $G_1 \triangleq$

$$\left(\exists \bar{\epsilon}_1. \odot_{\bar{B}_1^A} (\epsilon, \bar{\epsilon}_1) \wedge \bigwedge_{i=1}^N H_{r_i^1} [\epsilon_{1,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^1} [\epsilon_{1,N+j}/\epsilon] \right) \wedge W_1$$

As seen in the previous development step W_1 is ϵ -free and can be decomposed into sub-requirements $W_{r_i^1}$ and $W_{w_j^1}$ ($i = 1, \dots, N$ and $j = 1, \dots, M$). W_2 however can't be decomposed into sub-requirements but it is ϵ -free. Now Lemma 9, 10 and 11 can be used for the proof, i.e., following proof rule can be used

$$\frac{\begin{array}{l} \bigotimes_{i=1}^N H_{r_i^1} \otimes \bigotimes_{j=1}^M H_{w_j^1} \cap W_2 \subseteq \\ \bigotimes_{i=1}^N (H_{r_i^1} \cap W_2) \otimes \bigotimes_{j=1}^M (H_{w_j^1} \cap W_2) \\ \bigcap_{i=1}^N W_{r_i^1} \cap \bigcap_{j=1}^M W_{w_j^1} \subseteq W_1 \\ \mathcal{S}_{r_i^2} W_2 \text{ ref }^{W_{r_i^1}} \mathcal{S}_{r_i^1} \\ \mathcal{S}_{w_j^2} W_2 \text{ ref }^{W_{w_j^1}} \mathcal{S}_{w_j^1} \end{array}}{\mathcal{S}_2 W_2 \text{ ref }^{W_1} \mathcal{S}_1} \quad \begin{array}{l} W_{r_i^1} \text{ constraining } B_{r_i^1} \\ W_{w_j^1} \text{ constraining } B_{w_j^1} \end{array}$$

This means we have to prove for $i = 1, \dots, N$ and $j = 1, \dots, M$:

- (1) $(\exists X_{r_i^2}. (H_{r_i^2} \wedge W_2)) \rightarrow (\exists X_{r_i^1}. (H_{r_i^1} \wedge W_{r_i^1}))$
- (2) $(\exists X_{w_j^2}. (H_{w_j^2} \wedge W_2)) \rightarrow (\exists X_{w_j^1}. (H_{w_j^1} \wedge W_{w_j^1}))$
- (3) $(W_{r_i^1} \wedge W_{w_j^1}) \rightarrow W_1$
- (4) $(\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A} (\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N H_{r_i^2} [\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^2} [\epsilon_{2,N+j}/\epsilon]) \wedge W_2$
 \rightarrow
 $(\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A} (\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N (H_{r_i^2} \wedge W_2) [\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M (H_{w_j^2} \wedge W_2) [\epsilon_{2,N+j}/\epsilon])$

ad (1) Rule 3 will be used to prove (1). This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_2 to \mathcal{S}_1 , defined as: $\bar{f} = f_x, f_{\ell_{r_i^1}}, f_{\mathbf{ar}}, f_{\mathbf{aw}}$ where $f_{\ell_{r_i^1}}$ is defined as

$$\begin{array}{ll} \text{if} & \\ & \ell_{r_i^2} = 6 \quad \text{then } \ell_{r_i^1} = 6 \\ & \ell_{r_i^2} = 7 \quad \text{then } \ell_{r_i^1} = 7 \\ & \ell_{r_i^2} \neq 6 \wedge \ell_{r_i^2} \neq 7 \quad \text{then } \ell_{r_i^1} \\ \text{fi} & \end{array}$$

and f_x is defined as

$$\begin{array}{ll} \text{if} & \\ & \ell_{r_i^2} = 6 \quad \text{then } m - 1 \\ & \ell_{r_i^2} \neq 6 \quad \text{then } m + r + w \\ \text{fi} & \end{array}$$

, i.e., the first PV-section is stuttering and semaphore x is split into semaphores m , r and w . Note: the refinement mappings for aw and ar are equal to the identity mapping, so we can leave them out.

$$\begin{aligned}
 (a) \quad \mathcal{S}_2 \cap Hist(W_2) & \models (I_{r_i^2} \wedge p_2) \rightarrow (I_{r_i^1} \wedge p_{ri}) [\bar{f}/X_1] \\
 (b) \quad \mathcal{S}_2 \cap Hist(W_2) & \models T_{r_i^2} \wedge ((p_2 \wedge p'_2) \vee \mathbf{stut}_2) \\
 & \rightarrow (T_{r_i^1} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^1})) [\bar{f}/X_1] \\
 (c) \quad \mathcal{S}_2 \cap Hist(W_2) & \models L_{r_i^1} [\bar{f}/X_1]
 \end{aligned}$$

(a) **Proof 3**

$$\begin{aligned}
 & I_{r_i^2} \wedge p_2 \\
 = & \quad \% Def. I_{r_i^2} \\
 & (m, r, w, ar, aw, \mathbf{s}_{r_i}, \ell_{r_i^2}) = (1, 0, 0, 0, 0, 0, 0) \\
 \rightarrow & \quad \% Def. f_x, f_{\ell_{r_i^1}} \\
 & ((x, ar, aw, \mathbf{s}_{r_i}, \ell_{r_i^1}) = (1, 0, 0, 0, 0) \wedge \ell_{r_i^1} = 1 \rightarrow aw = 0) [\bar{f}/X_1] \\
 = & \quad \% Def. I_{r_i^1}, p_{ri} \\
 & (I_{r_i^1} \wedge p_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

(b) **Proof 4**

Since $T_{r_i^2}$ is of the form $\mathbf{stut}_{r_i^2} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau})$ then $T_{r_i^2} \wedge ((p_2 \wedge p'_2) \vee \mathbf{stut}_2)$ is equal to $\mathbf{stut}_{r_i^2} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau} \wedge p_2 \wedge p'_2)$. $T_{r_i^1}$ is of the form $\mathbf{stut}_{r_i^1} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau})$ so $T_{r_i^1} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^1})$ is equal to $\mathbf{stut}_{r_i^1} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau} \wedge p_{ri} \wedge p'_{ri})$.

$$\begin{aligned}
 - & \quad \tau_{r_i^1,1} \wedge p_2 \wedge p'_2 \\
 = & \quad \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, m) = (0, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 6/m, \ell_{r_i^2}] \right) \\
 \rightarrow & \quad \left(\epsilon = \mathbf{i} \wedge \Psi'_1 = \Psi_1 \right) [\bar{f}/X_1] \\
 \rightarrow & \quad \mathbf{stut}_{r_i^1} [\bar{f}/X_1]
 \end{aligned}$$

The first P-operation of reader_i² is an stuttering step in reader_i¹.

$$\begin{aligned}
 - & \quad \tau_{r_i^2,2} \wedge p_2 \wedge p'_2 \\
 = & \quad \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 6 \wedge p_2 \wedge p'_2 \wedge CHO(7) \right) \\
 \rightarrow & \quad \left(\epsilon = \mathbf{i} \wedge \Psi'_1 = \Psi_1 \right) [\bar{f}/X_1] \\
 \rightarrow & \quad \mathbf{stut}_{r_i^1} [\bar{f}/X_1]
 \end{aligned}$$

The first V-operation of reader_i² is an stuttering step in reader_i¹.

$$\begin{aligned}
 - & \quad \tau_{r_i^1,3} \wedge p_2 \wedge p'_2 \\
 = & \quad \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, r) = (7, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 1/r, \ell_{r_i^2}] \right) \\
 \rightarrow & \quad \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 0 \wedge x = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 1/x, \ell_{r_i^1}] \right) [\bar{f}/X_1] \\
 = & \quad (\tau_{r_i^1,1} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

The second P-operation of reader_i² corresponds to the first P-operation of reader_i¹.

3.4 The second development step

$$\begin{aligned}
& - \tau_{r_i^2,4} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [\text{ar} + 1, 2/\text{ar}, \ell_{r_i^2}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^1}, \text{aw}) = (1, 0) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [\text{ar} + 1, 2/\text{ar}, \ell_{r_i^1}] \right) \\
& \quad [\bar{f}/X_1] \\
& = (\tau_{r_i^1,2} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

The ar increment step of reader_i² corresponds to the ar increment step of reader_i¹.

$$\begin{aligned}
& - \tau_{r_i^2,5} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 2 \wedge p_2 \wedge p'_2 \wedge CHO(3) \wedge \Psi'_2 = \Psi_2 [1/\mathbf{s}_{r_i}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 2 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1, 1, 3/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i^1}] \right) [\bar{f}/X_1] \\
& = (\tau_{r_i^1,3} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

If reader_i² becomes critical then reader_i¹ becomes critical.

$$\begin{aligned}
& - \tau_{r_i^2,6} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{m}) = (3, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 4/\mathbf{m}, \ell_{r_i^2}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 3 \wedge \mathbf{x} = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 4/\mathbf{x}, \ell_{r_i^1}] \right) [\bar{f}/X_1] \\
& = (\tau_{r_i^1,4} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

The third P-operation of reader_i² corresponds to the second P-operation of reader_i¹.

$$\begin{aligned}
& - \tau_{r_i^2,7} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 4 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^2}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 4 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^1}] \right) [\bar{f}/X_1] \\
& = (\tau_{r_i^1,5} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

The ar decrement step of reader_i² corresponds to the ar decrement step of reader_i¹.

$$\begin{aligned}
& - \tau_{r_i^2,8} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 5 \wedge p_2 \wedge p'_2 \wedge CHO(0) \wedge \Psi'_2 = \Psi_2 [0/\mathbf{s}_{r_i}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^1} = 5 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0, 1, 0/\mathbf{s}_{r_i}, \mathbf{x}, \ell_{r_i^1}] \right) [\bar{f}/X_1] \\
& = (\tau_{r_i^1,6} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

If reader_i² becomes non-critical then reader_i¹ becomes non-critical.

$$\begin{aligned}
& - \tau_{r_i^2,9} \wedge p_2 \wedge p'_2 \\
& = \left(\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/\mathbf{m}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{e} \wedge \mathbf{x} = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0/\mathbf{x}] \right) [\bar{f}/X_1] \\
& = (\tau_{r_i^1,7} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
\end{aligned}$$

If the environment of reader_i² executes a P-operation then the environment of reader_i¹ also executes a P-operation.

$$\begin{aligned}
 - & \tau_{r_i^2, 10} \wedge p_2 \wedge p'_2 \\
 &= \left(\epsilon = \mathbf{e} \wedge r = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/r] \right) \\
 &\rightarrow \left(\epsilon = \mathbf{e} \wedge x = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0/x] \right) [\bar{f}/X_1] \\
 &= (\tau_{r_i^1, 7} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

If the environment of reader_i² executes a P-operation then the environment of reader_i¹ also executes a P-operation.

$$\begin{aligned}
 - & \tau_{r_i^2, 11} \wedge p_2 \wedge p'_2 \\
 &= \left(\epsilon = \mathbf{e} \wedge w = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/w] \right) \\
 &\rightarrow \left(\epsilon = \mathbf{e} \wedge x = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [0/x] \right) [\bar{f}/X_1] \\
 &= (\tau_{r_i^1, 7} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

If the environment of reader_i² executes a P-operation then the environment of reader_i¹ also executes a P-operation.

$$\begin{aligned}
 - & \tau_{r_i^2, 12} \wedge p_2 \wedge p'_2 \\
 &= \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge m = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/m] \right) \\
 &\rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^1} \in \{0, 3\} \wedge x = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1/x] \right) [\bar{f}/X_1] \\
 &= (\tau_{r_i^1, 8} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

If the environment of reader_i² executes a V-operation then the environment of reader_i¹ also executes a V-operation.

$$\begin{aligned}
 - & \tau_{r_i^2, 13} \wedge p_2 \wedge p'_2 \\
 &= \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge r = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/r] \right) \\
 &\rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^1} \in \{0, 3\} \wedge x = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1/x] \right) [\bar{f}/X_1] \\
 &= (\tau_{r_i^1, 8} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

If the environment of reader_i² executes a V-operation then the environment of reader_i¹ also executes a V-operation.

$$\begin{aligned}
 - & \tau_{r_i^2, 14} \wedge p_2 \wedge p'_2 \\
 &= \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge w = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/w] \right) \\
 &\rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^1} \in \{0, 3\} \wedge x = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_1 = \Psi_1 [1/x] \right) [\bar{f}/X_1] \\
 &= (\tau_{r_i^1, 8} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_1]
 \end{aligned}$$

If the environment of reader_i² executes a V-operation then the environment of reader_i¹ also executes a V-operation.

$$\begin{aligned}
 - & \mathbf{stut}_{r_i^2} \rightarrow \mathbf{stut}_{r_i^1} [\bar{f}/X_1] \\
 & \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$

(c) Let $\mathbf{WF}_{r_i^2} \triangleq \{\tau_{r_i^2, k} \mid k \in \{4, 7\}\}$ and

$\mathbf{SF}_{r_i^2} \triangleq \{\tau_{r_i^2, k} \mid k \in \{1, 2, 3, 5, 6, 8, 9, 10, 11, 12, 13, 14\}\}$ then

$$\mathbf{L}_{r_i^2} \triangleq \bigwedge_{\tau \in \mathbf{WF}_{r_i^2}} (\diamond \square \mathbf{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \mathbf{SF}_{r_i^2}} (\square \diamond \mathbf{En}(\tau) \rightarrow \square \diamond \tau)$$

3.5 The third development step

Let $\text{WF}_{r_i^1} \triangleq \{\tau_{r_i^1, k} \mid k \in \{2, 5\}\}$ and $\text{SF}_{r_i^1} \triangleq \{\tau_{r_i^1, k} \mid k \in \{1, 3, 4, 6, 7, 8\}\}$ then

$$L_{r_i^1} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^1}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^1}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

The following holds:

$$\mathcal{S}_2 \cap \text{Hist}(W_2) \models L_{r_i^2} \rightarrow L_{r_i^1} [\bar{f}/X_1]$$

since $\tau_{r_i^1, 2}$ is relatively refined by $\tau_{r_i^2, 4}$, $\tau_{r_i^1, 5}$ is relatively refined by $\tau_{r_i^2, 7}$, and $\tau_{r_i^1, 1}$ is relatively refined by $\tau_{r_i^2, 1}$, $\tau_{r_i^2, 2}$ and $\tau_{r_i^2, 3}$, and $\tau_{r_i^1, 3}$ is relatively refined by $\tau_{r_i^2, 5}$, and $\tau_{r_i^1, 4}$ is relatively refined by $\tau_{r_i^2, 6}$, and $\tau_{r_i^1, 6}$ is relatively refined by $\tau_{r_i^2, 8}$, and $\tau_{r_i^1, 7}$ is relatively refined by $\tau_{r_i^2, 9}$, $\tau_{r_i^2, 10}$ and $\tau_{r_i^2, 11}$, and $\tau_{r_i^1, 8}$ is relatively refined by $\tau_{r_i^2, 12}$, $\tau_{r_i^2, 13}$ and $\tau_{r_i^2, 14}$. So

$$\mathcal{S}_2 \cap \text{Hist}(W_2) \models L_{r_i^1} [\bar{f}/X_1]$$

ad (2) Analogue to the proof of (1).

ad (3) This is trivial because $W_1 \leftrightarrow (\bigwedge_{i=1}^N W_{r_i^1} \wedge \bigwedge_{j=1}^M W_{w_j^1})$.

ad (4) The following holds because W_2 doesn't contain ϵ_2 variables, i.e., it can be put within the existential quantification, and furthermore $W_2 = W_2[\epsilon_{2,i}/\epsilon] = W_2[\epsilon_{2,N+j}/\epsilon]$ ($i = 1, \dots, N$ and $j = 1, \dots, M$) because W_2 doesn't constrain the ϵ variable.

$$\begin{aligned} & (\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A}(\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N H_{r_i^2}[\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^2}[\epsilon_{2,N+j}/\epsilon]) \wedge W_2 \\ & \rightarrow \\ & (\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A}(\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N (H_{r_i^2} \wedge W_2)[\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M (H_{w_j^2} \wedge W_2)[\epsilon_{2,N+j}/\epsilon]) \end{aligned}$$

3.5 The third development step

Dijkstra's solution to the problem of the newly introduced deadlocked sequences is as follows: record in a shared variable bX the number of components that can generate a P-operation on a semaphore X as their first coming P-operation. A component that executed a P-operation on X decreases bX by one. The component "knows" what its next P-operation is, so it increases the corresponding shared variable by one. The guards in the CHOOSE segment are changed so that the correct V-branch is chosen. The initial value of bm is $N + M$ because initially all processes have $P(m)$ as their first coming P-operation. The initial value of br and bw is then of course 0. Like in the second step the initial value of m is 1 and that of ar, aw, r and w 0. The result of this transformation is as follows:

reader_i^3 :

```

do true → NCS;
    P(m); bm := bm - 1; br := br + 1; CHOOSE;
    P(r); br := br - 1; ar := ar + 1; bm := bm + 1; CHOOSE;
    READ;

```

```

                                P(m);bm:=bm-1;ar:=ar-1;bm:=bm+1;CHOOSE
    od
writerj3:
    do true → NCS;
        P(m);bm:=bm-1;bw:=bw+1;CHOOSE;
        P(w);bw:=bw-1;aw:=aw+1;bm:=bm+1;CHOOSE;
        WRITE;
        P(m);bm:=bm-1;aw:=aw-1;bm:=bm+1;CHOOSE
    od
with CHOOSE: if bm>0 →V(m)
                □ aw=0 ∧ br>0 →V(r)
                □ aw=0 ∧ ar=0 ∧ bw>0 →V(w)
            fi

```

$\text{Syn}^3 : \parallel_{i=1}^N \text{reader}_i^3 \parallel \parallel_{j=1}^M \text{writer}_j^3$

Syn^3 still generates sequences that Dijkstra does not allow. These sequences are generated because CHOOSE is still non-deterministic. Suppose a reader_i^3 can choose between a $V(m)$ and a $V(r)$ operation. Choosing $V(m)$ causes that another reader_k^3 (writer_j^3) can signal that it has finished executing READ (WRITE) or wants to execute READ (WRITE). A $V(r)$ causes that a reader_k^3 can execute READ. Choosing $V(m)$ thus unnecessarily blocks a reader_k^3 . So it is not a deadlocked sequence but only an inefficient sequence. The informal requirement of Syn^3 is that no unnecessary blocking sequences are allowed.

In the following sections the DTL machine specifications $\mathcal{S}_{r_i^3}$ (corresponding to program reader_i^3) and $\mathcal{S}_{w_j^3}$ (corresponding to program writer_j^3), and the extra requirement W_3 , excluding inefficient computations, are given.

3.5.1 Specification $\mathcal{S}_{r_i^3}$

The formal specification $\mathcal{S}_{r_i^3} \triangleq (B_{r_i^3}, H_{r_i^3})$ where $H_{r_i^3} \triangleq I_{r_i^3} \wedge \square T_{r_i^3} \wedge L_{r_i^3}$ and $B_{r_i^3}, I_{r_i^3}, T_{r_i^3}$ and $L_{r_i^3}$ are as follows:

1. **Basis** $B_{r_i^3} = ((\text{In}_{r_i^3}, \text{Out}_{r_i^3}), (\text{V}_{r_i^3}, \text{X}_{r_i^3}))$

$$\begin{aligned}
 \text{In}_{r_i^3} &\triangleq \emptyset, \\
 \text{Out}_{r_i^3} &\triangleq \emptyset, \\
 \text{V}_{r_i^3} &\triangleq \{\mathbf{m}, \mathbf{bm}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}\}, \\
 \text{X}_{r_i^3} &\triangleq \{\ell_{r_i^3}\}
 \end{aligned}$$

- $\ell_{r_i^3} = 0$: reader_i^3 is non critical.
- $\ell_{r_i^3} = 6$: reader_i^3 executed first P-action on \mathbf{m} .

3.5 The third development step

- $\ell_{r_i^3} = 8$: reader $_i^3$ updated **bm** and **br**.
- $\ell_{r_i^3} = 7$: reader $_i^3$ executed first **CHOOSE**.
- $\ell_{r_i^3} = 1$: reader $_i^3$ executed P-action on **r**.
- $\ell_{r_i^3} = 2$: reader $_i^3$ updated **br**, **ar** and **bm**.
- $\ell_{r_i^3} = 3$: reader $_i^3$ is critical.
- $\ell_{r_i^3} = 4$: reader $_i^3$ executed second P-action on **m**.
- $\ell_{r_i^3} = 5$: reader $_i^3$ updated **ar**.

Let $\Psi_3 \triangleq (\mathbf{m}, \mathbf{bm}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}, \ell_{r_i^3})$ and $\Psi'_3 \triangleq (\mathbf{m}', \mathbf{bm}', \mathbf{r}', \mathbf{br}', \mathbf{w}', \mathbf{bw}', \mathbf{ar}', \mathbf{aw}', \mathbf{s}'_{r_i}, \ell'_{r_i^3})$.

2. Initial States

$$I_{r_i^3} \triangleq \Psi_3 = (1, N + M, 0, 0, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\begin{aligned} \text{Let } CHO(X) \triangleq & \vee (\mathbf{bm} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{m}, \ell_{r_i^3}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{br} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{r}, \ell_{r_i^3}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \mathbf{bw} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{w}, \ell_{r_i^3}]) \end{aligned}$$

$$T_{r_i^3} \triangleq$$

$$\tau_{r_{i,1}^3} \quad \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, \mathbf{m}) = (0, 1) \wedge \Psi'_3 = \Psi_3 [0, 6/\mathbf{m}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ executes first P-action on **m**.

$$\tau_{r_{i,2}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 6 \wedge \Psi'_3 = \Psi_3 [\mathbf{bm} - 1, \mathbf{br} + 1, 8/\mathbf{bm}, \mathbf{br}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ updates **bm** and **br**.

$$\tau_{r_{i,3}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 8 \wedge CHO(7) \right)$$

Reader $_i^3$ executes first **CHOOSE**.

$$\tau_{r_{i,4}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, \mathbf{r}) = (7, 1) \wedge \Psi'_3 = \Psi_3 [0, 1/\mathbf{r}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ executes its P-action on **r**.

$$\tau_{r_{i,5}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 1 \wedge \Psi'_3 = \Psi_3 [\mathbf{br} - 1, \mathbf{ar} + 1, \mathbf{bm} + 1, 2/\mathbf{br}, \mathbf{ar}, \mathbf{bm}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ updates **br**, **ar** and **bm**.

$$\tau_{r_{i,6}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 2 \wedge CHO(3) \wedge \Psi'_3 = \Psi_3 [1/\mathbf{s}_{r_i}] \right)$$

Reader $_i^3$ becomes critical.

$$\tau_{r_{i,7}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, \mathbf{m}) = (3, 1) \wedge \Psi'_3 = \Psi_3 [0, 4/\mathbf{m}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ executes second P-action on **m**.

$$\tau_{r_{i,8}^3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 4 \wedge \Psi'_3 = \Psi_3 [\mathbf{ar} - 1, 5/\mathbf{ar}, \ell_{r_i^3}] \right)$$

Reader $_i^3$ updates **ar**.

$\tau_{r_i^3,9}$	$\vee (\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 5 \wedge CHO(0) \wedge \Psi'_3 = \Psi_3 [0/s_{r_i}])$
	Reader _{<i>i</i>} ³ becomes non critical.
$\tau_{r_i^3,10}$	$\vee (\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{m}])$
	The environment executes a P-operation on \mathbf{m} .
$\tau_{r_i^3,11}$	$\vee (\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{r}])$
	The environment executes a P-operation on \mathbf{r} .
$\tau_{r_i^3,12}$	$\vee (\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{w}])$
	The environment executes a P-operation on \mathbf{w} .
$\tau_{r_i^3,13}$	$\vee (\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_3 = \Psi_3 [1/\mathbf{m}])$
	The environment executes a V-operation on \mathbf{m} .
$\tau_{r_i^3,14}$	$\vee (\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_3 = \Psi_3 [1/\mathbf{r}])$
	The environment executes a V-operation on \mathbf{r} .
$\tau_{r_i^3,15}$	$\vee (\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_3 = \Psi_3 [1/\mathbf{w}])$
	The environment executes a P-operation on \mathbf{w} .
$\tau_{r_i^3,0}$	$\vee \mathbf{stut}_{r_i^3}$

These transitions are illustrated in figure 3.7

4. Liveness:

$L_{r_i^3}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are strongly fair and all the other transitions are weakly fair.

Let $WF_{r_i^3} \triangleq \{\tau_{r_i^3,k} \mid k \in \{2, 5, 8\}\}$ and

$SF_{r_i^3} \triangleq \{\tau_{r_i^3,k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{r_i^3} \triangleq \bigwedge_{\tau \in WF_{r_i^3}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{r_i^3}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

3.5.2 Specification $\mathcal{S}_{w_j^3}$

The formal specification $\mathcal{S}_{w_j^3} \triangleq (B_{w_j^3}, H_{w_j^3})$ where $H_{w_j^3} \triangleq I_{w_j^3} \wedge \square T_{w_j^3} \wedge L_{w_j^3}$ and $B_{w_j^3}$, $I_{w_j^3}$, $T_{w_j^3}$ and $L_{w_j^3}$ are as follows:

1. **Basis** $B_{w_j^3} = ((In_{w_j^3}, Out_{w_j^3}), (V_{w_j^3}, X_{w_j^3}))$

$$\begin{aligned} In_{w_j^3} &\triangleq \emptyset, \\ Out_{w_j^3} &\triangleq \emptyset, \\ V_{w_j^3} &\triangleq \{\mathbf{m}, \mathbf{bm}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{w_j}\}, \\ X_{w_j^3} &\triangleq \{\ell_{w_j^3}\} \end{aligned}$$

3.5 The third development step

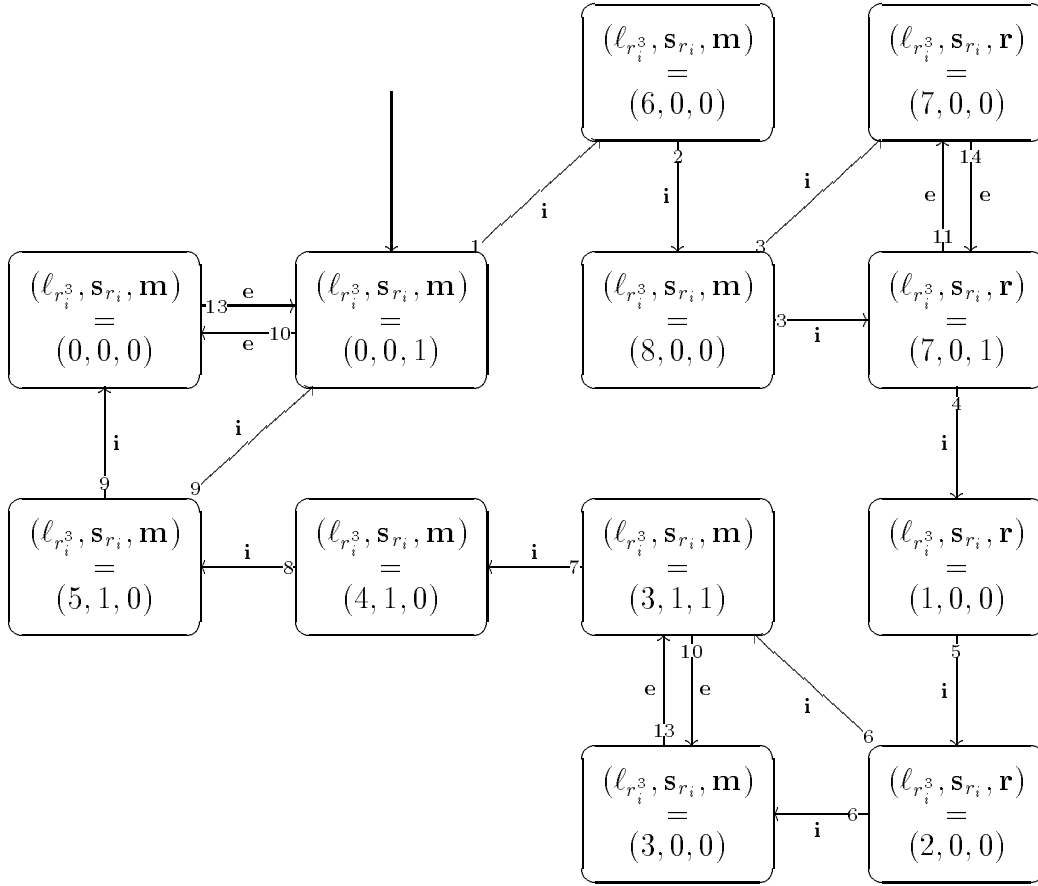


Figure 3.7: Transitions of reader_{*i*}³.

- $l_{w_j^3} = 0$: writer_{*j*}³ is non critical.
- $l_{w_j^3} = 6$: writer_{*j*}³ executed first P-action on **m**.
- $l_{w_j^3} = 8$: writer_{*j*}³ updated **bm** and **bw**.
- $l_{w_j^3} = 7$: writer_{*j*}³ executed first CHOOSE.
- $l_{w_j^3} = 1$: writer_{*j*}³ executed P-action on **w**.
- $l_{w_j^3} = 2$: writer_{*j*}³ updated **bw**, **aw** and **bm**.
- $l_{w_j^3} = 3$: writer_{*j*}³ is critical.
- $l_{w_j^3} = 4$: writer_{*j*}³ executed second P-action on **m**.
- $l_{w_j^3} = 5$: writer_{*j*}³ updated **aw**.

Let $\Psi_3 \triangleq (\mathbf{m}, \mathbf{bm}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, s_{w_j}, l_{w_j^3})$ and $\Psi'_3 \triangleq (\mathbf{m}', \mathbf{bm}', \mathbf{r}', \mathbf{br}', \mathbf{w}', \mathbf{bw}', \mathbf{ar}', \mathbf{aw}', s'_{w_j}, l'_{w_j^3})$.

2. Initial States

$$I_{w_j^3} \triangleq \Psi_3 = (1, N + M, 0, 0, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\begin{aligned} \text{Let } CHO(X) \triangleq & \vee (\mathbf{bm} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{m}, \ell_{w_j^3}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{br} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{r}, \ell_{w_j^3}]) \\ & \vee (\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \mathbf{bw} > 0 \wedge \Psi'_3 = \Psi_3 [1, X/\mathbf{w}, \ell_{w_j^3}]) \end{aligned}$$

$$\Gamma_{w_j^3} \triangleq$$

$$\tau_{w_{i,1}^3} \quad (\epsilon = \mathbf{i} \wedge (\ell_{w_j^3}, \mathbf{m}) = (0, 1) \wedge \Psi'_3 = \Psi_3 [0, 6/\mathbf{m}, \ell_{w_j^3}])$$

Writer_j³ executes first P-action on **m**.

$$\tau_{w_{i,2}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 6 \wedge \Psi'_3 = \Psi_3 [\mathbf{bm} - 1, \mathbf{bw} + 1, 8/\mathbf{bm}, \mathbf{bw}, \ell_{w_j^3}])$$

Writer_j³ updates **bm** and **bw**.

$$\tau_{w_{i,2}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 8 \wedge CHO(7))$$

Writer_j³ executes first CHOOSE.

$$\tau_{w_{i,4}^3} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{w_j^3}, \mathbf{w}) = (7, 1) \wedge \Psi'_3 = \Psi_3 [0, 1/\mathbf{w}, \ell_{w_j^3}])$$

Writer_j³ executes its P-action on **w**.

$$\tau_{w_{i,5}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 1 \wedge \Psi'_3 = \Psi_3 [\mathbf{bw} - 1, \mathbf{aw} + 1, \mathbf{bm} + 1, 2/\mathbf{bw}, \mathbf{aw}, \mathbf{bm}, \ell_{w_j^3}])$$

Writer_j³ updates **bw**, **aw** and **bm**.

$$\tau_{w_{i,6}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 2 \wedge CHO(3) \wedge \Psi'_3 = \Psi_3 [1/\mathbf{s}_{w_j}])$$

Writer_j³ becomes critical.

$$\tau_{w_{i,7}^3} \quad \vee (\epsilon = \mathbf{i} \wedge (\ell_{w_j^3}, \mathbf{m}) = (3, 1) \wedge \Psi'_3 = \Psi_3 [0, 4/\mathbf{m}, \ell_{w_j^3}])$$

Writer_j³ executes second P-action on **m**.

$$\tau_{w_{i,8}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 4 \wedge \Psi'_3 = \Psi_3 [\mathbf{aw} - 1, 5/\mathbf{aw}, \ell_{w_j^3}])$$

Writer_j³ updates **aw**.

$$\tau_{w_{i,9}^3} \quad \vee (\epsilon = \mathbf{i} \wedge \ell_{w_j^3} = 5 \wedge CHO(0) \wedge \Psi'_3 = \Psi_3 [0/\mathbf{s}_{w_j}])$$

Writer_j³ becomes non critical.

$$\tau_{w_{i,10}^3} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{m}])$$

The environment executes a P-operation on **m**.

$$\tau_{w_{i,11}^3} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{r}])$$

The environment executes a P-operation on **r**.

$$\tau_{w_{i,12}^3} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_3 = \Psi_3 [0/\mathbf{w}])$$

The environment executes a P-operation on **w**.

3.5 The third development step

$$\tau_{w_j^3, i, 13} \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^3} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_3 = \Psi_3[1/\mathbf{m}])$$

The environment executes a V-operation on \mathbf{m} .

$$\tau_{w_j^3, i, 14} \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^3} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_3 = \Psi_3[1/\mathbf{r}])$$

The environment executes a V-operation on \mathbf{r} .

$$\tau_{w_j^3, i, 15} \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^3} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_3 = \Psi_3[1/\mathbf{w}])$$

The environment executes a P-operation on \mathbf{w} .

$$\tau_{w_j^3, i, 0} \quad \vee \quad \mathbf{stut}_{w_j^3}$$

These transitions are illustrated in figure 3.8

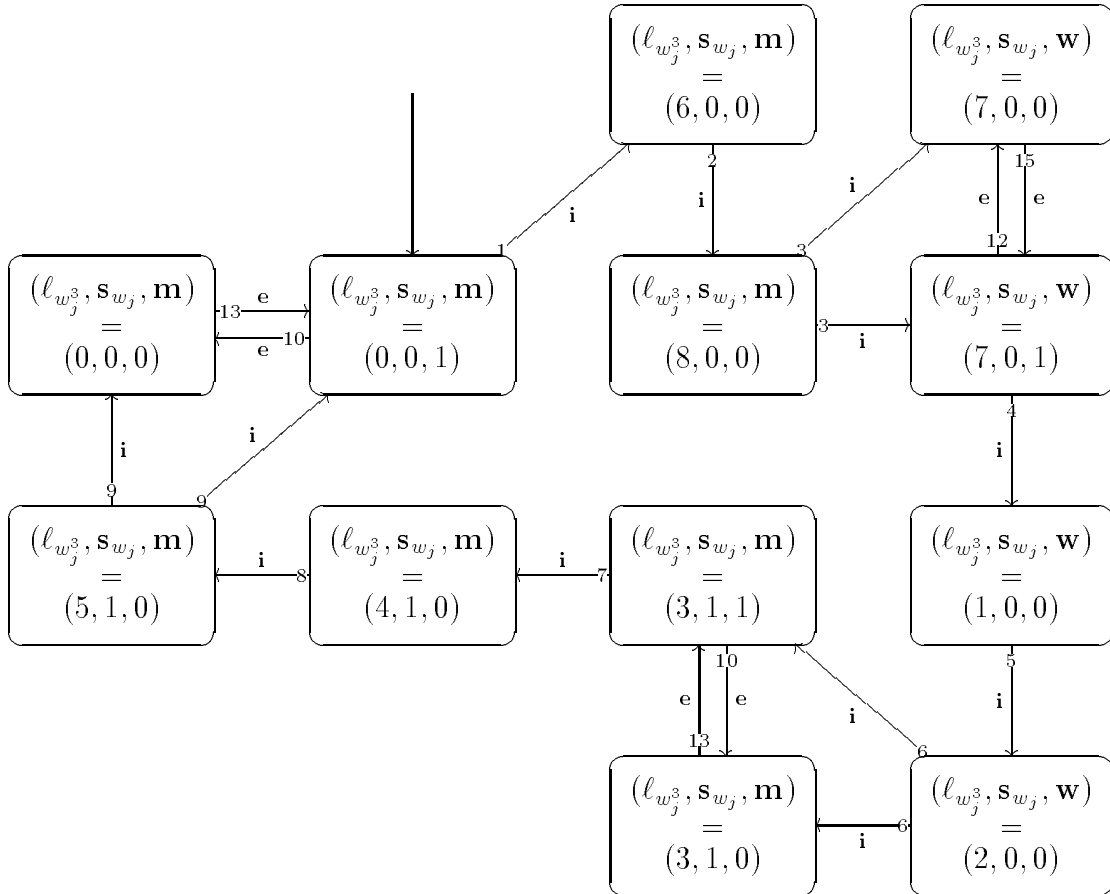


Figure 3.8: Transitions of w_j^3 .

4. Liveness:

$L_{w_j^3}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are strongly fair and all the other transitions are weakly fair.

Let $WF_{w_j^3} \triangleq \{\tau_{w_j^3, k} \mid k \in \{2, 5, 8\}\}$ and

$SF_{w_j^3} \triangleq \{\tau_{w_j^3, k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{w_j^3} \triangleq \bigwedge_{\tau \in WF_{w_j^3}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{w_j^3}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

3.5.3 Requirement W_3

The extra requirement W_3 should exclude inefficient computations caused by the nondeterminism of **CHOOSE**. So it is natural to make **CHOOSE** more deterministic, i.e., when one can choose between a $V(\mathbf{r})$ ($V(\mathbf{w})$) and a $V(\mathbf{m})$ operation priority is given to the $V(\mathbf{r})$ ($V(\mathbf{w})$) operation. Let q_3 be defined as

$$(\mathbf{m} = 1 \wedge \mathbf{bm} > 0 \wedge \neg(\mathbf{aw} = 0 \wedge \mathbf{br} > 0) \wedge \neg(\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \mathbf{bw} > 0))$$

$$\begin{aligned} \vee (\mathbf{r} = 1 \wedge \mathbf{aw} = 0 \wedge \mathbf{br} > 0) \\ \vee (\mathbf{w} = 1 \wedge \mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \mathbf{bw} > 0) \end{aligned}$$

Then W_3 is as follows

$$W_3 \triangleq \square \left(\left(\bigwedge_{i=1}^N \ell_{r_i^3} \in \{0, 7, 3\} \rightarrow q_3 \right) \wedge \left(\bigwedge_{j=1}^M \ell_{w_j^3} \in \{0, 7, 3\} \rightarrow q_3 \right) \right)$$

So in **CHOOSE** priority is given to $V(\mathbf{r})$ and $V(\mathbf{w})$ by strengthen the guard of $V(\mathbf{m})$ with the complement of the guards of $V(\mathbf{r})$ and $V(\mathbf{w})$. The same construction as in the previous development step is used to write this down as a machine. Let $p_3 \triangleq (\bigwedge_{i=1}^N \ell_{r_i^3} \in \{0, 7, 3\} \rightarrow q_3) \wedge (\bigwedge_{j=1}^M \ell_{w_j^3} \in \{0, 7, 3\} \rightarrow q_3)$ then $W_3 = p_3 \wedge \square((p_3 \wedge p'_3) \vee \mathbf{stut}_3)$. Again the liveness part of W_3 equals **true**.

3.5.4 \mathcal{S}_3 relatively refines \mathcal{S}_2

Since the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} , and the shared variables \mathbf{ar} , \mathbf{aw} , \mathbf{br} , \mathbf{bw} and \mathbf{bm} are used only by the subcomponents of \mathcal{S}_3 and the semaphores \mathbf{m} , \mathbf{w} and \mathbf{r} and the shared variables \mathbf{ar} and \mathbf{aw} only by the subcomponents of \mathcal{S}_2 , we should prove $\mathcal{S}_3 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm}\}$ relatively refines $\mathcal{S}_2 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}\}$. According to definition 35, 36 and theorem 8 $\mathcal{S}_3 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm}\}$ relatively refines $\mathcal{S}_2 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}\}$ with respect to (W_3, W_2) iff the following holds:

$$\begin{aligned} \mathfrak{D}(B_3) = \mathfrak{D}(B_2) \text{ and} \\ \models (\exists X_3. (G_3 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm})' = (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm})))) \\ \rightarrow \\ (\exists X_2. (G_2 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw})' = (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw})))))) \end{aligned}$$

where X_3 are the local variables from \mathcal{S}_3 , i.e., $X_3 \triangleq \{\ell_{r_i^3} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^3} \mid j = 1, \dots, M\} \cup \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{aw}, \mathbf{ar}, \mathbf{br}, \mathbf{bw}, \mathbf{bm}\}$ and G_3 is the composition of $\mathcal{S}_{r_i^3}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^3}$ ($j = 1, \dots, M$) and W_3 ,

let $\bar{\epsilon}_3 \triangleq \epsilon_{3,1}, \dots, \epsilon_{3,N}, \epsilon_{3,N+1}, \dots, \epsilon_{3,N+M}$, and

3.5 The third development step

let $\bar{B}_3^A \triangleq B_{r_1^3}^A, \dots, B_{r_N^3}^A, B_{w_1^3}^A, \dots, B_{w_M^3}^A$

then $G_3 \triangleq$

$$\left(\exists \bar{\epsilon}_3. \odot_{\bar{B}_3^A} (\epsilon, \bar{\epsilon}_3) \wedge \bigwedge_{i=1}^N H_{r_i^3} [\epsilon_{3,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^3} [\epsilon_{3,N+j}/\epsilon] \right) \wedge W_3$$

X_2 are the local variables from \mathcal{S}_2 , i.e., $X_2 \triangleq \{\ell_{r_i^2} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^2} \mid j = 1, \dots, M\} \cup \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}\}$ and G_2 is the composition of $\mathcal{S}_{r_i^2}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^2}$ ($j = 1, \dots, M$) and W_2 ,

let $\bar{\epsilon}_2 \triangleq \epsilon_{2,1}, \dots, \epsilon_{2,N}, \epsilon_{2,N+1}, \dots, \epsilon_{2,N+M}$, and

let $\bar{B}_2^A \triangleq B_{r_1^2}^A, \dots, B_{r_N^2}^A, B_{w_1^2}^A, \dots, B_{w_M^2}^A$

then $G_2 \triangleq$

$$\left(\exists \bar{\epsilon}_2. \odot_{\bar{B}_2^A} (\epsilon, \bar{\epsilon}_2) \wedge \bigwedge_{i=1}^N H_{r_i^2} [\epsilon_{2,i}/\epsilon] \wedge \bigwedge_{j=1}^M H_{w_j^2} [\epsilon_{2,N+j}/\epsilon] \right) \wedge W_2$$

As seen in the previous development step W_2 is ϵ -free but can not be decomposed into sub-requirements. W_3 is ϵ -free and can be decomposed into sub-requirements. Let $p_{ri} \triangleq (\ell_{r_i^3} \in \{0, 7, 3\} \rightarrow q_3)$ and $W_{r_i^3} \triangleq \square p_{ri}$, and $p_{wj} \triangleq (\ell_{w_j^3} \in \{0, 7, 3\} \rightarrow q_3)$ and $W_{w_j^3} \triangleq \square p_{wj}$ then $W_3 = (\bigwedge_{i=1}^N W_{r_i^3}) \wedge (\bigwedge_{j=1}^M W_{w_j^3})$. Now Lemma 9, 10 and 11 can be used for the proof, i.e., following proof rule can be used

$$\frac{\begin{array}{l} W_3 \subseteq \bigcap_{i=1}^N W_{r_i^3} \cap \bigcap_{j=1}^M W_{w_j^3} \\ \mathcal{S}_{r_i^3} \ W_{r_i^3} \ \mathbf{ref} \ W_2 \ \mathcal{S}_{r_i^2} \quad W_{r_i^3} \ \text{constraining} \ B_{r_i^3} \\ \mathcal{S}_{w_j^3} \ W_{w_j^3} \ \mathbf{ref} \ W_2 \ \mathcal{S}_{w_j^2} \quad W_{w_j^3} \ \text{constraining} \ B_{w_j^3} \end{array}}{\mathcal{S}_3 \ W_3 \ \mathbf{ref} \ W_2 \ \mathcal{S}_2}$$

This means we have to prove for $i = 1, \dots, N$ and $j = 1, \dots, M$:

- (1) $\left(\exists X_{r_i^3}. (H_{r_i^2} \wedge W_{r_i^3}) \right) \rightarrow \left(\exists X_{r_i^2}. (H_{r_i^2} \wedge W_2) \right)$
- (2) $\left(\exists X_{w_j^3}. (H_{w_j^3} \wedge W_{w_j^3}) \right) \rightarrow \left(\exists X_{w_j^2}. (H_{w_j^2} \wedge W_2) \right)$
- (3) $W_3 \rightarrow (W_{r_i^3} \wedge W_{w_j^3})$

ad (1) Rule 3 will be used to prove (1). This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_3 to \mathcal{S}_2 , defined as: $\bar{f} = f_{\ell_{r_i^2}}, f_{\mathbf{m}}, f_{\mathbf{r}}, f_{\mathbf{w}}, f_{\mathbf{aw}}, f_{\mathbf{ar}}$ where $f_{\ell_{r_i^2}}$ is defined as

$$\begin{array}{l} \textit{if} \\ \quad \ell_{r_i^3} = 8 \quad \textit{then} \quad \ell_{r_i^3} - 2 \\ \quad \ell_{r_i^3} \neq 8 \quad \textit{then} \quad \ell_{r_i^3} \\ \textit{fi} \end{array}$$

, i.e., the updating of \mathbf{bm} and \mathbf{br} in the first PV-section in reader_i^3 is a stuttering step in reader_i^2 . Note: the refinement mappings for \mathbf{m} , \mathbf{r} , \mathbf{w} , \mathbf{aw} and \mathbf{ar} are equal to the

identity mapping, so we can leave them out.

$$\begin{aligned}
 (a) \quad \mathcal{S}_3 \cap Hist(W_3) &\models (I_{r_i^3} \wedge p_{ri}) \rightarrow (I_{r_i^2} \wedge p_2) [\bar{f}/X_2] \\
 (b) \quad \mathcal{S}_3 \cap Hist(W_3) &\models T_{r_i^3} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^3}) \\
 &\rightarrow (T_{r_i^2} \wedge ((p_2 \wedge p'_2) \vee \mathbf{stut}_2)) [\bar{f}/X_2] \\
 (c) \quad \mathcal{S}_3 \cap Hist(W_3) &\models L_{r_i^2} [\bar{f}/X_2]
 \end{aligned}$$

(a) **Proof 5**

$$\begin{aligned}
 &I_{r_i^3} \wedge p_{ri} \\
 = &\% Def. I_{r_i^3}, p_{ri} \\
 &(m, bm, r, br, w, bw, ar, aw, \mathbf{s}_{r_i}, \ell_{r_i^3}) = (1, N + M, 0, 0, 0, 0, 0, 0, 0) \\
 \rightarrow &\% Def. f_{\ell_{r_i^3}}, \\
 &bm = \#(k : 1 \leq k \leq N : \ell_{r_k^3} \in \{0, \dots, 5\}) + \\
 &\quad \#(n : 1 \leq n \leq M : \ell_{w_n^3} \in \{0, 1, 2, 3, 4, 5\}) \\
 &br = \#(k : 1 \leq k \leq N : \ell_{r_k^3} \in \{6, 7, 8\}) \\
 &bw = \#(n : 1 \leq n \leq M : \ell_{w_n^3} \in \{6, 7, 8\}) \\
 &((m, r, w, ar, aw, \mathbf{s}_{r_i}, \ell_{r_i^2}) = (1, 0, 0, 0, 0, 0) \wedge p_2) [\bar{f}/X_2] \\
 = &\% Def. I_{r_i^2} \\
 &(I_{r_i^2} \wedge p_2) [\bar{f}/X_2]
 \end{aligned}$$

(b) **Proof 6**

Since $T_{r_i^2}$ is of the form $\mathbf{stut}_{r_i^2} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau})$ then $T_{r_i^2} \wedge ((p_2 \wedge p'_2) \vee \mathbf{stut}_2)$ is equal to $\mathbf{stut}_{r_i^2} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau} \wedge p_2 \wedge p'_2)$. $T_{r_i^3}$ is of the form $\mathbf{stut}_{r_i^3} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau})$ so $T_{r_i^3} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^3})$ is equal to $\mathbf{stut}_{r_i^3} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau} \wedge p_{ri} \wedge p'_{ri})$.

$$\begin{aligned}
 - &\tau_{r_{i,1}^3} \wedge p_{ri} \wedge p'_{ri} \\
 = &\left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, m) = (0, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0, 6/m, \ell_{r_i^3}] \right) \\
 \rightarrow &\left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, m) = (0, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 6/m, \ell_{r_i^2}] \right) [\bar{f}/X_2] \\
 = &(\tau_{r_{i,1}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

The first P-operation of reader_i³ corresponds with the first P-operation of reader_i².

$$\begin{aligned}
 - &\tau_{r_{i,2}^3} \wedge p_{ri} \wedge p'_{ri} \\
 = &\left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 6 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [bm - 1, br + 1, 8/bm, br, \ell_{r_i^3}] \right) \\
 \rightarrow &\left(\epsilon = \mathbf{i} \wedge \Psi'_2 = \Psi_2 \right) [\bar{f}/X_2] \\
 \rightarrow &\mathbf{stut}_{r_i^2} [\bar{f}/X_2]
 \end{aligned}$$

The updating of br and bm in reader_i³ is an stuttering step in reader_i².

$$\begin{aligned}
 - &\tau_{r_{i,3}^3} \wedge p_{ri} \wedge p'_{ri} \\
 = &\left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 8 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(7) \right) \\
 \rightarrow &\left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 6 \wedge p_2 \wedge p'_2 \wedge CHO(7) \right) [\bar{f}/X_2] \\
 = &(\tau_{r_{i,2}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

3.5 The third development step

The first V-operation of reader_i³ corresponds to the first V-operation of reader_i².

$$\begin{aligned}
- & \tau_{r_{i,4}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, \mathbf{r}) = (7, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0, 1/r, \ell_{r_i^3}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{r}) = (7, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 1/r, \ell_{r_i^2}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,3}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

The second P-operation of reader_i³ corresponds to the second P-operation of reader_i².

$$\begin{aligned}
- & \tau_{r_{i,5}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 1 \wedge p_{ri} \right. \\
& \quad \left. \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [\text{br} - 1, \text{ar} + 1, \text{bm} + 1, 2/\text{br}, \text{ar}, \text{bm}, \ell_{r_i^3}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [\text{ar} + 1, 2/\text{ar}, \ell_{r_i^2}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,4}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

The ar decrement step of reader_i³ corresponds to the ar decrement step of reader_i².

$$\begin{aligned}
- & \tau_{r_{i,6}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 2 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(3) \wedge \Psi'_3 = \Psi_3 [1/s_{r_i}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 2 \wedge p_2 \wedge p'_2 \wedge CHO(3) \wedge \Psi'_2 = \Psi_2 [1/s_{r_i}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,5}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

If reader_i³ becomes critical then reader_i² becomes critical.

$$\begin{aligned}
- & \tau_{r_{i,7}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, \mathbf{m}) = (3, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0, 4/\mathbf{m}, \ell_{r_i^3}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^2}, \mathbf{m}) = (3, 1) \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0, 4/\mathbf{m}, \ell_{r_i^2}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,6}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

The third P-operation of reader_i³ corresponds to the third P-operation of reader_i².

$$\begin{aligned}
- & \tau_{r_{i,8}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 4 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^3}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 4 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^2}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,7}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

The ar decrement step of reader_i³ corresponds to the ar decrement step of reader_i².

$$\begin{aligned}
- & \tau_{r_{i,9}^3} \wedge p_{ri} \wedge p'_{ri} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 5 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(0) \wedge \Psi'_3 = \Psi_3 [0/s_{r_i}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^2} = 5 \wedge p_2 \wedge p'_2 \wedge CHO(0) \wedge \Psi'_2 = \Psi_2 [0/s_{r_i}] \right) [\bar{f}/X_2] \\
& = (\tau_{r_{i,8}^2} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
\end{aligned}$$

The third V-operation of reader_i³ corresponds to the third V-operation of reader_i².

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 10} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge m = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/m] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge m = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/m] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 9} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a P-operation then the environment of reader_i² also executes a P-operation.

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 11} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge r = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/r] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge r = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/r] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 10} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a P-operation then the environment of reader_i² also executes a P-operation.

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 12} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge w = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/w] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge w = 1 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [0/w] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 11} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a P-operation then the environment of reader_i² also executes a P-operation.

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 13} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge m = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [1/m] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge m = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/m] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 12} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a V-operation then the environment of reader_i² also executes a V-operation.

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 14} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge r = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [1/r] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge r = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/r] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 13} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a V-operation then the environment of reader_i² also executes a V-operation.

$$\begin{aligned}
 & - \quad \tau_{r_i^3, 15} \wedge p_{ri} \wedge p'_{ri} \\
 & = \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge w = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [1/w] \right) \\
 & \rightarrow \quad \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge w = 0 \wedge p_2 \wedge p'_2 \wedge \Psi'_2 = \Psi_2 [1/w] \right) [\bar{f}/X_2] \\
 & = \quad (\tau_{r_i^2, 14} \wedge p_2 \wedge p'_2) [\bar{f}/X_2]
 \end{aligned}$$

If the environment of reader_i³ executes a V-operation then the environment of reader_i² also executes a V-operation.

$$\begin{aligned}
 & - \quad \mathbf{stut}_{r_i^3} \rightarrow \mathbf{stut}_{r_i^2} [\bar{f}/X_2] \\
 & \text{since } \mathbf{s}_{r_i} \text{ doesn't change.}
 \end{aligned}$$

3.6 The fourth development step

(c) Let $WF_{r_i^2} \triangleq \{\tau_{r_i^2, k} \mid k \in \{4, 7\}\}$ and
 $SF_{r_i^2} \triangleq \{\tau_{r_i^2, k} \mid k \in \{1, 2, 3, 5, 6, 8, 9, 10, 11, 12, 13, 14\}\}$ then

$$L_{r_i^2} \triangleq \bigwedge_{\tau \in WF_{r_i^2}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{r_i^2}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

Let $WF_{r_i^3} \triangleq \{\tau_{r_i^3, k} \mid k \in \{2, 5, 8\}\}$ and
 $SF_{r_i^3} \triangleq \{\tau_{r_i^3, k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{r_i^3} \triangleq \bigwedge_{\tau \in WF_{r_i^3}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{r_i^3}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

The following holds:

$$\mathcal{S}_3 \cap Hist(W_3) \models L_{r_i^3} \rightarrow L_{r_i^2} [\bar{f}/X_2]$$

since $\tau_{r_i^2, 1}$ is relatively refined by $\tau_{r_i^3, 1}$ and $\tau_{r_i^3, 2}$, and $\tau_{r_i^2, 2}$ is relatively refined by $\tau_{r_i^3, 3}$, and $\tau_{r_i^2, 3}$ is relatively refined by $\tau_{r_i^3, 4}$, and $\tau_{r_i^2, 4}$ is relatively refined by $\tau_{r_i^3, 5}$, and $\tau_{r_i^2, 5}$ is relatively refined by $\tau_{r_i^3, 6}$, and $\tau_{r_i^2, 6}$ is relatively refined by $\tau_{r_i^3, 7}$, and $\tau_{r_i^2, 7}$ is relatively refined by $\tau_{r_i^3, 8}$, and $\tau_{r_i^2, 8}$ is relatively refined by $\tau_{r_i^3, 9}$, and $\tau_{r_i^2, 9}$ is relatively refined by $\tau_{r_i^3, 10}$, and $\tau_{r_i^2, 10}$ is relatively refined by $\tau_{r_i^3, 11}$, and $\tau_{r_i^2, 11}$ is relatively refined by $\tau_{r_i^3, 12}$, and $\tau_{r_i^2, 12}$ is relatively refined by $\tau_{r_i^3, 13}$, and $\tau_{r_i^2, 13}$ is relatively refined by $\tau_{r_i^3, 14}$, and $\tau_{r_i^2, 14}$ is relatively refined by $\tau_{r_i^3, 15}$,

So

$$\mathcal{S}_3 \cap Hist(W_3) \models L_{r_i^2} [\bar{f}/X_2]$$

ad (2) Analogue to the proof of (1).

ad (3) This is trivial because $W_3 \leftrightarrow (\bigwedge_{i=1}^N W_{r_i^3} \wedge \bigwedge_{j=1}^M W_{w_j^3})$.

3.6 The fourth development step

We have already seen how we can prevent reader_i³ to choose wrongly between $V(r)$ and $V(w)$. Dijkstra also updates the PV-segments in such a way that only statements that are actually executed are listed. It turns out that we do not anymore need **bm**. Also the guards of CHOOSE get simpler. The result of this transformation is:

```

readeri4:
  do true → NCS;
    P(m); br:=br+1; if aw>0 → V(m) [] aw=0 → V(r) fi;
    P(r); br, ar:=br-1, ar+1;
    if br=0 → V(m) [] br>0 → V(r) fi;
    READ;
    P(m); ar:=ar-1;

```

```

        if ar>0 ∨ bw=0 → V(m) [] ar=0 ∧ bw>0 → V(w) fi
    od

writerj4:
    do true → NCS;
        P(m); bw:=bw+1;
        if aw>0 ∨ ar>0 → V(m) [] aw=0 ∧ ar=0 → V(w) fi;
        P(w); bw, aw:=bw-1, aw+1; V(m);
        WRITE;
        P(m); aw:=aw-1;
        if br=0 ∧ bw=0 → V(m) [] br>0 → V(r) [] bw>0 → V(w) fi
    od

Syn4 : ||i=1N readeri4 || ||j=1M writerj4

```

In the following sections the DTL machine specifications $\mathcal{S}_{r_i^4}$ (corresponding to program reader_i^4) and $\mathcal{S}_{w_j^4}$ (corresponding to program writer_j^4) are given. It should be clear that the extra requirement W_4 is equal to **true** because no further requirements are imposed on \mathcal{S}_4 .

3.6.1 Specification $\mathcal{S}_{r_i^4}$

The formal specification $\mathcal{S}_{r_i^4} \triangleq (B_{r_i^4}, H_{r_i^4})$ where $H_{r_i^4} \triangleq I_{r_i^4} \wedge \square T_{r_i^4} \wedge L_{r_i^4}$ and $B_{r_i^4}, I_{r_i^4}, T_{r_i^4}$ and $L_{r_i^4}$ are as follows:

1. **Basis** $B_{r_i^4} = ((\text{In}_{r_i^4}, \text{Out}_{r_i^4}), (\text{V}_{r_i^4}, \text{X}_{r_i^4}))$

$$\begin{aligned}
 \text{In}_{r_i^4} &\triangleq \emptyset, \\
 \text{Out}_{r_i^4} &\triangleq \emptyset, \\
 \text{V}_{r_i^4} &\triangleq \{\mathbf{m}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}\}, \\
 \text{X}_{r_i^4} &\triangleq \{\ell_{r_i^4}\}
 \end{aligned}$$

- $\ell_{r_i^4} = 0$: reader_i^4 is non critical.
- $\ell_{r_i^4} = 6$: reader_i^4 executes first P-action on **m**.
- $\ell_{r_i^4} = 7$: reader_i^4 has updated **br**.
- $\ell_{r_i^4} = 8$: reader_i^4 has left first PV-section.
- $\ell_{r_i^4} = 1$: reader_i^4 has executed P-action on **r**.
- $\ell_{r_i^4} = 2$: reader_i^4 has updated **br** and **ar**.
- $\ell_{r_i^4} = 3$: reader_i^4 is critical.
- $\ell_{r_i^4} = 4$: reader_i^4 has executed second P-action **m**.
- $\ell_{r_i^4} = 5$: reader_i^4 has updated **ar**.

3.6 The fourth development step

Let $\Psi_4 \triangleq (\mathbf{m}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{r_i}, \ell_{r_i^4})$ and
 $\Psi'_4 \triangleq (\mathbf{m}', \mathbf{r}', \mathbf{br}', \mathbf{w}', \mathbf{bw}', \mathbf{ar}', \mathbf{aw}', \mathbf{s}'_{r_i}, \ell'_{r_i^4})$.

2. Initial States:

$$I_{r_i^4} \triangleq \Psi_4 = (1, 0, 0, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\text{Let } CHO1(X) \triangleq \bigvee (\mathbf{aw} > 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{r_i^4}]) \\ \bigvee (\mathbf{aw} = 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{r}, \ell_{r_i^4}])$$

$$\text{Let } CHO2(X) \triangleq \bigvee (\mathbf{br} = 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{r_i^4}]) \\ \bigvee (\mathbf{br} > 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{r}, \ell_{r_i^4}])$$

$$\text{Let } CHO3(X) \triangleq \bigvee ((\mathbf{ar} > 0 \vee \mathbf{bw} = 0) \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{r_i^4}]) \\ \bigvee (\mathbf{ar} = 0 \wedge \mathbf{bw} > 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{w}, \ell_{r_i^4}])$$

$$T_{r_i^4} \triangleq$$

$$\tau_{r_i^4,1} \quad (\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, \mathbf{m}) = (0, 1) \wedge \Psi'_4 = \Psi_4 [0, 6/\mathbf{m}, \ell_{r_i^4}])$$

Reader_i⁴ executes its first P-action on \mathbf{m} .

$$\tau_{r_i^4,2} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 6 \wedge \Psi'_4 = \Psi_4 [\mathbf{br} + 1, 8/\mathbf{br}, \ell_{r_i^4}])$$

Reader_i⁴ updates \mathbf{br} .

$$\tau_{r_i^4,3} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 8 \wedge CHO1(7))$$

Reader_i⁴ leaves the first PV-section.

$$\tau_{r_i^4,4} \quad \bigvee (\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, \mathbf{r}) = (7, 1) \wedge \Psi'_4 = \Psi_4 [0, 1/\mathbf{r}, \ell_{r_i^4}])$$

Reader_i⁴ executes its P-action on \mathbf{r} .

$$\tau_{r_i^4,5} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 1 \wedge \Psi'_4 = \Psi_4 [\mathbf{br} - 1, \mathbf{ar} + 1, 2/\mathbf{br}, \mathbf{ar}, \ell_{r_i^4}])$$

Reader_i⁴ updates \mathbf{br} and \mathbf{ar} .

$$\tau_{r_i^4,6} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 2 \wedge CHO2(3) \wedge \Psi'_4 = \Psi_4 [1/\mathbf{s}_{r_i}])$$

Reader_i⁴ becomes critical.

$$\tau_{r_i^4,7} \quad \bigvee (\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, \mathbf{m}) = (3, 1) \wedge \Psi'_4 = \Psi_4 [0, 4/\mathbf{m}, \ell_{r_i^4}])$$

Reader_i⁴ executes its second P-action on \mathbf{m} .

$$\tau_{r_i^4,8} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 4 \wedge \Psi'_4 = \Psi_4 [\mathbf{ar} - 1, 5/\mathbf{ar}, \ell_{r_i^4}])$$

Reader_i⁴ updates \mathbf{ar} .

$$\tau_{r_i^4,9} \quad \bigvee (\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 5 \wedge CHO3(0) \wedge \Psi'_4 = \Psi_4 [0/\mathbf{s}_{r_i}])$$

Reader_i⁴ becomes non critical.

$$\tau_{r_i^4,10} \quad \bigvee (\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_4 = \Psi_4 [0/\mathbf{m}])$$

The environment executes a P-operation on \mathbf{m} .

- $\tau_{r_i^4,11} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_4 = \Psi_4[0/\mathbf{r}])$
 The environment executes a P-operation on \mathbf{r} .
- $\tau_{r_i^4,12} \quad \vee (\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_4 = \Psi_4[0/\mathbf{w}])$
 The environment executes a P-operation on \mathbf{w} .
- $\tau_{r_i^4,13} \quad \vee (\epsilon = \mathbf{e} \wedge l_{r_i^4} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{m}])$
 The environment executes a V-operation on \mathbf{m} .
- $\tau_{r_i^4,14} \quad \vee (\epsilon = \mathbf{e} \wedge l_{r_i^4} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{r}])$
 The environment executes a V-operation on \mathbf{r} .
- $\tau_{r_i^4,15} \quad \vee (\epsilon = \mathbf{e} \wedge l_{r_i^4} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{w}])$
 The environment executes a V-operation on \mathbf{w} .
- $\tau_{r_i^4,0} \quad \vee \text{stut}_{r_i^4}$

These transitions are illustrated in figure 3.9

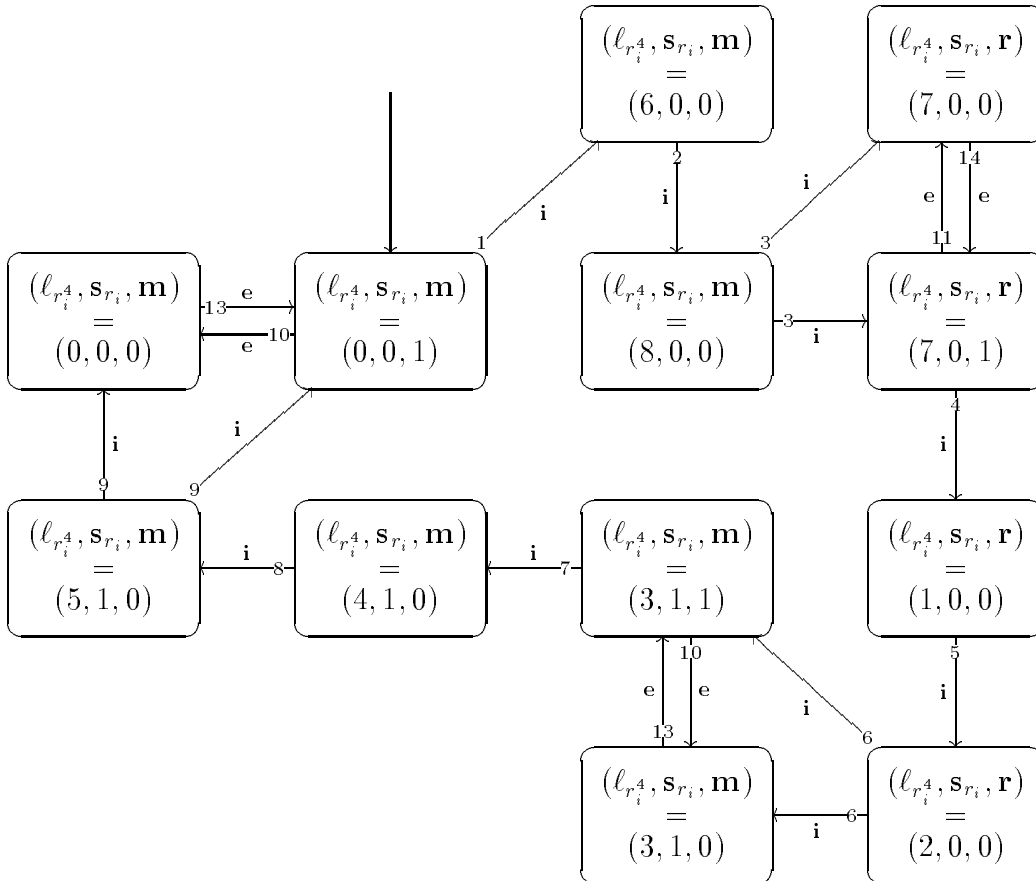


Figure 3.9: Transitions of reader_i⁴.

4. Liveness:

$L_{r_i^4}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are

3.6 The fourth development step

strongly fair and all the other transitions are weakly fair.

Let $\text{WF}_{r_i^4} \triangleq \{\tau_{r_{i,k}^4} \mid k \in \{2, 5, 8\}\}$ and

$\text{SF}_{r_i^4} \triangleq \{\tau_{r_{i,k}^4} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{r_i^4} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^4}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^4}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

3.6.2 Specification $\mathcal{S}_{w_j^4}$

The formal specification $\mathcal{S}_{w_j^4} \triangleq (B_{w_j^4}, H_{w_j^4})$ where $H_{w_j^4} \triangleq I_{w_j^4} \wedge \square T_{w_j^4} \wedge L_{w_j^4}$ and $B_{w_j^4}, I_{w_j^4}, T_{w_j^4}$ and $L_{w_j^4}$ are as follows:

1. **Basis** $B_{w_j^4} = ((\text{In}_{w_j^4}, \text{Out}_{w_j^4}), (\text{V}_{w_j^4}, \text{X}_{w_j^4}))$

$$\begin{aligned} \text{In}_{w_j^4} &\triangleq \emptyset, \\ \text{Out}_{w_j^4} &\triangleq \emptyset, \\ \text{V}_{w_j^4} &\triangleq \{\mathbf{m}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{w_j}\}, \\ \text{X}_{w_j^4} &\triangleq \{\ell_{w_j^4}\} \end{aligned}$$

- $\ell_{w_j^4} = 0$: writer_j^4 is non critical.
- $\ell_{w_j^4} = 6$: writer_j^4 executes first P-action on \mathbf{m} .
- $\ell_{w_j^4} = 8$: writer_j^4 has updated \mathbf{bw} .
- $\ell_{w_j^4} = 7$: writer_j^4 has left first PV-section.
- $\ell_{w_j^4} = 1$: writer_j^4 has executed P-action on \mathbf{w} .
- $\ell_{w_j^4} = 2$: writer_j^4 has updated \mathbf{bw} and \mathbf{aw} .
- $\ell_{w_j^4} = 3$: writer_j^4 is critical.
- $\ell_{w_j^4} = 4$: writer_j^4 has executed second P-action \mathbf{m} .
- $\ell_{w_j^4} = 5$: writer_j^4 has updated \mathbf{aw} .

Let $\Psi_4 \triangleq (\mathbf{m}, \mathbf{r}, \mathbf{br}, \mathbf{w}, \mathbf{bw}, \mathbf{ar}, \mathbf{aw}, \mathbf{s}_{w_j}, \ell_{w_j^4})$ and

$\Psi'_4 \triangleq (\mathbf{m}', \mathbf{r}', \mathbf{br}', \mathbf{w}', \mathbf{bw}', \mathbf{ar}', \mathbf{aw}', \mathbf{s}'_{w_j}, \ell'_{w_j^4})$.

2. **Initial States:**

$$I_{w_j^4} \triangleq \Psi_4 = (1, 0, 0, 0, 0, 0, 0, 0, 0)$$

3. Transitions:

$$\text{Let } CHO1(X) \triangleq \bigvee \left((\mathbf{aw} > 0 \vee \mathbf{ar} > 0) \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{w_j^4}] \right) \\ \bigvee \left(\mathbf{aw} = 0 \wedge \mathbf{ar} = 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{w}, \ell_{w_j^4}] \right)$$

$$\text{Let } CHO2(X) \triangleq \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{w_j^4}]$$

$$\text{Let } CHO3(X) \triangleq \bigvee \left(\mathbf{br} = 0 \wedge \mathbf{bw} = 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{m}, \ell_{w_j^4}] \right) \\ \bigvee \left(\mathbf{br} > 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{r}, \ell_{w_j^4}] \right) \\ \bigvee \left(\mathbf{bw} > 0 \wedge \Psi'_4 = \Psi_4 [1, X/\mathbf{w}, \ell_{w_j^4}] \right)$$

$$\mathbb{T}_{w_j^4} \triangleq$$

$$\tau_{w_{i,1}^4} \quad \left(\epsilon = \mathbf{i} \wedge (\ell_{w_j^4}, \mathbf{m}) = (0, 1) \wedge \Psi'_4 = \Psi_4 [0, 6/\mathbf{m}, \ell_{w_j^4}] \right)$$

Writer_j⁴ executes its first P-action on **m**.

$$\tau_{w_{i,2}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 6 \wedge \Psi'_4 = \Psi_4 [\mathbf{bw} + 1, 8/\mathbf{bw}, \ell_{w_j^4}] \right)$$

Writer_j⁴ updates **bw**.

$$\tau_{w_{i,3}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 8 \wedge CHO1(7) \right)$$

Writer_j⁴ leaves the first PV-section.

$$\tau_{w_{i,4}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge (\ell_{w_j^4}, \mathbf{r}) = (7, 1) \wedge \Psi'_4 = \Psi_4 [0, 1/\mathbf{w}, \ell_{w_j^4}] \right)$$

Writer_j⁴ executes its P-action on **w**.

$$\tau_{w_{i,5}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 1 \wedge \Psi'_4 = \Psi_4 [\mathbf{bw} - 1, \mathbf{aw} + 1, 2/\mathbf{bw}, \mathbf{aw}, \ell_{w_j^4}] \right)$$

Writer_j⁴ updates **bw** and **aw**.

$$\tau_{w_{i,6}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 2 \wedge CHO2(3) \wedge \Psi'_4 = \Psi_4 [1/\mathbf{s}_{w_j}] \right)$$

Writer_j⁴ becomes critical.

$$\tau_{w_{i,7}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge (\ell_{w_j^4}, \mathbf{m}) = (3, 1) \wedge \Psi'_4 = \Psi_4 [0, 4/\mathbf{m}, \ell_{w_j^4}] \right)$$

Writer_j⁴ executes its second P-action on **m**.

$$\tau_{w_{i,8}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 4 \wedge \Psi'_4 = \Psi_4 [\mathbf{aw} - 1, 5/\mathbf{aw}, \ell_{w_j^4}] \right)$$

Writer_j⁴ updates **aw**.

$$\tau_{w_{i,9}^4} \quad \bigvee \left(\epsilon = \mathbf{i} \wedge \ell_{w_j^4} = 5 \wedge CHO3(0) \wedge \Psi'_4 = \Psi_4 [0/\mathbf{s}_{w_j}] \right)$$

Writer_j⁴ becomes non critical.

$$\tau_{w_{i,10}^4} \quad \bigvee \left(\epsilon = \mathbf{e} \wedge \mathbf{m} = 1 \wedge \Psi'_4 = \Psi_4 [0/\mathbf{m}] \right)$$

The environment executes a P-operation on **m**.

$$\tau_{w_{i,11}^4} \quad \bigvee \left(\epsilon = \mathbf{e} \wedge \mathbf{r} = 1 \wedge \Psi'_4 = \Psi_4 [0/\mathbf{r}] \right)$$

The environment executes a P-operation on **r**.

$$\tau_{w_{i,12}^4} \quad \bigvee \left(\epsilon = \mathbf{e} \wedge \mathbf{w} = 1 \wedge \Psi'_4 = \Psi_4 [0/\mathbf{w}] \right)$$

The environment executes a P-operation on **w**.

3.6 The fourth development step

$$\tau_{w_{i,13}}^4 \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^4} \in \{0, 7, 3\} \wedge \mathbf{m} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{m}])$$

The environment executes a V-operation on \mathbf{m} .

$$\tau_{w_{i,14}}^4 \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^4} \in \{0, 7, 3\} \wedge \mathbf{r} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{r}])$$

The environment executes a V-operation on \mathbf{r} .

$$\tau_{w_{i,15}}^4 \quad \vee \quad (\epsilon = \mathbf{e} \wedge l_{w_j^4} \in \{0, 7, 3\} \wedge \mathbf{w} = 0 \wedge \Psi'_4 = \Psi_4[1/\mathbf{w}])$$

The environment executes a V-operation on \mathbf{w} .

$$\tau_{w_{i,0}}^4 \quad \vee \quad \mathbf{stut}_{w_j^4}$$

These transitions are illustrated in figure 3.10

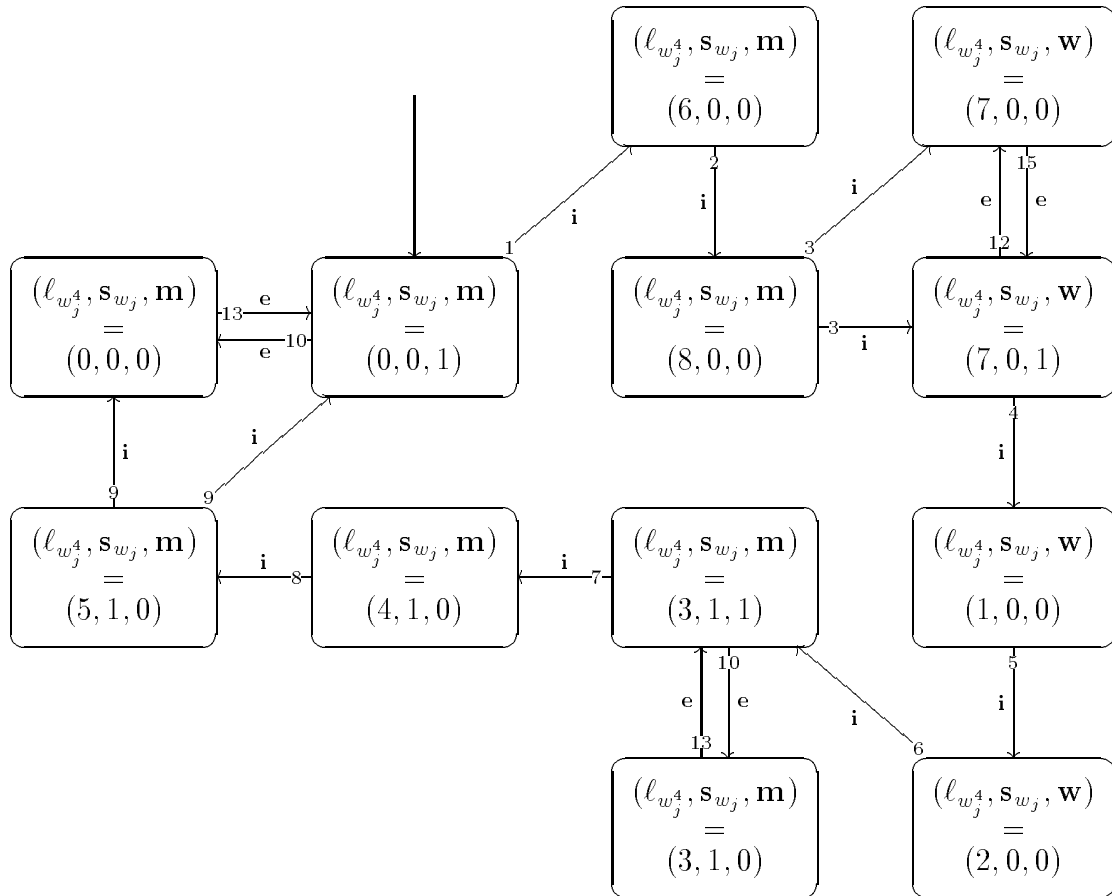


Figure 3.10: Transitions of writer_j^4 .

4. Liveness:

$L_{w_j^4}$ expresses that the P- and V-operations on the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} are strongly fair and all the other transitions are weakly fair.

Let $\text{WF}_{w_j^4} \triangleq \{\tau_{w_{j,k}}^4 \mid k \in \{2, 5, 8\}\}$ and

$SF_{w_j^4} \triangleq \{\tau_{w_j^4, k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{w_j^4} \triangleq \bigwedge_{\tau \in WF_{w_j^4}} (\diamond \square En(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in SF_{w_j^4}} (\square \diamond En(\tau) \rightarrow \square \diamond \tau)$$

3.6.3 \mathcal{S}_4 relatively refines \mathcal{S}_3

Since the semaphores \mathbf{m} , \mathbf{r} and \mathbf{w} , and the shared variables \mathbf{ar} , \mathbf{aw} , \mathbf{br} , \mathbf{bw} and \mathbf{bm} are used only by the subcomponents of \mathcal{S}_3 and the semaphores \mathbf{m} , \mathbf{w} and \mathbf{r} and the shared variables \mathbf{ar} , \mathbf{aw} , \mathbf{br} and \mathbf{bw} only by the subcomponents of \mathcal{S}_4 , we should prove $\mathcal{S}_4 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}\}$ relatively refines $\mathcal{S}_3 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{bm}, \mathbf{br}, \mathbf{bw}\}$. According to definition 35, 36 and theorem 8 $\mathcal{S}_4 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}\}$ relatively refines $\mathcal{S}_3 \upharpoonright \{\mathbf{m}, \mathbf{w}, \mathbf{r}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm}\}$ with respect to (W_4, W_3) iff the following holds:

$$\begin{aligned} & \mathfrak{D}(B_4) = \mathfrak{D}(B_3) \text{ and} \\ & \models (\exists X_4. (G_4 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw})' = (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw})))) \\ & \rightarrow \\ & (\exists X_3. (G_3 \wedge (\epsilon = \mathbf{e} \Rightarrow (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm})' = (\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bw}, \mathbf{bm})))) \end{aligned}$$

where X_4 are the local variables from \mathcal{S}_4 , i.e., $X_4 \triangleq \{\ell_{r_i^4} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^4} \mid j = 1, \dots, M\} \cup \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{aw}, \mathbf{ar}, \mathbf{br}, \mathbf{bw}\}$ and G_4 is the composition of $\mathcal{S}_{r_i^4}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^4}$ ($j = 1, \dots, M$),

let $\bar{\epsilon}_4 \triangleq \epsilon_{4,1}, \dots, \epsilon_{4,N}, \epsilon_{4,N+1}, \dots, \epsilon_{4,N+M}$, and
 let $\bar{B}_4^A \triangleq B_{r_1^4}^A, \dots, B_{r_N^4}^A, B_{w_1^4}^A, \dots, B_{w_M^4}^A$
 then $G_4 \triangleq$

$$\left(\exists \bar{\epsilon}_4. \odot_{\bar{B}_4^A} (\epsilon, \bar{\epsilon}_4) \wedge \bigwedge_{i=1}^N \mathbf{H}_{r_i^4} [\epsilon_{4,i}/\epsilon] \wedge \bigwedge_{j=1}^M \mathbf{H}_{w_j^4} [\epsilon_{4,N+j}/\epsilon] \right)$$

X_3 are the local variables from \mathcal{S}_3 , i.e., $X_3 \triangleq \{\ell_{r_i^3} \mid i = 1, \dots, N\} \cup \{\ell_{w_j^3} \mid j = 1, \dots, M\} \cup \{\mathbf{m}, \mathbf{r}, \mathbf{w}, \mathbf{ar}, \mathbf{aw}, \mathbf{br}, \mathbf{bm}, \mathbf{bw}\}$ and G_3 is the composition of $\mathcal{S}_{r_i^3}$ ($i = 1, \dots, N$) and $\mathcal{S}_{w_j^3}$ ($j = 1, \dots, M$) and W_3 ,

let $\bar{\epsilon}_3 \triangleq \epsilon_{3,1}, \dots, \epsilon_{3,N}, \epsilon_{3,N+1}, \dots, \epsilon_{3,N+M}$, and
 let $\bar{B}_3^A \triangleq B_{r_1^3}^A, \dots, B_{r_N^3}^A, B_{w_1^3}^A, \dots, B_{w_M^3}^A$
 then $G_3 \triangleq$

$$\left(\exists \bar{\epsilon}_3. \odot_{\bar{B}_3^A} (\epsilon, \bar{\epsilon}_3) \wedge \bigwedge_{i=1}^N \mathbf{H}_{r_i^3} [\epsilon_{3,i}/\epsilon] \wedge \bigwedge_{j=1}^M \mathbf{H}_{w_j^3} [\epsilon_{3,N+j}/\epsilon] \right) \wedge W_3$$

As seen in the previous development step W_3 is ϵ -free and can be decomposed into sub-requirements. Let $p_{ri} \triangleq (\ell_{r_i^3} \in \{0, 7, 3\} \rightarrow q_3)$ and $W_{r_i^3} \triangleq \square p_{ri}$, and $p_{wj} \triangleq (\ell_{w_j^3} \in \{0, 7, 3\} \rightarrow q_3)$ and $W_{w_j^3} \triangleq \square p_{wj}$ then $W_3 = (\bigwedge_{i=1}^N W_{r_i^3}) \wedge (\bigwedge_{j=1}^M W_{w_j^3})$. Now Lemma 9, 10 and 11 can be used for the proof, i.e., following proof rule can be used

$$\frac{\begin{array}{l} \bigcap_{i=1}^N W_{r_i^3} \cap \bigcap_{j=1}^M W_{w_j^3} \subseteq W_3 \\ \mathcal{S}_{r_i^3} \ W_{r_i^3} \ \mathbf{ref} \ W_2 \ \mathcal{S}_{r_i^2} \quad W_{r_i^3} \text{ constraining } B_{r_i^3} \\ \mathcal{S}_{w_j^3} \ W_{w_j^3} \ \mathbf{ref} \ W_2 \ \mathcal{S}_{w_j^2} \quad W_{w_j^3} \text{ constraining } B_{w_j^3} \end{array}}{\mathcal{S}_4 \ \mathcal{H} \ \mathbf{ref} \ W_2 \ \mathcal{S}_3}$$

3.6 The fourth development step

This means we have to prove for $i = 1, \dots, N$ and $j = 1, \dots, M$:

- (1) $(\exists X_{r_i^4} \cdot (\mathbf{H}_{r_i^4})) \rightarrow (\exists X_{r_i^3} \cdot (\mathbf{H}_{r_i^3} \wedge \mathbf{W}_{r_i^3}))$
- (2) $(\exists X_{w_j^4} \cdot (\mathbf{H}_{w_j^4})) \rightarrow (\exists X_{w_j^3} \cdot (\mathbf{H}_{w_j^3} \wedge \mathbf{W}_{w_j^3}))$
- (3) $(\mathbf{W}_{r_i^3} \wedge \mathbf{W}_{w_j^3}) \rightarrow \mathbf{W}_3$

ad (1) Rule 3 will be used to prove (1). This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_4 to \mathcal{S}_3 , defined as:

$\bar{f} = f_{\ell_{r_i^3}}, f_m, f_r, f_w, f_{aw}, f_{ar}, f_{br}, f_{bw}, f_{bm}$ where f_{bm} is defined as

$$N + M - br - bw$$

, i.e., bm can be expressed in terms of br and bw . Note: the refinement mappings for $ltr4$, m , r , w , aw , ar , br and bw are equal to the identity mapping, so we can leave them out.

- (a) $\mathcal{S}_4 \models I_{r_i^4} \rightarrow (I_{r_i^3} \wedge p_{ri}) [\bar{f}/X_3]$
- (b) $\mathcal{S}_3 \models T_{r_i^4} \rightarrow (T_{r_i^3} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^3})) [\bar{f}/X_3]$
- (c) $\mathcal{S}_3 \models L_{r_i^3} [\bar{f}/X_3]$

(a) **Proof 7**

$$\begin{aligned}
& I_{r_i^4} \\
= & \quad \% \text{ Def. } I_{r_i^4} \\
& (m, r, br, w, bw, ar, aw, s_{r_i}, \ell_{r_i^3}) = (1, 0, 0, 0, 0, 0, 0, 0, 0) \\
\rightarrow & \quad \% \text{ Def. } f_{bm} \\
& ((m, bm, r, br, w, bw, ar, aw, s_{r_i}, \ell_{r_i^3}) = \\
& \quad (1, N + M, 0, 0, 0, 0, 0, 0, 0) \wedge p_{ri}) [\bar{f}/X_3] \\
= & \quad \% \text{ Def. } I_{r_i^3} \\
& (I_{r_i^3} \wedge p_{ri}) [\bar{f}/X_3]
\end{aligned}$$

(b) **Proof 8**

$T_{r_i^3}$ is of the form $\mathbf{stut}_{r_i^3} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau})$ so $T_{r_i^3} \wedge ((p_{ri} \wedge p'_{ri}) \vee \mathbf{stut}_{r_i^3})$ is equal to $\mathbf{stut}_{r_i^3} \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathit{trans}_{\tau} \wedge p_{ri} \wedge p'_{ri})$.

$$\begin{aligned}
- & \\
= & \quad \tau_{r_{i,1}^4} \\
& \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, m) = (0, 1) \wedge \Psi_4 = \Psi_4 [0, 6/m, \ell_{r_i^4}] \right) \\
\rightarrow & \quad \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, m) = (0, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi_3 = \Psi_3 [0, 6/m, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
= & \quad (\tau_{r_{i,1}^3} \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

The first P -operation of reader $_i^4$ corresponds to the first P -operation of reader $_i^3$.

$$\begin{aligned}
 & - \quad \tau_{i,2}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 6 \wedge \Psi'_4 = \Psi_4 [\text{br} + 1, 8/\text{br}, \ell_{r_i^4}] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 6 \wedge p_{ri} \right. \\
 & \quad \left. p'_{ri} \wedge \Psi'_3 = \Psi_3 [\text{bm} - 1, \text{br} + 1, 8/\text{bm}, \text{br}, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,2}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

The updating of br in reader_i^4 corresponds to the updating of br and bm in reader_i^3 .

$$\begin{aligned}
 & - \quad \tau_{i,3}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 8 \wedge CHO1(7) \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 8 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(7) \right) [\bar{f}/X_3] \\
 & = (\tau_{i,3}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

The first V -operation of reader_i^4 corresponds to the first V -operation of reader_i^3 .

$$\begin{aligned}
 & - \quad \tau_{i,4}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, r) = (7, 1) \wedge \Psi'_4 = \Psi_4 [0, 1/r, \ell_{r_i^4}] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, r) = (7, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0, 1/r, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,4}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

The second P -operation of reader_i^4 corresponds to the second P -operation of reader_i^3 .

$$\begin{aligned}
 & - \quad \tau_{i,5}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 1 \wedge \Psi'_4 = \Psi_4 [\text{br} - 1, \text{ar} + 1, 2/\text{br}, \text{ar}, \ell_{r_i^4}] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 1 \wedge p_{ri} \right. \\
 & \quad \left. \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [\text{br} - 1, \text{ar} + 1, \text{bm} + 1, 2/\text{br}, \text{ar}, \text{bm}, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,5}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

The ar decrement step of reader_i^4 corresponds to the ar decrement step of reader_i^3 .

$$\begin{aligned}
 & - \quad \tau_{i,6}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 2 \wedge CHO2(3) \wedge \Psi'_4 = \Psi_4 [1/s_{r_i}] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 2 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(3) \wedge \Psi'_3 = \Psi_3 [1/s_{r_i}] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,6}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

If reader_i^4 becomes critical then reader_i^3 becomes critical.

$$\begin{aligned}
 & - \quad \tau_{i,7}^4 \\
 & = \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^4}, m) = (3, 1) \wedge \Psi'_4 = \Psi_4 [0, 4/m, \ell_{r_i^4}] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{i} \wedge (\ell_{r_i^3}, m) = (3, 1) \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0, 4/m, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,7}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

The third P -operation of reader_i^4 corresponds to the third P -operation of reader_i^3 .

3.6 The fourth development step

$$\begin{aligned}
- & \tau_{i,8}^4 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 4 \wedge \Psi'_4 = \Psi_4 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^4}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 4 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [\text{ar} - 1, 5/\text{ar}, \ell_{r_i^3}] \right) [\bar{f}/X_3] \\
& = (\tau_{i,8}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

The ar decrement step of reader_i⁴ corresponds to the ar decrement step of reader_i³.

$$\begin{aligned}
- & \tau_{i,9}^4 \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^4} = 5 \wedge CHO3(0) \wedge \Psi'_4 = \Psi_4 [0/s_{r_i}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \ell_{r_i^3} = 5 \wedge p_{ri} \wedge p'_{ri} \wedge CHO(0) \wedge \Psi'_3 = \Psi_3 [0/s_{r_i}] \right) [\bar{f}/X_3] \\
& = (\tau_{i,9}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

The third V-operation of reader_i⁴ corresponds to the third V-operation of reader_i³.

$$\begin{aligned}
- & \tau_{i,10}^4 \\
& = \left(\epsilon = \mathbf{e} \wedge m = 1 \wedge \Psi'_4 = \Psi_4 [0/m] \right) \\
& \rightarrow \left(\epsilon = \mathbf{e} \wedge m = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/m] \right) [\bar{f}/X_3] \\
& = (\tau_{i,10}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

If the environment of reader_i⁴ executes a P-operation then the environment of reader_i³ also executes a P-operation.

$$\begin{aligned}
- & \tau_{i,11}^4 \\
& = \left(\epsilon = \mathbf{e} \wedge r = 1 \wedge \Psi'_4 = \Psi_4 [0/r] \right) \\
& \rightarrow \left(\epsilon = \mathbf{e} \wedge r = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/r] \right) [\bar{f}/X_3] \\
& = (\tau_{i,11}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

If the environment of reader_i⁴ executes a P-operation then the environment of reader_i³ also executes a P-operation.

$$\begin{aligned}
- & \tau_{i,12}^4 \\
& = \left(\epsilon = \mathbf{e} \wedge w = 1 \wedge \Psi'_4 = \Psi_4 [0/w] \right) \\
& \rightarrow \left(\epsilon = \mathbf{e} \wedge w = 1 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [0/w] \right) [\bar{f}/X_3] \\
& = (\tau_{i,12}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

If the environment of reader_i⁴ executes a P-operation then the environment of reader_i³ also executes a P-operation.

$$\begin{aligned}
- & \tau_{i,13}^4 \\
& = \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^4} \in \{0, 7, 3\} \wedge m = 0 \wedge \Psi'_4 = \Psi_4 [1/m] \right) \\
& \rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge m = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3 [1/m] \right) [\bar{f}/X_3] \\
& = (\tau_{i,13}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
\end{aligned}$$

If the environment of reader_i⁴ executes a V-operation then the environment of reader_i³ also executes a V-operation.

$$\begin{aligned}
 & - \tau_{i,14}^4 \\
 & = \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^4} \in \{0, 7, 3\} \wedge r = 0 \wedge \Psi'_4 = \Psi_4[1/r] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^3} \in \{0, 7, 3\} \wedge r = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3[1/r] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,14}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

If the environment of reader_i⁴ executes a V-operation then the environment of reader_i³ also executes a V-operation.

$$\begin{aligned}
 & - \tau_{i,15}^4 \\
 & = \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^4} \in \{0, 7, 3\} \wedge w = 0 \wedge \Psi'_4 = \Psi_4[1/w] \right) \\
 & \rightarrow \left(\epsilon = \mathbf{e} \wedge \ell_{r_i^2} \in \{0, 7, 3\} \wedge w = 0 \wedge p_{ri} \wedge p'_{ri} \wedge \Psi'_3 = \Psi_3[1/w] \right) [\bar{f}/X_3] \\
 & = (\tau_{i,15}^3 \wedge p_{ri} \wedge p'_{ri}) [\bar{f}/X_3]
 \end{aligned}$$

If the environment of reader_i⁴ executes a V-operation then the environment of reader_i³ also executes a V-operation.

$$\begin{aligned}
 & - \text{stut}_{r_i^4} \rightarrow \text{stut}_{r_i^3} [\bar{f}/X_3] \\
 & \text{since } s_{r_i} \text{ doesn't change.}
 \end{aligned}$$

(c) Let $\text{WF}_{r_i^4} \triangleq \{\tau_{r_i^4, k} \mid k \in \{2, 5, 8\}\}$ and

$\text{SF}_{r_i^4} \triangleq \{\tau_{r_i^4, k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{r_i^4} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^4}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^4}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

Let $\text{WF}_{r_i^3} \triangleq \{\tau_{r_i^3, k} \mid k \in \{2, 5, 8\}\}$ and

$\text{SF}_{r_i^3} \triangleq \{\tau_{r_i^3, k} \mid k \in \{1, 3, 4, 6, 7, 9, 10, 11, 12, 13, 14, 15\}\}$ then

$$L_{r_i^3} \triangleq \bigwedge_{\tau \in \text{WF}_{r_i^3}} (\diamond \square \text{En}(\tau) \rightarrow \square \diamond \tau) \wedge \bigwedge_{\tau \in \text{SF}_{r_i^3}} (\square \diamond \text{En}(\tau) \rightarrow \square \diamond \tau)$$

The following holds:

$$\mathcal{S}_4 \models L_{r_i^4} \rightarrow L_{r_i^3} [\bar{f}/X_3]$$

since $\tau_{r_i^3, k}$ is relatively refined by $\tau_{r_i^4, k}$ for $k = 0, \dots, 15$.

So

$$\mathcal{S}_4 \models L_{r_i^3} [\bar{f}/X_3]$$

ad (2) Analogue to the proof of (1).

ad (3) This is trivial because $W_3 \leftrightarrow (\bigwedge_{i=1}^N W_{r_i^3} \wedge \bigwedge_{j=1}^M W_{w_j^3})$.

Chapter 4

Stable Storage Example

This chapter first introduces in sect. 4.1 a *general methodology* for proving fault tolerant systems correct. This general methodology uses the relative refinement concept of sect. 2.3.2. The remaining sections of this chapter give an illustration of this general methodology by applying it to a fault tolerant system consisting of a number of disks implementing stable storage. Section 4.2 introduces this application. In sections 4.3, 4.4, 4.5 and 4.6 the four steps of this general methodology are applied to the stable storage example [Cri85, Sch91].

4.1 The General Methodology

The general methodology consists of four steps. In the first step one gives the abstract specification $\mathcal{S} \triangleq (B, H)$ where H is a DTL formula specifying the fault tolerant system. In this specification no faults are visible, hence they don't occur as observables. The designer's task is to give an implementation of this system under the assumption that only faults from certain classes can occur. These faults are called *anticipated faults*. These are faults which may affect the implementation in that they may give rise to errors in the state of the implementation, resulting subsequently in failures of that implementation. In step 2,3 and 4 of the methodology a fault-tolerant system is developed.

The second step *identifies* the anticipated faults which can affect an implementation $\mathcal{S}_P \triangleq (B_P, H_P)$. This implementation serves as first approximation to the final implementation of \mathcal{S} . It should be clear that \mathcal{S}_P is not a refinement of \mathcal{S} because of the possible occurrences of anticipated faults. \mathcal{S}_P is only a refinement when these faults do not occur, i.e., \mathcal{S}_P is a *relative refinement* of \mathcal{S} . So in step 2 we must prove:

$$(1) \quad \mathcal{S}_P \text{ } w_P \text{ ref } \mathcal{S}$$

In the third step one specifies *how these anticipated faults are detected*, i.e., one has to specify a detection layer \mathcal{S}_{D_s} for these faults. This layer is added in bottom-up fashion to the implementation \mathcal{S}_P of the second step and stops upon detection of the first error, i.e., \mathcal{S}_{D_s} is a *fail-stop* implementation. So the second approximation to the final implementation consists of the composition of \mathcal{S}_P and \mathcal{S}_{D_s} . This approximation is clearly not a refinement because when in \mathcal{S}_P a fault occurs, and \mathcal{S}_{D_s} detects the corresponding error, the whole

approximation stops. One would like to have (eventually) an approximation that doesn't stop, i.e., the physical disk isn't affected by faults and the detection layer should detect no error. Let $\overline{W} \triangleq (W_{D_s}, W_P)$ where W_P expresses that no faults occur and W_{D_s} expresses that no error is detected. Then we must prove the following:

$$(2) \quad \mathcal{S}_{D_s} \mid \overline{W} \mid \mathcal{S}_P \mathbf{ref}^{W_P} \mathcal{S}_P.$$

From (1), (2) and the transitivity of relative refinement relation follows:

$$\mathcal{S}_{D_s} \mid \overline{W} \mid \mathcal{S}_P \mathbf{ref} \mathcal{S}.$$

In the fourth step one *specifies the corrective action to be undertaken after detection of an error*. This means in general that one needs *redundancy*, i.e., several copies of \mathcal{S}_P and \mathcal{S}_D components, because when a detection layer \mathcal{S}_D detects an error, the state before that error has to be recovered and that can only be done by accessing another copy of \mathcal{S}_P through its corresponding detection layer \mathcal{S}_D . Note that the \mathcal{S}_D component doesn't stop anymore on the detection of an error but merely waits for the corrective action to be undertaken. Say, we need N copies of \mathcal{S}_P and \mathcal{S}_D . The final implementation consists then of those N copies of \mathcal{S}_P and \mathcal{S}_D plus a recovery layer \mathcal{S}_R . Let W_R express which kind of errors can be recovered. If the following holds:

$$(3) \quad \parallel_{i=1}^N (\mathcal{S}_{P_i} \parallel \mathcal{S}_{D_i}) \parallel \mathcal{S}_R \mid W_R \mathbf{ref} \mathcal{S}_{D_s} \mid \overline{W} \mid \mathcal{S}_P$$

then from (1), (2), (3) and the transitivity of relative refinement follows the desired result, i.e.:

$$\parallel_{i=1}^N (\mathcal{S}_{P_i} \parallel \mathcal{S}_{D_i}) \parallel \mathcal{S}_R \mid W_R \mathbf{ref} \mathcal{S}$$

This ends our exposition of the general methodology. In the next sections this methodology will be applied to a stable storage example.

4.2 Application: Introduction

Stable storage is defined as follows. A disk is used to store and retrieve data. During these operations some faults can occur in the underlying hardware. To make the disk more reliable one introduces layers for the detection and correction of errors, due to these faults. The system with these detection and correction layers is called "stable storage". This stable storage is a fault tolerant system because it stores and retrieves data in a reliable way under the assumption that faults from a certain class are recovered (corrected). This class consists of two kinds of faults. The first one consists of faults that damage the disk surface -the contents of the disk are said to be corrupted by these faults. The second one consists of faults that affect the disk control system, and results into the contents of the disk being read from or written to the wrong location. Notice that other kinds of faults, such as power failure or physical destruction of the whole stable storage system, are not taken into account. I.e., stable storage should function correctly provided such latter faults do not occur.

4.3 First Step: Stable Storage

4.3.1 Introduction

In this section we give a specification of a stable storage system as we ideally would like to have it. So no faults are observed. If they occur internally, they should be repaired by the system without leaving any observable trace. For that is the meaning of ‘stable’ here!

4.3.2 Specification

The abstract specification of the stable storage specifies the following: The user signals with a read request event that he wants to read the contents of some location of stable storage. Stable storage will then respond by sending the requested contents. The user signals with a write request event that some data has to be written on some location of stable storage, with a response event the stable storage signals that the write has been performed. Note: we have a very simple stable storage that can handle only one request at a time. The formal specification $\mathcal{S} = (B, H)$ where $H \triangleq I \wedge \Box T \wedge L$ and B, I, T and L are as follows:

1. **Basis** $B = ((In, Out), (V, X))$

$$\begin{aligned} In &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}\}, \\ Out &\triangleq \{\mathbf{Rres}, \mathbf{Wres}\}, \\ V &\triangleq \emptyset, \\ X &\triangleq \{\ell, r, s, M[n] \mid n \in SN\} \end{aligned}$$

where SN is the set of sector numbers: $[1, \dots, Z]$. Let Inf be the set of information items that could be stored and retrieved by stable storage but that will not be further specified. For $n \in SN$ and $c, d \in Inf$:

- $\mathbf{Rreq}?(n)$: the request to read sector n .
- $\mathbf{Rres}!(c)$: the response to the previous read request where c are the contents of requested sector.
- $\mathbf{Wreq}?(d)$: write information item d onto sector n .
- $\mathbf{Wres}!$: previous write has been performed.
- ℓ : local variable indicating the status of the stable storage; $\ell = 0$ means no requests are issued, $\ell = 1$ means a read request has been issued, and $\ell = 2$ means a write request has been issued.
- r : local variable indicating the requested sector.
- s : local variable indicating the contents of the requested sector or the to be written data.
- $M[n]$: the physical sector n .

Let $\Psi_0 \triangleq (\ell, r, s, M[1], \dots, M[Z])$ and $\Psi'_0 \triangleq (\ell', r', s', M'[1], \dots, M'[Z])$.

2. Initial States:

$$I \triangleq \ell = 0 \wedge \bigwedge_{i \in SN} M[i] = dflt$$

Where $dflt \in Inf$ is some default information item.

3. Transitions:

$$T \triangleq$$

$$\tau_1 \quad \left(\epsilon = \mathbf{Rreq}?(n) \wedge \ell = 0 \wedge \Psi'_0 = \Psi_0[1, n/\ell, r] \right)$$

The user requests the contents of sector n .

$$\tau_2 \quad \vee \left(\epsilon = \mathbf{Rres}(M[r]) \wedge \ell = 1 \wedge \Psi'_0 = \Psi_0[0/\ell] \right)$$

Stable storage responds with the contents of the requested sector.

$$\tau_3 \quad \vee \left(\epsilon = \mathbf{Wreq}?(n, d) \wedge \ell = 0 \wedge \Psi'_0 = \Psi_0[2, n, d/\ell, r, s] \right)$$

The user requests that d should be written onto sector n .

$$\tau_4 \quad \vee \left(\epsilon = \mathbf{Wres}! \wedge \ell = 2 \wedge \Psi'_0 = \Psi_0[0, s/\ell, M[r]] \right)$$

Stable storage responds with a signal that requested write is performed.

$$\tau_0 \quad \vee \mathbf{stut}$$

These transitions are illustrated in figure 4.1

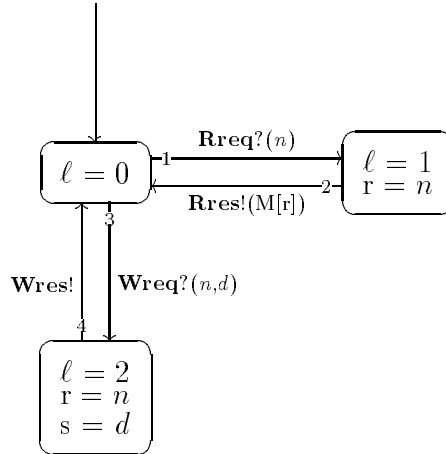


Figure 4.1: Transitions of stable storage.

4. Liveness condition:

The liveness condition expresses that the communication transitions are strongly fair.

Let $SF = \{\tau_i \mid i \in \{1, 2, 3, 4\}\}$ then

$$L \triangleq \bigwedge_{\tau \in SF} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

4.4 Second Step: Physical Disk

4.4.1 Introduction

In this step, which is the first stage in our task to develop a fault tolerant system, we give the specification of a physical disk. This specification is a first approximation to our fault tolerant system, i.e., it acts as bottom layer of our desired implementation and because the other layers haven't been developed yet it is the only layer we have at this moment. In this specification we must specify, because this is the first stage of our development, which are the anticipated faults our system, i.e., we have to specify which are the faults of our interest that could affect a physical disk. These faults are represented as events in our formalism. This first approximation of stable storage is not a correct one because of these anticipated faults (the physical disk doesn't anticipate on these faults at all!). But under the assumption that these faults don't occur this first implementation is a refinement of stable storage.

4.4.2 Specification

We must specify a physical disk, the anticipated faults and their impact on the physical disk. We take as anticipated faults the following ones (cf. [Cri85, Sch91]):

- Damages of the disk surface causing corruption of the contents of a physical sector.
- Disk control faults causing the contents of a particular physical sector to be read or written at a wrong location.

These two faults are described using two events: the *dam* event standing for a damage to the disk surface and the *csf* event standing for a disk control system fault. As in the specification of stable storage, the user requests with $\mathbf{Rreq}(n)$ that it wants to read the contents of physical sector n . The physical disk then responds with $\mathbf{Rres}(c)$ delivering the requested contents. With $\mathbf{Wreq}(n, d)$ the user signals that d should be written onto sector n . The physical disk responds with \mathbf{Wres} that the requested information has been written. The formal specification $\mathcal{S}_P = (B_P, H_P)$ where $H_P \triangleq I_P \wedge \square T_P \wedge L_P$ and B_P , I_P , T_P and L_P are as follows:

1. **Basis** $B_P = ((In_P, Out_P), (V_P, X_P))$

$$\begin{aligned}
 In_P &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}\}, \\
 Out_P &\triangleq \{\mathbf{Rres}, \mathbf{Wres}\}, \\
 V_P &\triangleq \emptyset, \\
 X_P &\triangleq \{\ell_P, r_P, s_P, M_P[n], F[n] \mid n \in PN\}
 \end{aligned}$$

where PN is the set of physical sector numbers: $[1, \dots, Y]$. Let Phy be the set of information items that could be stored and retrieved by the physical disk but that will not be further specified. The special information item \textcircled{c} is introduced to model disk surface damage faults. For $n \in PN$ and $c, d \in Phy$:

- **Rreq?**(n): the request to read sector n .
- **Res!**(c): the response to the previous request where c are the contents of requested sector.
- **Wreq?**(d): write information item d onto sector n .
- **Wres!**: response that previous write has been performed.
- ℓ_P : local variable indicating the status of the physical disk.
- r_P : local variable indicating the requested physical sector.
- s_P : local variable indicating the requested contents or the data to be written.
- $M_P[n]$: the physical sector n .
- F : the control system, i.e., the control system maps sector n to sector $F[n]$.

Let $\Psi_1 \triangleq (\ell_P, r_P, s_P, M_P[1], \dots, M_P[Y], F[1], \dots, F[Y])$ and $\Psi'_1 \triangleq (\ell'_P, r'_P, s'_P, M'_P[1], \dots, M'_P[Y], F'[1], \dots, F'[Y])$.

2. Initial States:

$$I_P \triangleq \ell_P = 0 \wedge \bigwedge_{i \in PN} (M_P[i] = \text{dflt} \wedge F[i] = i)$$

All sectors contain the default data item *dflt* and the control system has not been affected by control system faults.

3. Transitions:

$$T_P \triangleq$$

$$\tau_{P,1} \quad \left(\epsilon = \mathbf{Rreq?}(n) \wedge \ell_P = 0 \wedge \Psi'_1 = \Psi_1 [1, n/\ell_P, r_P] \right)$$

The user requests the contents of sector n .

$$\tau_{P,2} \quad \vee \left(\epsilon = \mathbf{Res!}(M_P[F[r_P]]) \wedge \ell_P = 1 \wedge \Psi'_1 = \Psi_1 [0/\ell_P] \right)$$

The physical disk responds with the contents of the requested sector.

$$\tau_{P,3} \quad \vee \left(\epsilon = \mathbf{Wreq?}(n, d) \wedge \ell_P = 0 \wedge \Psi'_1 = \Psi_1 [2, n, d/\ell_P, r_P, s_P] \right)$$

The user requests that d should be written onto sector n .

$$\tau_{P,4} \quad \vee \left(\epsilon = \mathbf{Wres!} \wedge \ell_P = 2 \wedge \Psi'_1 = \Psi_1 [0, s_P/\ell_P, M_P[F[r_P]]] \right)$$

The physical disk responds with a signal that requested write is performed.

$$\tau_{P,5} \quad \vee \left(\epsilon = \mathbf{i} \wedge n \neq j \wedge \Psi'_1 = \Psi_1 [j/F[n]] \right)$$

Due to control system fault the sector n is mapped to sector j .

$$\tau_{P,6} \quad \vee \left(\epsilon = \mathbf{i} \wedge \Psi'_1 = \Psi_1 [\textcircled{C}/M_P[n]] \right)$$

Due to disk surface fault the contents of sector n are replaced by corrupted data \textcircled{C} .

$$\tau_{P,0} \quad \vee \mathbf{stut}_P$$

These transitions are illustrated in figure 4.2 where *fault* is either a control system fault or a disk surface fault.

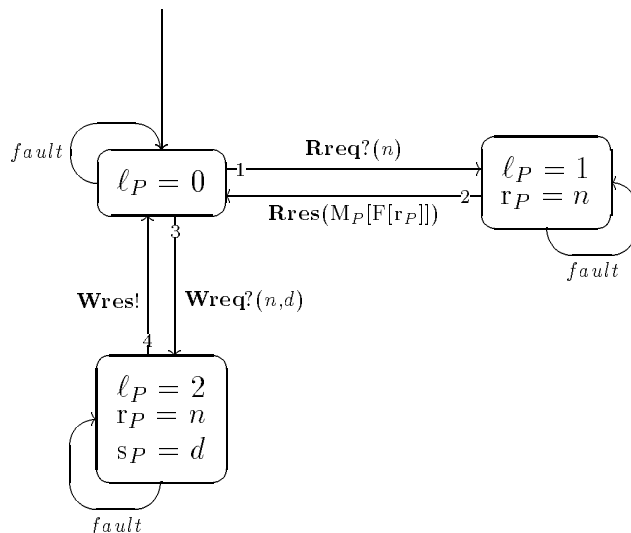


Figure 4.2: Transitions of the physical disk.

4. Liveness condition:

The liveness condition expresses that the communication transitions are strongly fair. Let $SF_P = \{\tau_{P,i} \mid i \in \{1, 2, 3, 4\}\}$ then

$$L_P \triangleq \bigwedge_{\tau \in SF_P} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

4.4.3 Requirement W_P

The requirement W_P expresses that the control system and disk surface faults never occur.

$$W_P \triangleq \Box \left(\bigwedge_{i \in PN} (M_P[i] \neq \odot \wedge F[i] = i) \right)$$

This corresponds to the following machine: Let

$$\begin{aligned} p &\triangleq \bigwedge_{i \in PN} (M_P[i] \neq \odot \wedge F[i] = i) \\ p' &\triangleq \bigwedge_{i \in PN} (M'_P[i] \neq \odot \wedge F'[i] = i) \end{aligned}$$

then W_P is equal to the machine $p \wedge \Box((p \wedge p') \vee stut_P)$.

4.4.4 \mathcal{S}_P relatively refines \mathcal{S}

We should prove \mathcal{S}_P relatively refines \mathcal{S} . Let the external requirement for the system \mathcal{S} be **true** (i.e., no extra requirement is imposed). According to theorem 8 \mathcal{S}_P relatively refines \mathcal{S} with respect to (W_P, W) iff the following holds:

$$\begin{aligned} \mathfrak{D}(B_P) &= \mathfrak{D}(B) \text{ and} \\ \models (\exists X_P. (G_P)) &\rightarrow (\exists X. (G)) \end{aligned}$$

where X_P are the local variables from \mathcal{S}_P , i.e., $X_P \triangleq \{\ell_P, r_P, s_P, M_P[n], F[n] \mid n \in PN\}$ and G_P is defined as

$$I_P \wedge \Box T_P \wedge L_P \wedge W_P$$

X are the local variables from \mathcal{S} , i.e., $X \triangleq \{\ell, r, s, M[n] \mid n \in SN\}$ and G is defined as

$$I \wedge \Box T \wedge L$$

Rule 3 will be used to prove

$$\models (\exists X_P. (G_P)) \rightarrow (\exists X. (G)).$$

This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_P to \mathcal{S} , defined as: $\bar{f} = f_\ell, f_r, f_s, f_{M[n]} \ (n \in SN)$. We will assume that the set of sector numbers SN is equal to the set of physical sector numbers PN . The refinement mappings are defined as:

$$\begin{aligned} f_\ell &\triangleq \ell_P \\ f_r &\triangleq r_P \\ f_s &\triangleq s_P \\ f_{M[n]} &\triangleq M_P[n] \end{aligned}$$

- (a) $\mathcal{S}_P \cap Hist(W_P) \models (I_P \wedge p) \rightarrow I[\bar{f}/X]$
- (b) $\mathcal{S}_P \cap Hist(W_P) \models T_P \wedge ((p \wedge p') \vee \mathbf{stut}_P) \rightarrow T[\bar{f}/X]$
- (c) $\mathcal{S}_P \cap Hist(W_P) \models L[\bar{f}/X]$

(a) **Proof 9**

$$\begin{aligned} &I_P \wedge p \\ = &\quad \% \text{ Def. } I_{P,p} \\ &\ell_P = 0 \wedge \bigwedge_{i \in PN} (M_P[i] = dflt \wedge F[i] = i) \\ &\quad \wedge \bigwedge_{i \in PN} (M_P[i] \neq \odot \wedge F[i] = i) \\ \rightarrow &\quad \% \text{ Def. } barf \\ &(\ell = 0 \wedge \bigwedge_{i \in SN} M[i] = dflt) [\bar{f}/X] \\ = &\quad \% \text{ Def. } I \\ &I[\bar{f}/X] \end{aligned}$$

(b) **Proof 10**

Since T_P is of the form $\mathbf{stut}_P \vee \bigvee_\tau (\epsilon = \mathbf{a}_\tau \wedge \mathbf{trans}_\tau)$ then $T_P \wedge ((p \wedge p') \vee \mathbf{stut}_P)$ is equal to $\mathbf{stut}_P \vee \bigvee_\tau (\epsilon = \mathbf{a}_\tau \wedge \mathbf{trans}_\tau \wedge p \wedge p')$.

$$\begin{aligned} - & \\ = & \tau_{P,1} \wedge p \wedge p' \\ & \left(\epsilon = \mathbf{Rreq?}(n) \wedge \ell_P = 0 \wedge p \wedge p' \wedge \Psi'_1 = \Psi_1[1, n/\ell_P, r_P] \right) \\ \rightarrow & \left(\epsilon = \mathbf{Rreq?}(n) \wedge \ell = 0 \wedge \Psi'_0 = \Psi_0[1, n/\ell, r] \right) [\bar{f}/X] \\ = & \tau_1 [\bar{f}/X] \end{aligned}$$

The read request at the physical disk level corresponds to the read request at the abstract level.

4.4 Second Step: Physical Disk

$$\begin{aligned}
& - \quad \tau_{P,2} \wedge p \wedge p' \\
& = \quad \left(\epsilon = \mathbf{Rres!}(M_P[F[r_P]]) \wedge \ell_P = 1 \wedge p \wedge p' \wedge \Psi'_1 = \Psi_1[0/\ell_P] \right) \\
& \rightarrow \quad \left(\epsilon = \mathbf{Rres}(M[r]) \wedge \ell = 1 \wedge \Psi'_0 = \Psi_0[0/\ell] \right) [\bar{f}/X] \\
& = \quad \tau_2 [\bar{f}/X]
\end{aligned}$$

The read response at the physical disk level corresponds to the read response at the abstract level.

$$\begin{aligned}
& - \quad \tau_{P,3} \wedge p \wedge p' \\
& = \quad \left(\epsilon = \mathbf{Wreq?}(n, d) \wedge \ell_P = 0 \wedge p \wedge p' \wedge \Psi'_0 = \Psi_0[2, n, d/\ell_P, r_P, s_P] \right) \\
& \rightarrow \quad \left(\epsilon = \mathbf{Wreq?}(n, d) \wedge \ell = 0 \wedge \Psi'_0 = \Psi_0[2, n, d/\ell, r, s] \right) [\bar{f}/X] \\
& = \quad \tau_3 [\bar{f}/X]
\end{aligned}$$

The write request at the physical disk level corresponds to the write request at the abstract level.

$$\begin{aligned}
& - \quad \tau_{P,4} \wedge p \wedge p' \\
& = \quad \left(\epsilon = \mathbf{Wres!} \wedge \ell_P = 2 \wedge p \wedge p' \wedge \Psi'_0 = \Psi_0[0, s_P/\ell_P, M_P[F[r_P]]] \right) \\
& \rightarrow \quad \left(\epsilon = \mathbf{Wres!} \wedge \ell = 2 \wedge \Psi'_0 = \Psi_0[0, s/\ell, M[r]] \right) [\bar{f}/X] \\
& = \quad \tau_4 [\bar{f}/X]
\end{aligned}$$

The write response at the physical disk level corresponds to the write response at the abstract level.

$$\begin{aligned}
& - \quad \tau_{P,5} \wedge p \wedge p' \\
& = \quad \left(\epsilon = \mathbf{i} \wedge n \neq j \wedge p \wedge p' \wedge \Psi'_1 = \Psi_1[j/F[n]] \right) \\
& = \quad \mathbf{false} \\
& \rightarrow \quad \mathbf{T} [\bar{f}/X]
\end{aligned}$$

Due to the external requirement the disk control fault transition can not be taken, i.e., is equal to **false** and from **false** everything can be inferred.

$$\begin{aligned}
& - \quad \tau_{P,6} \wedge p \wedge p' \\
& = \quad \left(\epsilon = \mathbf{i} \wedge p \wedge p' \wedge \Psi'_1 = \Psi_1[\odot/M_P[n]] \right) \\
& = \quad \mathbf{false} \\
& \rightarrow \quad \mathbf{T} [\bar{f}/X]
\end{aligned}$$

Due to the external requirement the disk surface fault transition can not be taken, i.e., is equal to **false** and from **false** everything can be inferred.

$$- \quad \mathbf{stut}_P \rightarrow \mathbf{stut} [\bar{f}/X]$$

(c) Let $\mathbf{SF}_P = \{\tau_{P,i} \mid i \in \{1, 2, 3, 4\}\}$ then

$$\mathbf{L}_P \triangleq \bigwedge_{\tau \in \mathbf{SF}_P} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

. Let $\mathbf{SF} = \{\tau_i \mid i \in \{1, 2, 3, 4\}\}$ then

$$\mathbf{L} \triangleq \bigwedge_{\tau \in \mathbf{SF}} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau).$$

Then the following holds

$$\mathcal{S}_P \cap Hist(W_P) \models L_P \rightarrow L[\bar{f}/X]$$

since τ_i is relatively refined by $\tau_{P,i}$ for $i = 1, \dots, 4$. So

$$\mathcal{S}_P \cap Hist(W_P) \models L[\bar{f}/X]$$

holds.

4.5 Third Step: Fail-Stop Detection Layer

4.5.1 Introduction

In this step, the second stage in our development of the fault tolerant system, we specify in bottom-up fashion on top of the physical disk that has been specified in Section 4.4, the layer that detects the faults that we assumed could affect the physical disk (the anticipated faults). The detection layer acts as a sort of “interface” between the user and the physical disk. It stops when an anticipated fault is detected by the detection mechanism, i.e., the whole system (detection layer plus physical disk) stops when such a fault occurs. It also informs the user which kind of anticipated fault has occurred. This second implementation is “better” than the first one because now the user is certain, under the assumption that the detection mechanism detects all the anticipated faults, that the retrieved data is reliable. The implementation of the detection layer is such that as soon as a fault is detected the system stops. This is called a *fail-stop implementation* [LA90]. As seen above, there are two classes of anticipated faults. Consequently there are two kinds of detection mechanisms. The first one checks whether the contents read from the physical disk are corrupted, i.e., detects errors due to damage of the disk surface. This is done with a cyclic redundancy mechanism [LA90]. The second one checks whether the contents of read from the physical disk originate from the right location. This is done with an address checking mechanism [LA90] which encodes the location of the contents of the physical disk in the contents itself.

4.5.2 Specification

The detection layer consists of three parts: the first part checks whether the data retrieved from the physical disk is affected by a corrupt data fault (the fault that damages the disk surface). This is done with a cyclic redundancy check (CRC) mechanism [LA90]. The second part checks whether the data retrieved from the physical disk is from the correct physical location, i.e., whether it is affected by a disk control system fault. This is done with an address checking (ADR) mechanism [LA90]. The third part prevents further access by the user of the physical disk when one of these two mechanisms detects a fault. This can be easily done because the detection layer acts as “interface” between the user and the physical disk, the detection layer then refuse to communicate with the user and the physical disk. Furthermore this part then gives a message to inform the user which anticipated fault has occurred.

4.5 Third Step: Fail-Stop Detection Layer

The protocol of this interface between user and physical disk is as follows. The user read requests the contents of some physical sector by issuing a **Rreq**(n) event to the detection disk layer. This detection disk layer issues after receipt of this event a **Rreqp**(m) event to the physical disk. The physical disk then responds with a **Rresp**(c) event delivering the requested contents of that physical sector. The detection layer then responds after checking the contents with a **Rresp**(cd) event delivering either the requested contents or an error message. The user write requests that d should be written on sector n by issuing a **Wreq**(n, d) event to the detection layer. The detection layer then issues a **Wreqp**(m, dd) event to the physical disk requesting that dd is written on sector m . The physical disk then responds with a **Wresp** event that the requested information is written. The detection layer then responds to the user with a **Wres** event that the information is written.

Logical sector numbers are introduced now, but are used in the next step to correct disk surface damage faults, i.e. when the detection layer detects that data from a physical sector number is affected by a disk surface damage fault, the correct data will be written to another physical sector number. In order to retrieve these contents from this new location *logical* sector numbers are introduced. When contents are stored at a new physical sector the logical sector number will be pointing to this new sector. So actually the data are retrieved from their logical sector number. In this step however, the mapping between the logical sector numbers and the physical sector numbers will be the identity mapping because they are not needed here. The detection layer is described more formally by the following specification: $\mathcal{S}_{D_s} = (B_{D_s}, H_{D_s})$ where $H_{D_s} \triangleq I_{D_s} \wedge \square T_{D_s} \wedge L_{D_s}$ and B_{D_s} , I_{D_s} , T_{D_s} and L_{D_s} are as follows:

1. **Basis** $B_{D_s} = ((In_{D_s}, Out_{D_s}), (V_{D_s}, X_{D_s}))$

$$\begin{aligned} In_{D_s} &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}, \mathbf{Rresp}, \mathbf{Wresp}\} \\ Out_{D_s} &\triangleq \{\mathbf{Rres}, \mathbf{Wres}, \mathbf{Rreqp}, \mathbf{Wreqp}\}, \\ V_{D_s} &\triangleq \emptyset, \\ X_{D_s} &\triangleq \{\ell_{D_s}, r_{D_s}, s_{D_s}, LS_{D_s}[i] \mid i \in LN\} \end{aligned}$$

where LN is the set of logical sector numbers: $([1, \dots, Y])$. Let Lg the set of data items that the user wants to store on or to retrieve from the physical disk and Phy the set information items that can be stored on or retrieved from the physical disk (Note: an item from Phy is an crc-encoded and address-encoded item of Lg .) For $n \in LN$, $c, d \in Lg$, $m \in PN$ and $cd, dd \in Phy$:

- **Rreq**?(n): the request from the user to read logical sector n .
- **Rres**!(c): the response of the detection layer to the previous request where c are the crc-decoded and address-decoded contents of the requested logical sector n .
- **Wreq**?(n, d): write information item d onto logical sector n .
- **Wres**!: response that previous write has been performed.
- **Rreqp**!(m): the request from the detection layer to read physical sector m .
- **Rresp**?(cd): the response of the physical disk to the previous request where c are the crc-encoded and address-encoded contents of requested physical.

- **Wreqp!**(m, dd): write information item dd onto physical sector m .
- **Wresp?**: response that previous write has been performed.
- ℓ_{D_s} : local variable indicating the status of the detection layer; $\ell_{D_s} = 0$: the detection layer is waiting for a request, $\ell_{D_s} = 1$: the user has issued a read request, $\ell_{D_s} = 2$: the detection layer has issued a read request, $\ell_{D_s} = 3$: the physical has responded to a read request with correct data, $\ell_{D_s} = 4$: the physical disk has responded to a read request with incorrect data, $\ell_{D_s} = 5$: the detection layer has responded to a read request with an error message (stop status), $\ell_{D_s} = 6$: the user has issued a write request, $\ell_{D_s} = 7$: the detection layer has issued a write request, $\ell_{D_s} = 8$: the physical disk has responded to a write request.
- r_{D_s} : local variable indicating the requested sector.
- s_{D_s} : local variable indicating the requested information or the data to be written.
- $LS_{D_s}[i]$: the physical sector mapped to logical sector. i .

Let $\Psi_2 \triangleq (\ell_{D_s}, r_{D_s}, s_{D_s}, LS_{D_s}[1], \dots, LS_{D_s}[Y])$ and $\Psi'_2 \triangleq (\ell'_{D_s}, r'_{D_s}, s'_{D_s}, LS'_{D_s}[1], \dots, LS'_{D_s}[Y])$.

2. Initial states:

$$I_{D_s} \triangleq \ell_{D_s} = 0 \wedge \bigwedge_{i \in LN} LS_{D_s}[i] = i$$

3. Transitions:

To describe the two detecting mechanisms as transitions the following functions are needed: (see [LA90] for more information about this CRC-coding)

- $CC : Phy \rightarrow Bool$
(Crc-Check) Is used to check whether data from the physical disk is damaged by a disk surface fault.
- $CD : Phy \rightarrow (Lg \times PN)$
(Crc-Decode) Is used to decode the CRC-coded physical data into address format.
- $CE : (Lg \times PN) \rightarrow Phy$
(Crc-Encode) Is used to encode data in address format into physical CRC format.
- $AC : (Lg \times PN \times PN) \rightarrow Bool$
(Adr-Check) Is used to check whether data is read from the correct physical location.
- $AD : (Lg \times PN) \rightarrow Lg$
(Adr-Decode) Is used to decode data in address format into user format.

4.5 Third Step: Fail-Stop Detection Layer

- $AE : (LN \times Lg) \rightarrow (Lg \times PN)$
(Adr-Encode) Is used to encode a physical sector number and a information item given by the user into address format.

Let

$Good$	\triangleq	$CC(cd) \wedge AC(CD(cd), LS_{D_s}[r_{D_s}])$ data has not been affected by faults
$A.er$	\triangleq	$CC(cd) \wedge \neg AC(CD(cd), LS_{D_s}[r_{D_s}])$ data has been affected by a control system fault
$C.er$	\triangleq	$\neg CC(cd)$ data has been affected by a disk surface damage
c	\triangleq	$AD(CD(cd))$ the address- and crc-decoded contents
m	\triangleq	$LS_{D_s}[r_{D_s}]$ physical sector
dd	\triangleq	$CE(AE(r_{D_s}, s_{D_s}))$ address- and crc-encoded contents
c_1	\triangleq	address error address error message
c_2	\triangleq	crc error crc error message

$T_{D_s} \triangleq$

$$\tau_{D_s,1} \quad (\epsilon = \mathbf{Rreq}?(n) \wedge \ell_{D_s} = 0 \wedge \Psi'_2 = \Psi_2[1, n/\ell_{D_s}, r_{D_s}])$$

The user requests the contents of logical sector n .

$$\tau_{D_s,2} \quad \vee (\epsilon = \mathbf{Rreqp}!(m) \wedge \ell_{D_s} = 1 \wedge \Psi'_2 = \Psi_2[2/\ell_{D_s}])$$

The detection layer requests the to logical sector r_{D_s} mapped physical sector.

$$\tau_{D_s,3} \quad \vee (\epsilon = \mathbf{Rresp}?(cd) \wedge \ell_{D_s} = 2 \wedge Good \wedge \Psi'_2 = \Psi_2[3, c/\ell_{D_s}, s_{D_s}])$$

The physical disk responds with the contents of the requested sector and the detection layer detects no error in them.

$$\tau_{D_s,4} \quad \vee (\epsilon = \mathbf{Rresp}?(cd) \wedge \ell_{D_s} = 2 \wedge A.er \wedge \Psi'_2 = \Psi_2[4, c_1/\ell_{D_s}, s_{D_s}])$$

The physical disk responds with the contents of the requested sector and the detection layer detects a control system error.

$$\tau_{D_s,5} \quad \vee (\epsilon = \mathbf{Rresp}(cd) \wedge \ell_{D_s} = 2 \wedge C.er \wedge \Psi'_2 = \Psi_2[4, c_2/\ell_{D_s}, s_{D_s}])$$

The physical disk responds with the contents of the requested sector and the detection layer detects a disk surface damage error.

$$\tau_{D_s,6} \quad \vee (\epsilon = \mathbf{Rres}!(s_{D_s}) \wedge \ell_{D_s} = 3 \wedge \Psi'_2 = \Psi_2[0/\ell_{D_s}])$$

The detection layer responds with the contents of the user requested sector.

$$\tau_{D_s,7} \quad \vee (\epsilon = \mathbf{Rres}!(s_{D_s}) \wedge \ell_{D_s} = 4 \wedge \Psi'_2 = \Psi_2[5/\ell_{D_s}])$$

The detection layer responds with an error message and then stops.

- $\tau_{D_s,8} \quad \vee \left(\epsilon = \mathbf{Wreq}?(n, d) \wedge \ell_{D_s} = 0 \wedge \Psi'_2 = \Psi_2 [6, n, d / \ell_{D_s}, r_{D_s}, s_{D_s}] \right)$
 The user requests that d should be written onto logical sector n .
- $\tau_{D_s,9} \quad \vee \left(\epsilon = \mathbf{Wreqp}!(m, dd) \wedge \ell_{D_s} = 6 \wedge \Psi'_2 = \Psi_2 [7 / \ell_{D_s}] \right)$
 The detection requests that dd should be written onto physical sector m .
- $\tau_{D_s,10} \quad \vee \left(\epsilon = \mathbf{Wresp}? \wedge \ell_{D_s} = 7 \wedge \Psi'_2 = \Psi_2 [8 / \ell_{D_s}] \right)$
 The physical disk responds with a signal to the detection layer that requested write is performed.
- $\tau_{D_s,11} \quad \vee \left(\epsilon = \mathbf{Wres}! \wedge \ell_{D_s} = 8 \wedge \Psi'_2 = \Psi_2 [0 / \ell_{D_s}] \right)$
 The detection layer responds with a signal to the user that the requested write is performed.
- $\tau_{D_s,0} \quad \vee \mathbf{stut}_{D_s}$

These transitions are illustrated in figure 4.3

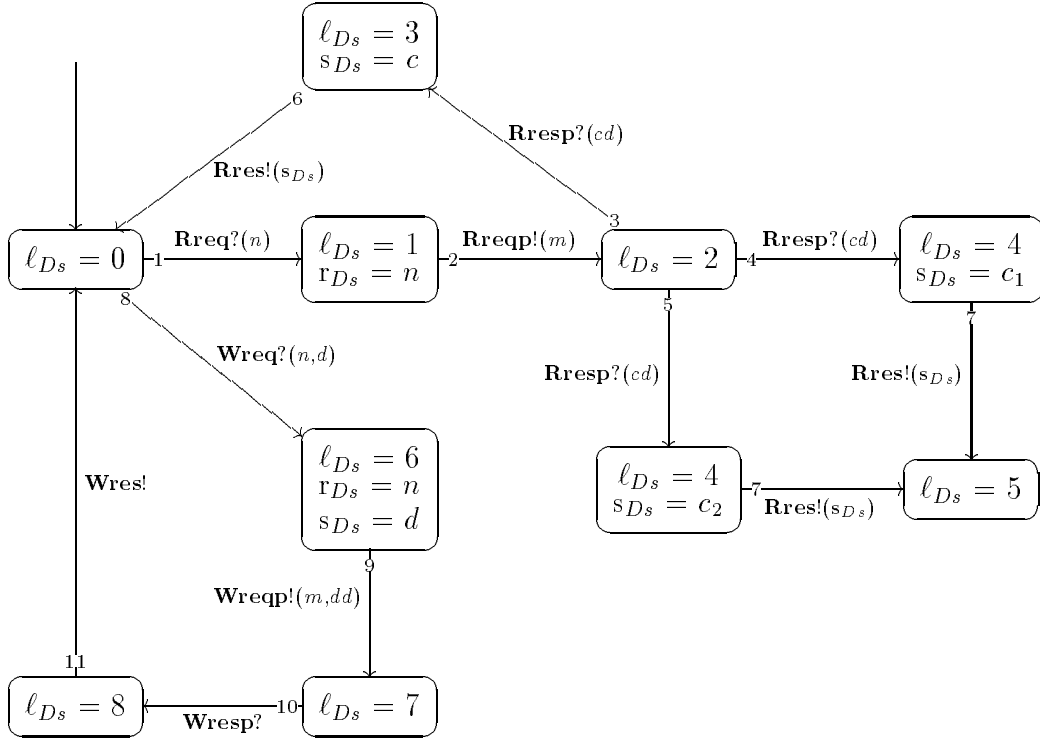


Figure 4.3: Transitions of the fail-stop detection layer.

4. Liveness condition:

The liveness condition expresses that the communication transitions are strongly fair. Let $SF_{D_s} = \{\tau_{D_s,i} \mid i \in \{1, \dots, 11\}\}$ then

$$L_{D_s} \triangleq \bigwedge_{\tau \in SF_{D_s}} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

4.5 Third Step: Fail-Stop Detection Layer

4.5.3 Requirement W_{D_s}

The requirement W_{D_s} should express that no errors are detected.

$$W_P \triangleq \Box(\epsilon = \mathbf{Rresp}?(cd) \rightarrow Good)$$

4.5.4 $\mathcal{S}_{D_s} \parallel \mathcal{S}_P$ relatively refines \mathcal{S}_P

The communication channels of \mathcal{S}_P in $\mathcal{S}_{D_s} \parallel \mathcal{S}_P$ should be renamed in order to compose \mathcal{S}_{D_s} with \mathcal{S}_P , i.e., instead of H_P as specification we should take

$$H_P[\mathbf{Rreq}, \mathbf{Wreq}, \mathbf{Rres}, \mathbf{Wres}/\mathbf{Rreq}, \mathbf{Wreq}, \mathbf{Rres}, \mathbf{Wres}].$$

Let \mathcal{S}_{P_1} be the specification with this renaming. According to theorem 8 $\mathcal{S}_{D_s} \parallel \mathcal{S}_{P_1}$ relatively refines \mathcal{S}_P with respect to (\mathbf{true}, W_P) iff the following holds:

$$\begin{aligned} \mathfrak{D}(B_{D_s, P_1}) &= \mathfrak{D}(B_P) \text{ and} \\ \models (\exists X_{D_s, P_1} \cdot (G_{D_s, P_1})) &\rightarrow (\exists X_P \cdot (G_P)) \end{aligned}$$

where X_{D_s, P_1} are the local variables from $\mathcal{S}_{D_s} \parallel \mathcal{S}_{P_1}$, i.e.,

$X_{D_s, P_1} \triangleq \{\ell_{D_s}, r_{D_s}, s_{D_s}, LS_{D_s}[i] \mid i \in LN\} \cup \{\ell_{P_1}, r_{P_1}, s_{P_1}, M_{P_1}[n], F[n] \mid n \in PN\}$ and G_{D_s, P_1} is defined as

$$\exists \epsilon_1, \epsilon_2 \cdot B_{D_s}^A \odot_{B_{P_1}^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (H_{D_s} \wedge W_{D_s})[\epsilon_1/\epsilon] \wedge (H_{P_1} \wedge W_{P_1})[\epsilon_2/\epsilon]$$

This can be rewritten to following machine specification of \mathcal{S}_2 : $\mathcal{S}_2 = (B_2, H_2)$ where $H_2 \triangleq I_2 \wedge \Box T_2 \wedge L_2$ and B_2, I_2, T_2 and L_2 are as follows:

1. **Basis** $B_2 = ((In_2, Out_2), (V_2, X_2))$

$$\begin{aligned} In_2 &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}\} \\ Out_2 &\triangleq \{\mathbf{Rres}, \mathbf{Wres}\}, \\ V_2 &\triangleq \emptyset, \\ X_2 &\triangleq \{\ell_{D_s}, r_{D_s}, s_{D_s}, LS_{D_s}[i] \mid i \in LN\} \\ &\quad \cup \{\ell_{P_1}, r_{P_1}, s_{P_1}, M_{P_1}[n], F[n] \mid n \in PN\} \end{aligned}$$

Let

$$\begin{aligned} \Psi_{12} &\triangleq (\ell_{D_s}, r_{D_s}, s_{D_s}, LS_{D_s}[1], \dots, LS_{D_s}[Y], \\ &\quad \ell_{P_1}, r_{P_1}, s_{P_1}, M_{P_1}[1], \dots, M_{P_1}[Y], F[1], \dots, F[Y]) \\ \Psi'_{12} &\triangleq (\ell'_{D_s}, r'_{D_s}, s'_{D_s}, LS'_{D_s}[1], \dots, LS'_{D_s}[Y], \\ &\quad \ell'_{P_1}, r'_{P_1}, s'_{P_1}, M'_{P_1}[1], \dots, M'_{P_1}[Y], F'[1], \dots, F'[Y]) \end{aligned}$$

2. **Initial states:**

$$I_2 \triangleq \ell_{D_s} = 0 \wedge \ell_{P_1} = 0 \wedge \bigwedge_{i \in LN} LS_{D_s}[i] = i \wedge \bigwedge_{i \in PN} (M_{P_1}[i] = dflt \wedge F[i] = i)$$

3. Transitions:

Let

$$\begin{aligned}
 c &\stackrel{\Delta}{=} AD(CD(M_{P_1}[F[r_{P_1}]])) \\
 &\quad \text{the address- and crc-decoded contents} \\
 m &\stackrel{\Delta}{=} LS_{D_s}[r_{D_s}] \\
 &\quad \text{physical sector} \\
 dd &\stackrel{\Delta}{=} CE(AE(r_{D_s}, s_{D_s})) \\
 &\quad \text{address- and crc-encoded contents}
 \end{aligned}$$

$T_2 \stackrel{\Delta}{=}$

$$\tau_{2,1} \quad \left(\epsilon = \mathbf{Rreq?}(n) \wedge \ell_{D_s} = 0 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[1, n/\ell_{D_s}, r_{D_s}] \right)$$

The user requests the contents of logical sector n .

$$\tau_{2,2} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 1 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[2, 1, m/\ell_{D_s}, \ell_{P_1}, r_{P_1}] \right)$$

The detection layer requests the to logical sector r_{D_s} mapped physical sector.

$$\tau_{2,3} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 2 \wedge \ell_{P_1} = 1 \wedge \Psi'_{12} = \Psi_{12}[3, c, 0/\ell_{D_s}, s_{D_s}, \ell_{P_1}] \right)$$

The physical disk responds with the contents of the requested sector and because of W_{P_1} and W_{D_s} they are correct.

$$\tau_{2,4} \quad \vee \left(\epsilon = \mathbf{Rres!}(s_{D_s}) \wedge \ell_{D_s} = 3 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[0/\ell_{D_s}] \right)$$

The detection layer responds with the contents of the user requested sector.

$$\tau_{2,5} \quad \vee \left(\epsilon = \mathbf{Wreq?}(n, d) \wedge \ell_{D_s} = 0 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[6, n, d/\ell_{D_s}, r_{D_s}, s_{D_s}] \right)$$

The user requests that d should be written onto logical sector n .

$$\tau_{2,6} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 6 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[7, 2, m, dd/\ell_{D_s}, \ell_{P_1}, r_{P_1}, s_{P_1}] \right)$$

The detection requests that dd should be written onto physical sector m .

$$\tau_{2,7} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 7 \wedge \ell_{P_1} = 2 \wedge \Psi'_{12} = \Psi_{12}[8, 0, s_{P_1}/\ell_{D_s}, \ell_{P_1}, M_{P_1}[F[r_{P_1}]]] \right)$$

The physical disk responds with a signal to the detection layer that requested write is performed.

$$\tau_{2,8} \quad \vee \left(\epsilon = \mathbf{Wres!} \wedge \ell_{D_s} = 8 \wedge \ell_{P_1} = 0 \wedge \Psi'_{12} = \Psi_{12}[0/\ell_{D_s}] \right)$$

The detection layer responds with a signal to the user that the requested write is performed.

$$\tau_{2,0} \quad \vee \mathbf{stut}_2$$

Figure 4.4 illustrates the transitions of the relative composed system $\mathcal{S}_{D_s} \mid \overline{W} \mid \mathcal{S}_{P_1}$. Due to $B_{D_s}^A \odot_{B_{P_1}^A} (\epsilon, \epsilon_1, \epsilon_2)$ the communications events with the physical disk are transformed into \mathbf{i} events and due to W_{D_s} and W_{P_1} no faults occur and no errors are detected.

4. Liveness condition:

The liveness condition expresses that all non-stutter transitions are strongly fair. Let $SF_{D_s} = \{\tau_{2,i} \mid i \in \{1, \dots, 8\}\}$ then

$$L_2 \stackrel{\Delta}{=} \bigwedge_{\tau \in SF_2} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

4.5 Third Step: Fail-Stop Detection Layer

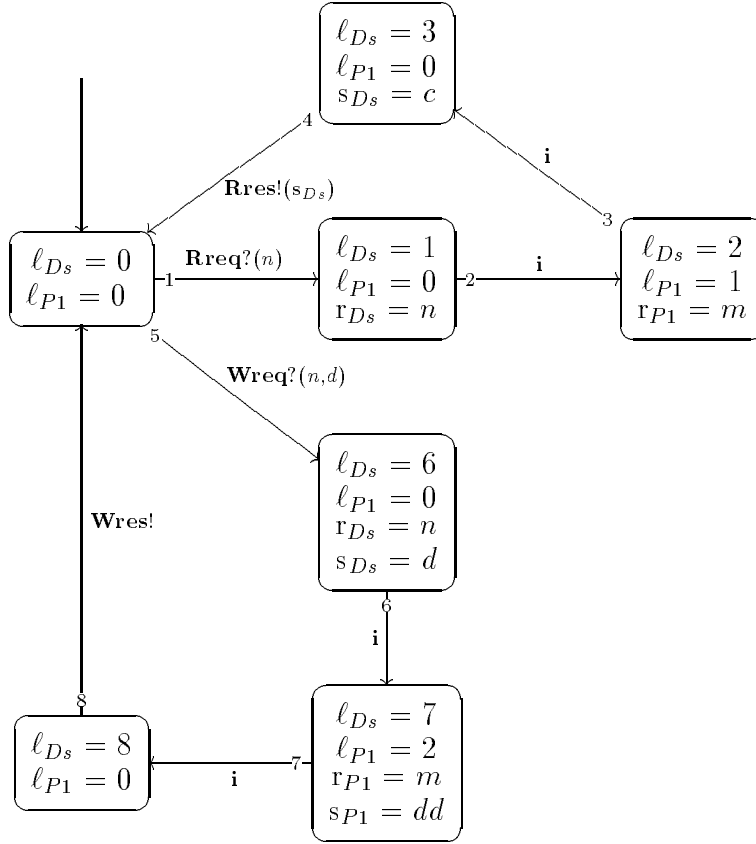


Figure 4.4: Transitions of the relative composed system.

X_P are the local variables from \mathcal{S}_P , i.e., $X_P \triangleq \{\ell_P, r_P, s_P, M_P[n], F[n] \mid n \in PN\}$ and G_P is defined as

$$I_P \wedge \Box T_P \wedge L_P \wedge W_P$$

Rule 3 will be used to prove

$$\models (\exists X_2 . (G_2)) \rightarrow (\exists X_P . (G_P)).$$

This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_2 to \mathcal{S}_P , defined as: $\bar{f} = f_{\ell_P}, f_{r_P}, f_{s_P}, f_{M_P[n]}, f_{F_P[n]}$ ($n \in SN$). The refinement mappings are defined as:

$$\begin{array}{l}
 f_{\ell_P} \\
 \text{if } \ell_{D_s} = 0 \text{ then } \ell_{D_s} \\
 \ell_{D_s} = 1 \text{ then } \ell_{D_s} \\
 \ell_{D_s} = 2 \text{ then } \ell_{D_s} - 1 \\
 \ell_{D_s} = 3 \text{ then } \ell_{D_s} - 2 \\
 \ell_{D_s} = 6 \text{ then } \ell_{D_s} - 4 \\
 \ell_{D_s} = 7 \text{ then } \ell_{D_s} - 5 \\
 \ell_{D_s} = 8 \text{ then } \ell_{D_s} - 6
 \end{array}$$

$$\begin{aligned}
 f_{r_P} &\triangleq r_{D_s} \\
 f_{s_P} &\triangleq s_{D_s} \\
 f_{M_P[n]} &\triangleq AD(CD(M_{P_1}[LS[n]])) \\
 f_{F_P[n]} &\triangleq F_{P_1}[LS[n]]
 \end{aligned}$$

$$\begin{aligned}
 (a) \quad \mathcal{S}_2 &\models (I_2) \rightarrow (I_P \wedge p) [\bar{f}/X_P] \\
 (b) \quad \mathcal{S}_2 &\models T_2 \rightarrow (T_P \wedge ((p \wedge p') \vee \mathbf{stut}_P)) [\bar{f}/X_P] \\
 (c) \quad \mathcal{S}_2 &\models L_P [\bar{f}/X_P]
 \end{aligned}$$

(a) **Proof 11**

$$\begin{aligned}
 &I_2 \\
 = &\quad \% \text{ Def. } I_2 \\
 &\ell_{D_s} = \mathbf{0} \wedge \ell_{P_1} = \mathbf{0} \wedge \bigwedge_{i \in LN} LS_{D_s}[i] = i \wedge \bigwedge_{i \in PN} (M_{P_1}[i] = dflt \wedge F[i] = i) \\
 \rightarrow &\quad \% \text{ Def. } \bar{f}, p \\
 &(\ell_P = \mathbf{0} \wedge \bigwedge_{i \in PN} (M_P[i] = dflt \wedge F[i] = i) \wedge p) [\bar{f}/X_P] \\
 = &\quad \% \text{ Def. } I_P \\
 &(I_P \wedge p) [\bar{f}/X_P]
 \end{aligned}$$

(b) **Proof 12**

Since T_P is of the form $\mathbf{stut}_P \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathbf{trans}_{\tau})$ then $T_P \wedge ((p \wedge p') \vee \mathbf{stut}_P)$ is equal to $\mathbf{stut}_P \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge \mathbf{trans}_{\tau} \wedge p \wedge p')$.

$$\begin{aligned}
 - &\quad \tau_{2,1} \\
 &= \left(\epsilon = \mathbf{Rreq}?(n) \wedge \ell_{D_s} = \mathbf{0} \wedge \ell_{P_1} = \mathbf{0} \wedge \Psi'_{12} = \Psi_{12} [1, n/\ell_{D_s}, r_{D_s}] \right) \\
 \rightarrow &\quad \left(\epsilon = \mathbf{Rreq}?(n) \wedge \ell_P = \mathbf{0} \wedge p \wedge p' \wedge \Psi'_1 = \Psi_1 [1, n/\ell_P, r_P] \right) [\bar{f}/X_P] \\
 &= (\tau_{P,1} \wedge p \wedge p') [\bar{f}/X_P]
 \end{aligned}$$

The user read request at the second level corresponds to the user read request at the first level.

$$\begin{aligned}
 - &\quad \tau_{2,2} \\
 &= \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 1 \wedge \ell_{P_1} = \mathbf{0} \wedge \Psi'_{12} = \Psi_{12} [2, 1, m/\ell_{D_s}, \ell_{P_1}, r_{P_1}] \right) \\
 \rightarrow &\quad \left(\epsilon = \mathbf{i} \wedge \Psi'_1 = \Psi_1 \right) [\bar{f}/X_P] \\
 &\rightarrow \mathbf{stut}_P [\bar{f}/X_P]
 \end{aligned}$$

The read request to the physical disk at the second level corresponds to stutter step of the physical disk at the first level.

$$\begin{aligned}
 - &\quad \tau_{2,3} \\
 &= \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 2 \wedge \ell_{P_1} = 1 \wedge \Psi'_{12} = \Psi_{12} [3, c, 0/\ell_{D_s}, s_{D_s}, \ell_{P_1}] \right) \\
 \rightarrow &\quad \left(\epsilon = \mathbf{i} \wedge \Psi'_1 = \Psi_1 \right) [\bar{f}/X_P] \\
 &\rightarrow \mathbf{stut}_P [\bar{f}/X_P]
 \end{aligned}$$

The read response of the physical disk at the second level corresponds to the stutter step of the physical disk at the first level.

4.5 Third Step: Fail-Stop Detection Layer

$$\begin{aligned}
- & \tau_{2,4} \\
& = \left(\epsilon = \mathbf{Rres}!(s_{D_s}) \wedge \ell_{D_s} = 3 \wedge \ell_{P_1} = 0 \wedge \Psi_{12}' = \Psi_{12}[0/\ell_{D_s}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{Rres}!(M_P[F[r_P]]) \wedge \ell_P = 1 \wedge p \wedge p' \wedge \Psi_1' = \Psi_1[0/\ell_P] \right) [\bar{f}/X_P] \\
& = (\tau_{P,2} \wedge p \wedge p') [\bar{f}/X_P]
\end{aligned}$$

The read response of the detection layer at the second level corresponds to the read response of the physical disk at the first level.

$$\begin{aligned}
- & \tau_{2,5} \\
& = \epsilon = \mathbf{Wreq}?(n, d) \wedge (\ell_{D_s}, \ell_{P_1}) = (0, 0) \\
& \quad \wedge \Psi_{12}' = \Psi_{12}[6, n, d/\ell_{D_s}, r_{D_s}, s_{D_s}] \\
& \rightarrow \epsilon = \mathbf{Wreq}?(n, d) \wedge \ell_P = 0 \wedge p \wedge p' \\
& \quad \wedge \Psi_1' = \Psi_1[2, n, d/\ell_P, r_P, s_P] [\bar{f}/X_P] \\
& = (\tau_{P,3} \wedge p \wedge p') [\bar{f}/X_P]
\end{aligned}$$

The user write request at the second level corresponds to the user write request at the first level.

$$\begin{aligned}
- & \tau_{2,6} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 6 \wedge \ell_{P_1} = 0 \wedge \Psi_{12}' = \Psi_{12}[7, 2, m, dd/\ell_{D_s}, \ell_{P_1}, r_{P_1}, s_{P_1}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \Psi_1' = \Psi_1 \right) [\bar{f}/X_P] \\
& \rightarrow \mathbf{stut}_P [\bar{f}/X_P]
\end{aligned}$$

The write request to the physical disk at the second level corresponds to the stutter step of the physical disk at the first level.

$$\begin{aligned}
- & \tau_{2,7} \\
& = \left(\epsilon = \mathbf{i} \wedge \ell_{D_s} = 7 \wedge \ell_{P_1} = 2 \wedge \Psi_{12}' = \Psi_{12}[8, 0/\ell_{D_s}, \ell_{P_1}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{i} \wedge \Psi_1' = \Psi_1 \right) [\bar{f}/X_P] \\
& \rightarrow \mathbf{stut}_P [\bar{f}/X_P]
\end{aligned}$$

The write response of the physical disk at the second level corresponds to the stutter step of the physical disk at the first level.

$$\begin{aligned}
- & \tau_{2,8} \\
& = \left(\epsilon = \mathbf{Wres}! \wedge \ell_{D_s} = 8 \wedge \ell_{P_1} = 0 \wedge \Psi_{12}' = \Psi_{12}[0/\ell_{D_s}] \right) \\
& \rightarrow \left(\epsilon = \mathbf{Wres}! \wedge \ell_P = 2 \wedge p \wedge p' \wedge \Psi_1' = \Psi_1[0, s_P/\ell_P, M_P[F[r_P]]] \right) [\bar{f}/X_P] \\
& = (\tau_{P,4} \wedge p \wedge p') [\bar{f}/X_P]
\end{aligned}$$

The write response of the detection layer at the second level corresponds to the write response of the physical disk at the first level.

$$- \quad \mathbf{stut}_2 \rightarrow \mathbf{stut}_P [\bar{f}/X_P]$$

(c) Let $\mathbf{SF}_P = \{\tau_{P,i} \mid i \in \{1, 2, 3, 4\}\}$ then

$$L_P \triangleq \bigwedge_{\tau \in \mathbf{SF}_P} (\Box \Diamond E_n(\tau) \rightarrow \Box \Diamond \tau)$$

. Let $SF_2 = \{\tau_{2,i} \mid i \in \{1, \dots, 8\}\}$ then

$$L_2 \triangleq \bigwedge_{\tau \in SF_2} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau).$$

Then the following holds

$$\mathcal{S}_2 \models L_2 \rightarrow L_P [\bar{f}/X_P]$$

So

$$\mathcal{S}_2 \models L_P [\bar{f}/X_P]$$

holds.

4.6 Fourth Step: Error Recovery Layer

4.6.1 Introduction

In this step the error recovery layer is specified. This is the layer that tries to correct the errors detected by the detection layer. The technique used for error recovery is that of the *mirrored disk concept* [LA90]. This mirrored disk concept is as follows: instead of one physical disk and corresponding detection layer N physical disks with identical contents and N corresponding detection layers ($N > 1$) are maintained. In case some information can no longer be retrieved from a disk, the information is still available on another one. The user requests some contents from the error recovery layer. The error recovery layer selects a disk from which it can retrieve these contents. Then it requests these contents from the corresponding detection layer of that disk. The detection layer requests then the contents from the physical disk and checks whether the contents are correct. The detection layer then signals if the contents are correct and if not it will signal which error has it has detected. If the contents are correct the error recovery layer will send them to the user and is then ready for new requests from the user. As seen before the detection layer can detect two kinds of errors: (1) errors due to disk surface damage fault and (2) errors due to disk control system faults. The error recovery layer will react as follows on these errors:

ad (1) First, the error recovery layer selects another disk from which it can retrieve the requested contents and when the corresponding detection layer signals that the contents are correct, the error recovery layer will write these contents to another location of the affected disk. In order to retrieve these contents from this new location *logical* locations are introduced. When contents are stored at a new physical location the logical location will be pointing to this new location. So actually the data are retrieved from their logical location. Subsequently the error recovery layer will send the contents to the user and is ready to receive new requests from the user. When the detection-layer of the second disk also reports an error the error recovery layer will react as described in *ad(1)* and *ad(2)* depending on the kind of error detected.

4.6 Fourth Step: Error Recovery Layer

ad (2) First, the error recovery layer disables the faulty disk and then it will select another disk from which it can retrieve the requested contents and when the corresponding detection layer signals that the contents are correct the error recovery layer will them to the user. When the detection-layer of the second disk also reports an error the error recovery layer will react as described in *ad(1)* and *ad(2)* depending on the kind of error detected.

This error recovery process only works if the following assumptions are made:

- In order to store the contents on a new physical location enough spare locations should be available on an affected disk.
- Furthermore, the following must always hold in order to recover the *ad(1)*-type of error on a disk or to retrieve the contents from a logical location: for all logical locations there exists at least one non-disabled physical disk that has correct data stored on that logical location. This condition guarantees that always, each logical location contains correct data (on which disk we don't know, but it is a non-disabled one and it is not the disk whose type 1 error has to be repaired).

4.6.2 Specification of the Recovery Layer

The error recovery layer acts as interface between the user and the N detection layers of the N physical disks. The user requests with a **Rreq**(n) event the contents of some logical sector n . The error recovery layer requests these contents, on receipt of this event, by issuing a **Rreqd** _{i} (n) event to one of the non-disabled detection layers. This detection layer responds with an **Rresd** _{i} (d) event. As seen in the third step there are three possibilities:

1. If this event delivers a message saying that the, to this detection layer corresponding, physical disk has been affected by a disk control system fault then this detection layer will be disabled and the error-recovery layer will send a **Rreqd** _{j} (n) event to another non-disabled detection layer.
2. If this event delivers a message that the, to this detection layer corresponding, physical disk has been affected by a disk surface damage fault then the error recovery layer requests the contents with a **Rreqd** _{j} (n) from another non-disabled detection layer until it finds a detection layer that responds with the correct contents. Then the error recovery layer can “repair” the physical disks that has been affected by a disk surface damage fault at the same logical sector by generating a **Wreqd** _{j} write request event with the correct data to the same logical sector number of the corresponding detection layers of those physical disks. The detections layers will respond with a **Wresd** indicating that the affected physical disks has been repaired. The design decision we make is that the detection layer has to find the spare physical sector to which these contents can be written. After that, the error recovery layer responds with a **Rres**(c) event to deliver the requested contents.
3. If this event delivers normal data the error recovery layer will respond with a **Rres**(c) event delivering the requested contents.

The user requests with a **Wreq**(n, d) event that d has to be written onto a logical sector n . The error recovery layer requests with a **Wreqd**(n, d) event to all non-disabled detection layers that d has to be written on logical sector n to ensure that the corresponding physical disks have identical contents on their logical sectors. The detection layers then respond to these requests with a **Wresd** event. The error recovery layer then responds with a **Wres** event to the user that the write operation has been performed.

The stable storage layer is described by the following specification: $\mathcal{S}_R = (B_R, H_R)$ where $H_R \triangleq I_R \wedge \square T_R \wedge L_R$ and B_R, I_R, T_R and L_R are as follows:

1. **Basis** $B_R = ((In_R, Out_R), (V_R, X_R))$

$$\begin{aligned} In_R &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}, \mathbf{Rresd}_i, \mathbf{Wresd}_i \mid i \in Nd\}, \\ Out_R &\triangleq \{\mathbf{Rres}, \mathbf{Wres}, \mathbf{Rreqd}_i, \mathbf{Wreqd}_i \mid i \in Nd\}, \\ V_R &\triangleq \emptyset, \\ X_R &\triangleq \{\ell_R, r_R, s_R, t_R, G, A, W\} \end{aligned}$$

- **Rreq**?(n): the request from the user to read logical sector n .
- **Rres**!(c): the response of the error recovery layer to the previous request where c are the crc-decoded and address-decoded contents of the requested logical sector.
- **Wreq**?(n, d): user request to write information item d onto logical sector n .
- **Wres**!: write response to the user that the requested information is written.
- **Rreqd**! _{i} (n): the read request from the error recovery layer towards detection layer i .
- **Rresd**? _{i} (c): the read response from detection layer i to the previous request where c are the contents of the requested logical sector.
- **Wreqd**! _{i} (n, d): the write request from the error recovery layer to detection layer i to write information item d onto logical sector n .
- **Wresd**? _{i} : response from detection layer i to the error recovery layer that the requested information has been written.
- ℓ_R : local variable indicating the status of the error recovery layer; $\ell_R = 0$: the error recovery layer is waiting for a request, $\ell_R = 1$: the user has issued a read request or the detection layer responded to a read request with affected data, $\ell_R = 2$: the error recovery layer has issued a read request, $\ell_R = 3$: the detection responded to a read request with correct data or all affected disk are repaired, $\ell_R = 4$: the the detection responded to a read request with correct data and there are affected disks, $\ell_R = 5$: the error recovery layer has issued a write request to repair an affected disk and there are still affected disks to be repaired, $\ell_R = 6$: the error recovery layer has issued a write request to repair an affected disk and there are no more affected disks, $\ell_R = 7$: the user has issued a write request, $\ell_R = 8$: the error recovery layer has issued a write request and there are still to be written disks, $\ell_R = 9$: the error recovery layer has issued a write request and there are no more to be written disks, $\ell_R = 10$: the detection layer of the last to be written disk responded to a write request.

4.6 Fourth Step: Error Recovery Layer

- r_R : local variable indicating the requested sector.
- s_R : local variable indicating the requested contents or the requested contents to be written.
- t_R : local variable indicating the index of the disk to which a request has been issued.
- G : local variable indicating the set of indexes of non-disabled disks.
- A : local variable indicating the set of indexes of by control system faults affected disks.
- W : local variable indicating the set of indexes on which data should be written.

Let $\Psi_3 \triangleq (\ell_R, r_R, s_R, t_R, G, A, W)$ and $\Psi'_3 \triangleq (\ell'_R, r'_R, s'_R, t_R, G', A', W')$.

2. Initial States:

$$I_R \triangleq \ell_R = 0 \wedge G = \{1, \dots, N\} \wedge A = \emptyset$$

The error recovery layer is waiting for requests from the user and all the N disks are non-disabled.

3. Transitions:

Let

- $c_1 \triangleq$ address error
address error message
- $c_2 \triangleq$ crc error
crc error message
- $Good1 \triangleq i = t_R \wedge dc \neq c_1 \wedge dc \neq c_2 \wedge A = \emptyset$
data is not affected by faults and the number of affected disks is zero
- $Good2 \triangleq i = t_R \wedge dc \neq c_1 \wedge dc \neq c_2 \wedge A \neq \emptyset$
data is not affected by faults and the number of affected disks is non-zero
- $A.er \triangleq i = t_R \wedge dc = c_1$
data is affected by control system fault
- $C.er \triangleq i = t_R \wedge dc = c_2$
data is affected by disk surface damage
- $G^- \triangleq G \setminus \{i\}$
set of good disks minus i
- $A^- \triangleq A \setminus \{i\}$
set of affected disks minus i
- $A^+ \triangleq A \cup \{i\}$
set of affected disks plus i
- $W^- \triangleq W \setminus \{i\}$
set of to be written disks minus i

- $C1 \triangleq i \in G \wedge i \notin A$
disk i is good and not affected
- $C2 \triangleq i \in A \wedge A^- \neq \emptyset$
disk i is affected and the number of affected disks is greater than 1
- $C3 \triangleq i \in A \wedge A^- = \emptyset$
disk i is the only affected disk
- $C4 \triangleq i \in W \wedge W^- \neq \emptyset$
disk i should be written onto and the number of to be written disks is greater than 1
- $C5 \triangleq i \in W \wedge W^- = \emptyset$
disk i is the only disk to be written onto

$T_R \triangleq$

- $\tau_{R,1} \quad (\epsilon = \mathbf{Rreq?}(n) \wedge \ell_R = 0 \wedge \Psi'_3 = \Psi_3[1, n/\ell_R, r_R])$
The user requests the contents of logical sector n .
- $\tau_{R,2} \quad \vee (\epsilon = \mathbf{Rreqd!}_i(r_R) \wedge \ell_R = 1 \wedge C1 \wedge \Psi'_3 = \Psi_3[2, i/\ell_R, t_R])$
The error recovery layer requests the contents of logical sector r_R from an enabled detection layer.
- $\tau_{R,3} \quad \vee (\epsilon = \mathbf{Rresd?}_i(cd) \wedge \ell_R = 2 \wedge Good1 \wedge \Psi'_3 = \Psi_3[3, cd/\ell_R, s_R])$
The detection layer responds with the contents of the requested sector and the detection layer has detected no error in them.
- $\tau_{R,4} \quad \vee (\epsilon = \mathbf{Rresd?}_i(cd) \wedge \ell_R = 2 \wedge A.er \wedge \Psi'_3 = \Psi_3[1, G^-/\ell_R, G])$
The detection layer responds with the contents of the requested sector and the detection layer has detected an control system error, so this detection layer will be disabled.
- $\tau_{R,5} \quad \vee (\epsilon = \mathbf{Rresd?}_i(cd) \wedge \ell_R = 2 \wedge C.er \wedge \Psi'_3 = \Psi_3[1, A^+/\ell_R, A])$
The detection layer responds with the contents of the requested sector and the detection layer detects an disk surface damage error, so disk i has to be repaired.
- $\tau_{R,6} \quad \vee (\epsilon = \mathbf{Rresd?}_i(cd) \wedge \ell_R = 2 \wedge Good2 \wedge \Psi'_3 = \Psi_3[4, cd/\ell_R, s_R])$
A correct disk has been found so the error recovery layer can repair the affected disks.
- $\tau_{R,7} \quad \vee (\epsilon = \mathbf{Wreqd!}_i(r_R, s_R) \wedge \ell_R = 4 \wedge C2 \wedge \Psi'_3 = \Psi_3[5, i, A^-/\ell_R, t_R, A])$
An affected disk is being repaired and there are still unrepaired disk.
- $\tau_{R,8} \quad \vee (\epsilon = \mathbf{Wresd?}_i \wedge \ell_R = 5 \wedge i = t_R \wedge \Psi'_3 = \Psi_3[4/\ell_R])$
The affected disk is repaired.
- $\tau_{R,9} \quad \vee (\epsilon = \mathbf{Wreqd!}_i(r_R, s_R) \wedge \ell_R = 4 \wedge C3 \wedge \Psi'_3 = \Psi_3[6, A^-/\ell_R, A])$
An affected disk is being repaired and there are no unrepaired disks.
- $\tau_{R,10} \quad \vee (\epsilon = \mathbf{Wresd?}_i \wedge \ell_R = 6 \wedge i = t_R \wedge \Psi'_3 = \Psi_3[3/\ell_R])$
All affected disk are repaired, so the user requested contents can be sent.

4.6 Fourth Step: Error Recovery Layer

$$\tau_{R,11} \quad \vee \left(\epsilon = \mathbf{Rres}!(s_R) \wedge \ell_R = 3 \wedge \Psi'_3 = \Psi_3 [0/\ell_R] \right)$$

The error recovery layer responds with the requested contents.

$$\tau_{R,12} \quad \vee \left(\epsilon = \mathbf{Wreq}?(n, d) \wedge \ell_R = 0 \wedge \Psi'_3 = \Psi_3 [7, n, d, G/\ell_R, r_R, s_R, W] \right)$$

The user requests that d should be written onto logical sector n .

$$\tau_{R,13} \quad \vee \left(\epsilon = \mathbf{Wreqd}!(i, r_R, s_R) \wedge \ell_R = 7 \wedge C4 \wedge \Psi'_3 = \Psi_3 [8, i, W^-/\ell_R, t_R, W] \right)$$

The requested information is being written to a disk and there are still disks which haven't written them.

$$\tau_{R,14} \quad \vee \left(\epsilon = \mathbf{Wresd}?(i) \wedge \ell_R = 8 \wedge i = t_R \wedge \Psi'_3 = \Psi_3 [7/\ell_R] \right)$$

The requested information is written onto disk i .

$$\tau_{R,15} \quad \vee \left(\epsilon = \mathbf{Wreqd}!(i, r_R, s_R) \wedge \ell_R = 7 \wedge C5 \wedge \Psi'_3 = \Psi_3 [9, i, W^-/\ell_R, t_R, W] \right)$$

The requested information is being written to a disk and there are no disks which haven't written them.

$$\tau_{R,16} \quad \vee \left(\epsilon = \mathbf{Wresd}?(i) \wedge \ell_R = 9 \wedge i = t_R \wedge \Psi'_3 = \Psi_3 [10/\ell_R] \right)$$

The requested information is written onto all disks.

$$\tau_{R,17} \quad \vee \left(\epsilon = \mathbf{Wres}! \wedge \ell_R = 10 \wedge \Psi'_3 = \Psi_3 [0/\ell_R] \right)$$

The error recovery layer responds with a signal to the user that requested write is performed.

$$\tau_{R,0} \quad \vee \mathbf{stut}_R$$

These transitions are illustrated in figure 4.5

4. Liveness Condition:

The liveness condition expresses that the communication transitions are strongly fair.

Let $SF_R = \{\tau_{R,i} \mid i \in \{1, \dots, 17\}\}$ then

$$L_R \stackrel{\Delta}{=} \bigwedge_{\tau \in SF_R} (\Box \Diamond E n(\tau) \rightarrow \Box \Diamond \tau)$$

4.6.3 Specification of the Detection Layer

The detection layer is nearly the same as the fail-stop detection layer the only difference is that when error due to a disk surface fault has been detected the detection layer waits for the corrective action to be undertaken, i.e., a write request of the correct data to the to be repaired logical sector. It therefore selects a spare physical sector and maps the logical sector to it. It then issues a write request to this new physical sector. The physical disk then responds to this write request. The detection layer responds that the disk has been repaired.

The detection layer is described more formally by the following specification: $\mathcal{S}_D = (B_D, H_D)$ where $H_D \stackrel{\Delta}{=} I_D \wedge \Box T_D \wedge L_D$ and B_D, I_D, T_D and L_D are as follows:

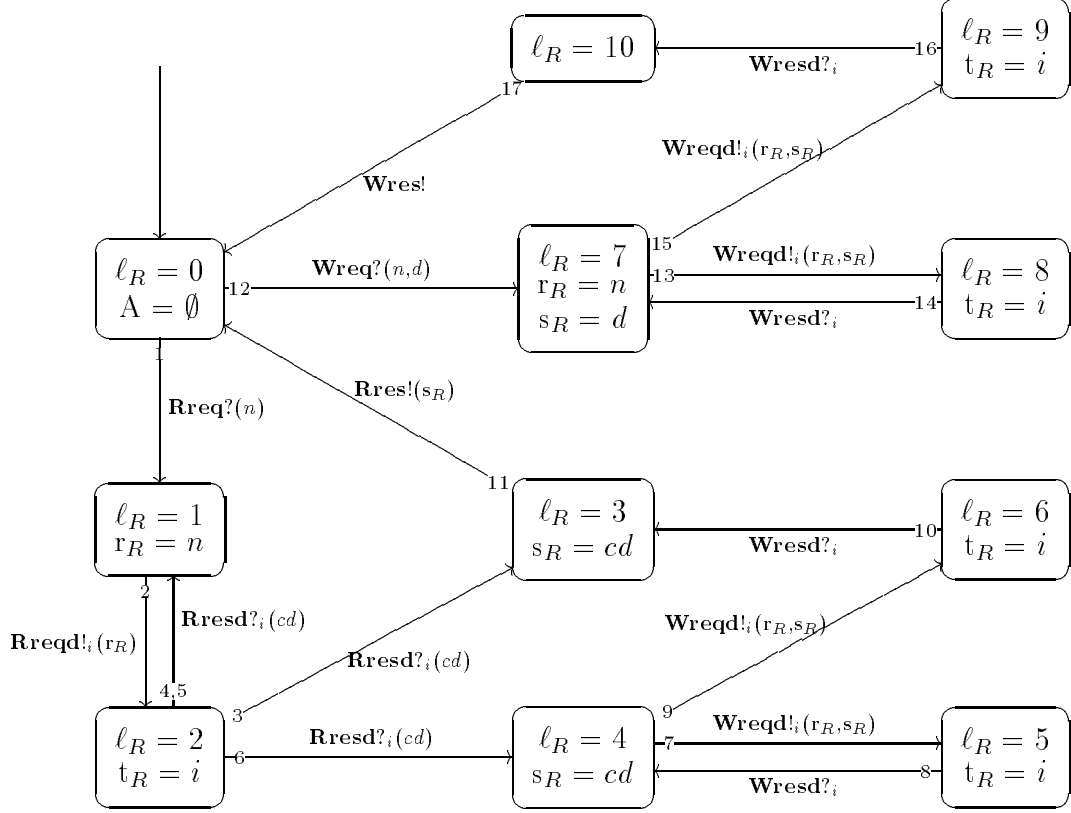


Figure 4.5: Transitions of the error recovery layer.

1. **Basis** $B_D = ((In_D, Out_D), (V_D, X_D))$

$$\begin{aligned}
 In_D &\triangleq \{\mathbf{Rreqd}, \mathbf{Wreqd}, \mathbf{Rresp}, \mathbf{Wresp}\}, \\
 Out_D &\triangleq \{\mathbf{Rresd}, \mathbf{Wresd}, \mathbf{Rreqp}, \mathbf{Wreqp}\}, \\
 V_D &\triangleq \emptyset, \\
 X_D &\triangleq \{\ell_D, r_D, s_D, LS_D[i] \mid i \in LN\}
 \end{aligned}$$

where LN is the set of logical sector numbers: $([1, \dots, Y])$. Let Lg be the set of data items that the user wants to store on or retrieve from the physical disk and Phy be the set information items that can be stored on or retrieved from the physical disk (Note: an item from Phy is an crc-encoded and address-encoded item of Lg .) For $n \in LN$, $c, d \in Lg$, $m \in PN$ and $cd, dd \in Phy$:

- **Rreqd?**(n): the request from the user to read logical sector n .
- **Rresd!**(c): the response of the detection layer to the previous request where c are the crc-decoded and address-decoded contents of the requested logical sector n .
- **Wreqd?**(n, d): write information item d onto logical sector n .
- **Wresd!**: response that previous write has been performed.

4.6 Fourth Step: Error Recovery Layer

- **Rreqp!**(m): the request from the detection layer to read physical sector m .
- **Rresp?**(cd): the response of the physical disk to the previous request where c are the crc-encoded and address-encoded contents of requested physical.
- **Wreqp!**(m, dd): write information item dd onto physical sector m .
- **Wresp?**: response that previous write has been performed.
- ℓ_D : local variable indicating the status of the detection layer; $\ell_D = 0$: the detection layer is waiting for a request, $\ell_D = 1$: the user has issued a read request, $\ell_D = 2$: the detection layer has issued a read request, $\ell_D = 3$: the physical has responded to a read request with correct data, $\ell_D = 4$: the physical disk has responded to a read request with incorrect data, $\ell_D = 5$: the detection layer has responded to a read request with an address error message (stop status), $\ell_D = 6$: the user has issued a write request, $\ell_D = 7$: the detection layer has issued a write request, $\ell_D = 8$: the physical disk has responded to a write request, $\ell_D = 9$: the detection layer has responded to a read request with a crc error message (can be repaired), $\ell_D = 9$: the user has issued a write request in order to repair the corresponding disk.
- r_D : local variable indicating the requested sector.
- s_D : local variable indicating the requested information or the data to be written.
- $LS_D[i]$: the physical sector mapped to logical sector

Let $\Psi_2 \triangleq (\ell_D, r_D, s_D, LS_D[1], \dots, LS_D[Y])$ and $\Psi'_2 \triangleq (\ell'_D, r'_D, s'_D, LS'_D[1], \dots, LS'_D[Y])$.

2. Initial states:

$$I_D \triangleq \ell_D = 0 \bigwedge_{i \in LN} LS_D[i] = i$$

3. Transitions:

The same detection mechanism as the fail-stop detection layer is used. Let *spare* be

a function that returns a spare physical sector number. Let

$Good$	\triangleq	$CC(cd) \wedge AC(CD(cd), LS_D[r_D])$ data has not been affected by faults
$A.er$	\triangleq	$CC(cd) \wedge \neg AC(CD(cd), LS_D[r_D])$ data has been affected by a control system fault
$C.er$	\triangleq	$\neg CC(cd)$ data has been affected by a disk surface damage
c	\triangleq	$AD(CD(cd))$ the address- and crc-decoded contents
m	\triangleq	$LS_D[r_D]$ physical sector
dd	\triangleq	$CE(AE(r_D, s_D))$ address- and crc-encoded contents
x	\triangleq	$spare$ spare physical sector
c_1	\triangleq	address error address error message
c_2	\triangleq	crc error crc error message

$T_D \triangleq$

$$\tau_{D,1} \quad (\epsilon = \mathbf{Rreqd}?(n) \wedge \ell_D = 0 \wedge \Psi'_2 = \Psi_2[1, n/\ell_D, r_D])$$

The user requests the contents of logical sector n .

$$\tau_{D,2} \quad \vee (\epsilon = \mathbf{Rreqp}!(m) \wedge \ell_D = 1 \wedge \Psi'_2 = \Psi_2[2/\ell_D])$$

The detection layer requests the to logical sector r_D mapped physical sector.

$$\tau_{D,3} \quad \vee (\epsilon = \mathbf{Rresp}?(cd) \wedge \ell_D = 2 \wedge Good \wedge \Psi'_2 = \Psi_2[3, c/\ell_D, s_D])$$

The physical disk responds with the contents of the requested sector and the detection layer detects no error in them.

$$\tau_{D,4} \quad \vee (\epsilon = \mathbf{Rresp}?(cd) \wedge \ell_D = 2 \wedge A.er \wedge \Psi'_2 = \Psi_2[4, c_1/\ell_D, s_D])$$

The physical disk responds with the contents of the requested sector and the detection layer detects an control system error.

$$\tau_{D,5} \quad \vee (\epsilon = \mathbf{Rresp}?(cd) \wedge \ell_D = 2 \wedge C.er \wedge \Psi'_2 = \Psi_2[4, c_2/\ell_D, s_D])$$

The physical disk responds with the contents of the requested sector and the detection layer detects an disk surface damage error.

$$\tau_{D,6} \quad \vee (\epsilon = \mathbf{Rresd}!(s_D) \wedge \ell_D = 3 \wedge \Psi'_2 = \Psi_2[0/\ell_D])$$

The detection layer responds with the contents of the user requested sector.

$$\tau_{D,7} \quad \vee (\epsilon = \mathbf{Rresd}!(s_D) \wedge \ell_D = 4 \wedge s_D = c_1 \wedge \Psi'_2 = \Psi_2[5/\ell_D])$$

In case of an address error the detection layer responds with the corresponding error message and then stops.

4.6 Fourth Step: Error Recovery Layer

$$\tau_{D,8} \quad \vee \left(\epsilon = \mathbf{Rresd!}(s_D) \wedge \ell_D = 4 \wedge s_D = c_2 \wedge \Psi'_2 = \Psi_2 [9/\ell_D] \right)$$

The detection layer responds with an error message and waits for the corrective action.

$$\tau_{D,9} \quad \vee \left(\epsilon = \mathbf{Wreqd?}(n, d) \wedge \ell_D = 9 \wedge \Psi'_2 = \Psi_2 [10, n, d, x/\ell_D, r_D, s_D, LS_D[n]] \right)$$

The user requests that d should be written on a spare physical sector.

$$\tau_{D,10} \quad \vee \left(\epsilon = \mathbf{Wreqp!}(m, dd) \wedge \ell_D = 10 \wedge \Psi'_2 = \Psi_2 [7/\ell_D] \right)$$

The detection layer requests that dd should be written onto physical sector m .

$$\tau_{D,11} \quad \vee \left(\epsilon = \mathbf{Wreqd?}(n, d) \wedge \ell_D = 0 \wedge \Psi'_2 = \Psi_2 [6, n, d/\ell_D, r_D, s_D] \right)$$

The user requests that d should be written onto logical sector n .

$$\tau_{D,12} \quad \vee \left(\epsilon = \mathbf{Wreqp!}(m, dd) \wedge \ell_D = 6 \wedge \Psi'_2 = \Psi_2 [7/\ell_D] \right)$$

The detection requests that dd should be written onto physical sector m .

$$\tau_{D,13} \quad \vee \left(\epsilon = \mathbf{Wresp!} \wedge \ell_D = 7 \wedge \Psi'_2 = \Psi_2 [8/\ell_D] \right)$$

The physical disk responds with a signal to the detection layer that requested write is performed.

$$\tau_{D,14} \quad \vee \left(\epsilon = \mathbf{Wresd!} \wedge \ell_D = 8 \wedge \Psi'_2 = \Psi_2 [0/\ell_D] \right)$$

The detection layer responds with a signal to the user that requested write is performed.

$$\tau_{D,0} \quad \vee \mathbf{stut}_D$$

These transitions are illustrated in figure 4.6

4. Liveness conditions:

The liveness condition expresses that the communication transitions are strongly fair.

Let $SF_D = \{\tau_{D,i} \mid i \in \{1, \dots, 14\}\}$ then

$$L_D \triangleq \bigwedge_{\tau \in SF_D} (\Box \Diamond E_n(\tau) \rightarrow \Box \Diamond \tau)$$

4.6.4 Requirement W_R

The error recovery requirement should express that for all logical locations there exists at least one non-disabled disk that has correct data stored on that logical location and enough spare locations should be available on an affected disk.

$$\begin{aligned} W_R &\triangleq \\ &\Box (\bigwedge_{n \in LN} (\exists i \in G. CC_i(M_{P_i}[LS_{D_i}[n]]) \wedge AC_i(CD_i(M_{P_i}[LS_{D_i}[n]], LS_{D_i}[n]))) \\ &\Box (\forall i \in G. \exists m \in PN_i. m = spare_i) \end{aligned}$$

This corresponds to the following machine: Let

$$\begin{aligned} p_3 &\triangleq (\bigwedge_{n \in LN} (\exists i \in G. CC_i(M_{P_i}[LS_{D_i}[n]]) \wedge AC_i(CD_i(M_{P_i}[LS_{D_i}[n]], LS_{D_i}[n]))) \\ &\quad \wedge (\forall i \in G. \exists m \in PN_i. m = spare_i)) \\ p'_3 &\triangleq (\bigwedge_{n \in LN} (\exists i \in G'. CC_i(M'_{P_i}[LS'_{D_i}[n]]) \wedge AC_i(CD_i(M'_{P_i}[LS'_{D_i}[n]], LS'_{D_i}[n]))) \\ &\quad \wedge (\forall i \in G'. \exists m \in PN_i. m = spare_i)) \end{aligned}$$

then W_R is equal to the machine $p_3 \wedge \Box((p_3 \wedge p'_3) \vee stut_3)$.

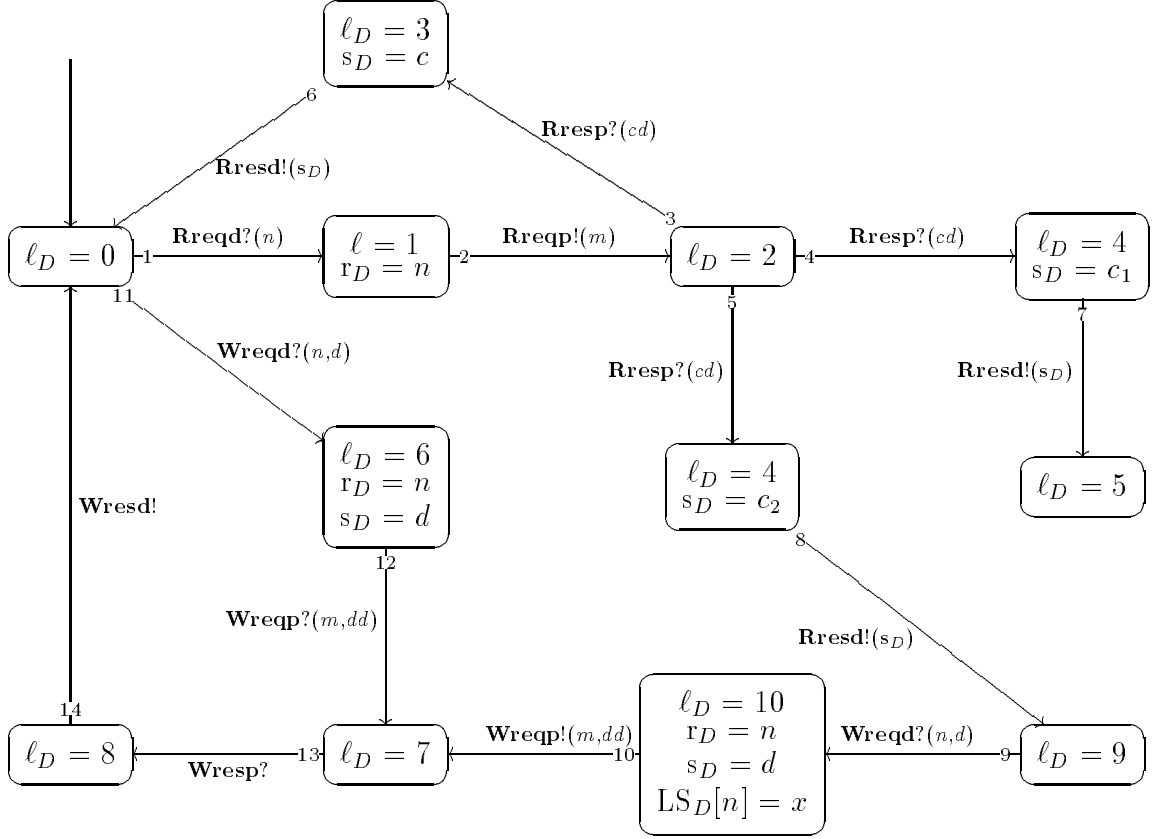


Figure 4.6: Transitions of the detection layer.

4.6.5 $\|_{i=1}^N (\mathcal{S}_{Di} \parallel \mathcal{S}_{Pi}) \parallel \mathcal{S}_R$ relatively refines $\mathcal{S}_{Ds} \parallel \mathcal{S}_P$

First we construct the system $\mathcal{S}_3 \triangleq \|_{i=1}^N (\mathcal{S}_{Di} \parallel \mathcal{S}_{Pi}) \parallel \mathcal{S}_R$ then according to theorem 8 \mathcal{S}_3 relatively refines $\mathcal{S}_2 (= \mathcal{S}_{Ds} \parallel \mathcal{S}_{P1})$ with respect to (W_R, \mathbf{true}) iff the following holds:

$$\begin{aligned} & \mathfrak{D}(B_3) = \mathfrak{D}(B_2) \text{ and} \\ & \models (\exists X_3. (G_3)) \rightarrow (\exists X_2. (G_2)) \end{aligned}$$

where X_2 are the local variables from $\mathcal{S}_{Ds} \parallel \mathcal{S}_{P1}$, i.e.,

$X_2 \triangleq \{\ell_{Ds}, r_{Ds}, s_{Ds}, LS_{Ds}[i] \mid i \in LN\} \cup \{\ell_{P1}, r_{P1}, s_{P1}, M_{P1}[n], F[n] \mid n \in PN\}$ and G_2 is defined as

$$\exists \epsilon_1, \epsilon_2. B_{Ds}^A \odot B_{P1}^A (\epsilon, \epsilon_1, \epsilon_2) \wedge (H_{Ds} \wedge W_{Ds})[\epsilon_1/\epsilon] \wedge (H_{P1} \wedge W_{P1})[\epsilon_2/\epsilon]$$

This can be rewritten to following machine specification of \mathcal{S}_2 : $\mathcal{S}_2 = (B_2, H_2)$ where $H_2 \triangleq I_2 \wedge \square T_2 \wedge L_2$ and B_2, I_2, T_2 and L_2 are defined in section 4.5. X_3 are the local variables from \mathcal{S}_3 , i.e., $X_3 \triangleq (\cup_{j=1}^N \{\ell_{Dj}, r_{Dj}, s_{Dj}, LS_{Dj}[i] \mid i \in LN\}) \cup \{\ell_{Pj}, r_{Pj}, s_{Pj}, M_{Pj}[n], F_j[n] \mid n \in PN\} \cup \{\ell_R, r_R, s_R, t_R, G, A, W\}$. Let $\bar{\epsilon} \triangleq \epsilon_{1,1}, \dots, \epsilon_{1,N}, \epsilon_{2,1}, \dots, \epsilon_{2,N}, \epsilon_3$ and let $\bar{B}^A \triangleq B_{D1}^A, \dots, B_{DN}^A, B_{P1}^A, \dots, B_{PN}^A, B_R^A$ then G_3 is defined as

$$(\exists \bar{\epsilon}. \odot_{\bar{B}^A} (\epsilon, \bar{\epsilon}) \wedge (\bigwedge_{j=1}^N (H_{Dj})[\epsilon_{1,j}/\epsilon] \wedge (H_{Pj})[\epsilon_{2,j}/\epsilon]) \wedge H_R[\epsilon_3/\epsilon]) \wedge W_R$$

4.6 Fourth Step: Error Recovery Layer

The $(\exists \bar{e}. \odot_{B^A}(\epsilon, \bar{e})(\bigwedge_{j=1}^N (\mathbf{H}_{D_j})[\epsilon_{1,j}/\epsilon] \wedge (\mathbf{H}_{P_j})[\epsilon_{2,j}/\epsilon]) \wedge \mathbf{H}_R[\epsilon_3/\epsilon])$ part can be rewritten to following machine specification $\mathcal{S}_3 \triangleq (B_3, H_3)$ where $H_3 \triangleq I_3 \wedge \square T_3 \wedge L_3$ and B_3, I_3, T_3 and L_3 are as follows:

1. **Basis** $B_3 = ((\text{In}_3, \text{Out}_3), (\text{V}_3, \text{X}_3))$

$$\begin{aligned} \text{In}_3 &\triangleq \{\mathbf{Rreq}, \mathbf{Wreq}\} \\ \text{Out}_3 &\triangleq \{\mathbf{Rres}, \mathbf{Wres}\}, \\ \text{V}_3 &\triangleq \emptyset, \\ \text{X}_3 &\triangleq \text{as above} \end{aligned}$$

Let

$$\begin{aligned} \Psi_3 &\triangleq ((\ell_{D_j}, r_{D_j}, s_{D_j}, \text{LS}_{D_j}[1], \dots, \text{LS}_{D_j}[Y], \\ &\quad \ell_{P_j}, r_{P_j}, s_{P_j}, \text{M}_{P_j}[1], \dots, \text{M}_{P_j}[Y], \text{F}_j[1], \dots, \text{F}_j[Y])_{j=1, \dots, N}, \\ &\quad \ell_R, r_R, s_R, t_R, \mathbf{G}, \mathbf{A}, \mathbf{W}) \\ \Psi'_3 &\triangleq ((\ell'_{D_j}, r'_{D_j}, s'_{D_j}, \text{LS}'_{D_j}[1], \dots, \text{LS}'_{D_j}[Y], \\ &\quad \ell'_{P_j}, r'_{P_j}, s'_{P_j}, \text{M}'_{P_j}[1], \dots, \text{M}'_{P_j}[Y], \text{F}'_j[1], \dots, \text{F}'_j[Y])_{j=1, \dots, N} \\ &\quad \ell'_R, r'_R, s'_R, t'_R, \mathbf{G}', \mathbf{A}', \mathbf{W}') \end{aligned}$$

2. **Initial states:**

$$I_3 \triangleq \bigwedge_{j=1}^N (I_{D_j} \wedge I_{P_j}) \wedge I_R$$

3. **Transitions:**

Let

- $c_1 \triangleq$ address error
address error message
- $c_2 \triangleq$ crc error
crc error message
- $\text{Good1} \triangleq i = t_R \wedge dc \neq c_1 \wedge dc \neq c_2 \wedge \mathbf{A} = \emptyset$
data is not affected by faults and the number of affected disks is zero
- $\text{Good2} \triangleq i = t_R \wedge dc \neq c_1 \wedge dc \neq c_2 \wedge \mathbf{A} \neq \emptyset$
data is not affected by faults and the number of affected disks is non-zero
- $A.er \triangleq i = t_R \wedge dc = c_1$
data is affected by control system fault
- $C.er \triangleq i = t_R \wedge dc = c_2$
data is affected by disk surface damage
- $\mathbf{G}^- \triangleq \mathbf{G} \setminus \{i\}$
set of good disks minus i

- $A^- \triangleq A \setminus \{i\}$
set of affected disks minus i
- $A^+ \triangleq A \cup \{i\}$
set of affected disks plus i
- $W^- \triangleq W \setminus \{i\}$
set of to be written disks minus i
- $C1 \triangleq i \in G \wedge i \notin A$
disk i is good and not affected
- $C2 \triangleq i \in A \wedge A^- \neq \emptyset$
disk i is affected and the number of affected disks is greater than 1
- $C3 \triangleq i \in A \wedge A^- = \emptyset$
disk i is the only affected disk
- $C4 \triangleq i \in W \wedge W^- \neq \emptyset$
disk i should be written onto and the number of to be written disks is greater than 1
- $C5 \triangleq i \in W \wedge W^- = \emptyset$
disk i is the only disk to be written onto
- $q \triangleq (\ell_{Di}, \ell_{Pi}, \ell_R)$
status of detection layer i and physical disk i and the error recovery layer.

$T_3 \triangleq$

$$\tau_{3,1} \quad \left(\epsilon = \mathbf{Rreq}?(n) \wedge \ell_R = 0 \wedge \Psi'_3 = \Psi_3[1, n/\ell_R, r_R] \right)$$

The user requests the contents of logical sector n .

$$\tau_{3,2} \quad \vee \left(\epsilon = \mathbf{i} \wedge \ell_R = 1 \wedge C1 \wedge \Psi'_3 = \Psi_3[2, i, 1, r_R/\ell_R, t_R, \ell_{Di}, r_{Di}] \right)$$

The error recovery layer requests the contents of logical sector r_R from an enabled detection layer i .

$$\tau_{3,3} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (1, 0, 2) \wedge \Psi'_3 = \Psi_3[2, 1, m/\ell_{Di}, \ell_{Pi}, r_{Pi}] \right)$$

The detection layer i requests the to logical sector r_{Di} mapped physical sector from physical disk i .

$$\tau_{3,4} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (2, 1, 2) \wedge Good_i \wedge \Psi'_3 = \Psi_3[3, c, 0/\ell_{Di}, s_{Di}, \ell_{Pi}] \right)$$

The physical disk i responds with the contents of the requested sector and the detection layer i detects no error in them.

$$\tau_{3,5} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (2, 1, 2) \wedge A.er_i \wedge \Psi'_3 = \Psi_3[4, c_1, 0/\ell_{Di}, s_{Di}, \ell_{Pi}] \right)$$

The physical disk i responds with the contents of the requested sector and the detection layer i detects a control system error.

$$\tau_{3,6} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (2, 1, 2) \wedge C.er_i \wedge \Psi'_2 = \Psi_2[4, c_2, 0/\ell_{Di}, s_{Di}, \ell_{Pi}] \right)$$

The physical disk i responds with the contents of the requested sector and the detection layer i detects a disk surface damage error.

4.6 Fourth Step: Error Recovery Layer

- $\tau_{3,7} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (3, 0, 2) \wedge \text{Good1} \wedge \Psi'_3 = \Psi_3 [0, 3, s_{Di}/\ell_{Di}, \ell_R, s_R] \right)$
The detection layer i responds with the contents of the requested sector and the detection layer i has detected no error in them.
- $\tau_{3,8} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (4, 0, 2) \wedge A.er_i \wedge \Psi'_3 = \Psi_3 [5, 1, G^-/\ell_{Di}, \ell_R, G] \right)$
The detection layer i responds with the contents of the requested sector and the detection layer i has detected an control system error, so this detection layer will be disabled.
- $\tau_{3,9} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (4, 0, 2) \wedge C.er_i \wedge \Psi'_3 = \Psi_3 [9, 1, A^+/\ell_{Di}, \ell_R, A] \right)$
The detection layer i responds with the contents of the requested sector and the detection layer i detects an disk surface damage error, so physical disk i has to be repaired.
- $\tau_{3,10} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (3, 0, 2) \wedge \text{Good2} \wedge \Psi'_3 = \Psi_3 [0, 4, s_{Di}/\ell_{Di}, \ell_R, s_R] \right)$
A correct disk i has been found so the error recovery layer can repair the affected disks.
- $\tau_{3,11} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (9, 0, 4) \wedge C2 \right.$
 $\quad \left. \wedge \Psi'_3 = \Psi_3 [10, r_R, s_R, x_i, 5, i, A^-/\ell_{Di}, r_{Di}, s_{Di}, LS_{Di}, \ell_R, t_R, A] \right)$
An affected disk i is being repaired and there are still unrepaired disk.
- $\tau_{3,12} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (10, 0, 5) \wedge \Psi'_3 = \Psi_3 [7, 2, m_i, dd_i/\ell_{Di}, \ell_{Pi}, r_{Pi}, s_{Pi}] \right)$
An affected disk i is being repaired and there are still unrepaired disk.
- $\tau_{3,13} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (7, 2, 5) \wedge \Psi'_3 = \Psi_3 [8, 0, s_{Pi}/\ell_{Di}, \ell_{Pi}, M_{Pi}[F_{Pi}[r_{Pi}]]] \right)$
An affected disk i is being repaired and there are still unrepaired disk.
- $\tau_{3,14} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (8, 0, 5) \wedge i = t_R \wedge \Psi'_3 = \Psi_3 [0, 4/\ell_{Di}, \ell_R] \right)$
The affected disk is repaired.
- $\tau_{3,15} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (9, 0, 4) \wedge C3 \right.$
 $\quad \left. \wedge \Psi'_3 = \Psi_3 [10, 6, r_R, s_R, x_i, A^-/\ell_{Di}, r_{Di}, s_{Di}, LS_{Di}, \ell_R, A] \right)$
An affected disk is being repaired and there are no further unrepaired disks.
- $\tau_{3,16} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (10, 0, 6) \wedge \Psi'_3 = \Psi_3 [7, 2, m_i, dd_i/\ell_{Di}, \ell_{Pi}, r_{Pi}, s_{Pi}] \right)$
An affected disk i is being repaired and there are no further unrepaired disks.
- $\tau_{3,17} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (7, 2, 6) \wedge \Psi'_3 = \Psi_3 [8, 0, s_{Pi}/\ell_{Di}, \ell_{Pi}, M_{Pi}[F_{Pi}[r_{Pi}]]] \right)$
An affected disk i is being repaired and there are no further unrepaired disks.
- $\tau_{3,18} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (8, 0, 6) \wedge i = t_R \wedge \Psi'_3 = \Psi_3 [0, 3/\ell_{Di}, \ell_R] \right)$
All affected disk are repaired, so the user requested contents can be sent.
- $\tau_{3,19} \quad \vee \left(\epsilon = \mathbf{Rres}!(s_R) \wedge \ell_R = 3 \wedge \Psi'_3 = \Psi_3 [0/\ell_R] \right)$
The error recovery layer responds with the requested contents.
- $\tau_{3,20} \quad \vee \left(\epsilon = \mathbf{Wreq}?(n, d) \wedge \ell_R = 0 \wedge \Psi'_3 = \Psi_3 [7, n, d, G/\ell_R, r_R, s_R, W] \right)$
The user requests that d should be written onto logical sector n .
- $\tau_{3,21} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (0, 0, 7) \wedge C4 \right.$
 $\quad \left. \wedge \Psi'_3 = \Psi_3 [8, 6, r_R, s_R, i, W^-/\ell_R, \ell_{Di}, r_{Di}, s_{di}, t_R, W] \right)$
The requested information is being written to a disk and there are still unwritten disks.

- $\tau_{3,22} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (6, 0, 8) \wedge \Psi'_3 = \Psi_3[7, 2, m_i, dd_i/\ell_{Di}, \ell_{Pi}, r_{Pi}, s_{Pi}] \right)$
 The requested information is being written to a disk and there are still unwritten disks.
- $\tau_{3,23} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (7, 2, 8) \wedge \Psi'_3 = \Psi_3[8, 0, s_{Pi}/\ell_{Di}, \ell_{Pi}, M_{Pi}[F_{Pi}[r_{Pi}]]] \right)$
 The requested information is being written to a disk and there are still unwritten disks.
- $\tau_{3,24} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (8, 0, 8) \wedge i = t_R \wedge \Psi'_3 = \Psi_3[0, 7/\ell_{Di}, \ell_R] \right)$
 The requested information is written onto disk i .
- $\tau_{3,25} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (0, 0, 7) \wedge C5 \right.$
 $\quad \left. \wedge \Psi'_3 = \Psi_3[9, 6, r_R, s_R, i, W^-/\ell_R, \ell_{Di}, r_{Di}, s_{Di}, t_R, W] \right)$
 The requested information is being written to a disk and there are no further unwritten disks.
- $\tau_{3,26} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (6, 0, 9) \wedge \Psi'_3 = \Psi_3[7, 2, m_i, dd_i/\ell_{Di}, \ell_{Pi}, r_{Pi}, s_{Pi}] \right)$
 The requested information is being written to a disk and there are no further unwritten disks.
- $\tau_{3,27} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (7, 2, 9) \wedge \Psi'_3 = \Psi_3[8, 0, s_{Pi}/\ell_{Di}, \ell_{Pi}, M_{Pi}[F_{Pi}[r_{Pi}]]] \right)$
 The requested information is being written to a disk and there are no further unwritten disks.
- $\tau_{3,28} \quad \vee \left(\epsilon = \mathbf{i} \wedge q = (8, 0, 9) \wedge i = t_R \wedge \Psi'_3 = \Psi_3[0, 10/\ell_{Di}, \ell_R] \right)$
 The requested information is written onto all disks.
- $\tau_{3,29} \quad \vee \left(\epsilon = \mathbf{Wres}! \wedge \ell_R = 10 \wedge \Psi'_3 = \Psi_3[0/\ell_R] \right)$
 The error recovery layer responds with a signal to the user that requested write is performed.
- $\tau_{3,0} \quad \vee \mathbf{stut}_3$

These transitions are illustrated in figure 4.7 with the transitions for the physical disk omitted.

4. Liveness Condition:

The liveness condition expresses that all non-stuttering transitions are strongly fair. Let $SF_3 = \{\tau_{3,i} \mid i \in \{1, \dots, 29\}\}$ then

$$L_3 \stackrel{\Delta}{=} \bigwedge_{\tau \in SF_3} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

Rule 3 will be used to prove

$$\models (\exists X_3 . (G_3)) \rightarrow (\exists X_2 . (G_2)).$$

4.6 Fourth Step: Error Recovery Layer

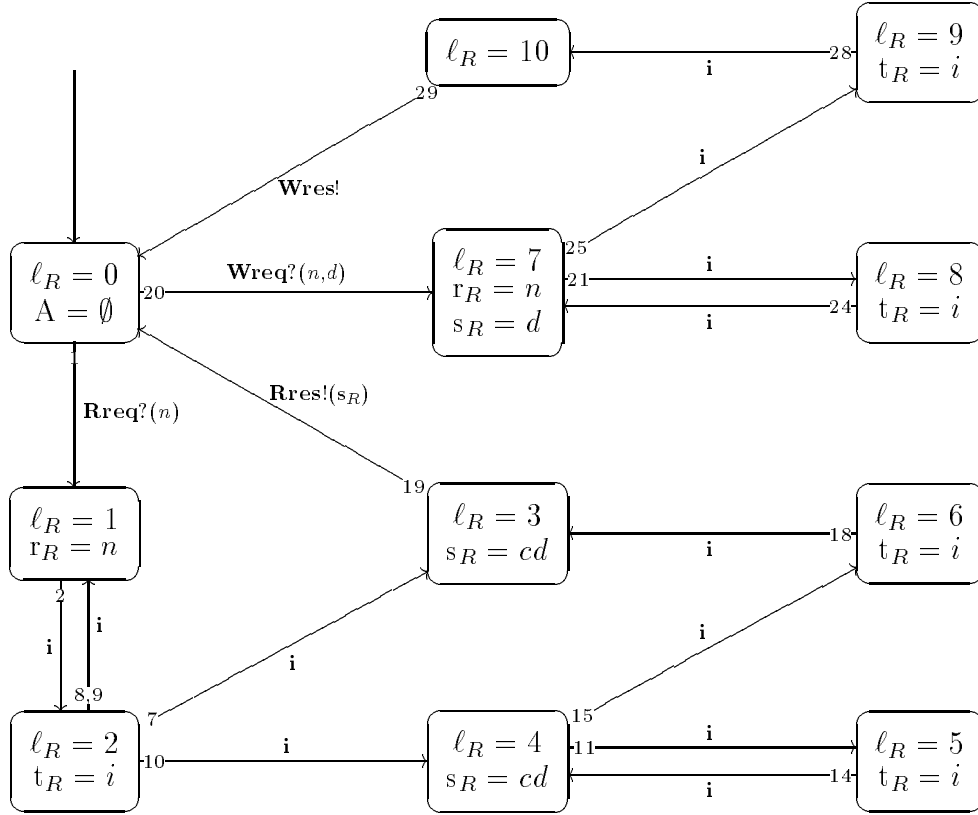


Figure 4.7: Transitions of the final implementation of stable storage.

This means one has to prove (a), (b) and (c) below, for \bar{f} the refinement mapping from \mathcal{S}_3 to \mathcal{S}_2 , defined as: $\bar{f} = f_{l_{D_s}}, f_{r_{D_s}}, f_{s_{D_s}}, f_{LS_{D_s}[m]}, \dots, f_{LS_{D_s}[Y]}, f_{l_{P_1}}, f_{r_{P_1}}, f_{s_{P_1}}, f_{M_{P_1}[n]}, f_{F_{P_1}[n]}$ ($n \in SN, m \in LN$). The refinement mappings are defined as:

$f_{l_{P_1}}$	$l_R = 0$	<i>then</i>	l_R
<i>if</i>	$l_R = 1$	<i>then</i>	$l_R - 1$
	$l_R = 2 \wedge l_{P_i} = 0$	<i>then</i>	$l_R - 2$
	$l_R = 2 \wedge l_{P_i} = 1$	<i>then</i>	$l_R - 1$
	$l_R = 3$	<i>then</i>	$l_R - 3$
	$l_R = 4$	<i>then</i>	$l_R - 4$
	$l_R = 5$	<i>then</i>	$l_R - 5$
	$l_R = 6$	<i>then</i>	$l_R - 6$
	$l_R = 7$	<i>then</i>	$l_R - 7$
	$l_R = 8 \wedge l_{P_i} = 0$	<i>then</i>	$l_R - 8$
	$l_R = 8 \wedge l_{P_i} = 2$	<i>then</i>	$l_R - 6$
	$l_R = 9 \wedge l_{P_i} = 0$	<i>then</i>	$l_R - 9$
	$l_R = 9 \wedge l_{P_i} = 2$	<i>then</i>	$l_R - 7$
	$l_R = 10$	<i>then</i>	$l_R - 10$

$f_{loc l_{D_s}}$		
$i f$	$\ell_R = 0$	$then \ell_R$
	$\ell_R = 1$	$then \ell_R$
	$\ell_R = 2 \wedge \ell_{P_i} = 0$	$then \ell_R - 1$
	$\ell_R = 2 \wedge \ell_{P_i} = 1$	$then \ell_R - 1$
	$\ell_R = 3$	$then \ell_R$
	$\ell_R = 4$	$then \ell_R - 2$
	$\ell_R = 5$	$then \ell_R - 3$
	$\ell_R = 6$	$then \ell_R - 4$
	$\ell_R = 7$	$then \ell_R - 1$
	$\ell_R = 8$	$then \ell_R - 1$
	$\ell_R = 9$	$then \ell_R - 2$
	$\ell_R = 10$	$then \ell_R - 2$

For $i \in G \wedge i \notin A$ (physical i is not affected by any fault)

$f_{r_{P_1}}$	\triangleq	r_{P_i}
$f_{s_{P_1}}$	\triangleq	s_{P_i}
$f_{M_{P_1}[n]}$	\triangleq	$M_{P_i}[n]$
$f_{F_{P_1}[n]}$	\triangleq	$F_{P_i}[n]$
$f_{r_{D_s}}$	\triangleq	r_{D_i}
$f_{s_{D_s}}$	\triangleq	s_{D_i}
$f_{LS_{D_s}[m]}$	\triangleq	$LS_{D_i}[m]$

- (a) $\mathcal{S}_3 \cap Hist(W_R) \models (I_3 \wedge p_3) \rightarrow (I_2) [\bar{f}/X_2]$
- (b) $\mathcal{S}_3 \cap Hist(W_R) \models (T_3 \wedge ((p_3 \wedge p'_3) \vee \mathbf{stut}_3)) \rightarrow (T_2) [\bar{f}/X_2]$
- (c) $\mathcal{S}_3 \cap Hist(W_R) \models L_2 [\bar{f}/X_2]$

(a) **Proof 13**

$$\begin{aligned} & I_3 \wedge p_3 \\ \rightarrow & \quad \% Def. I_3, p_3, \bar{f}, I_2 \\ & (I_2) [\bar{f}/X_2] \end{aligned}$$

(b) **Proof 14**

Since T_3 is of the form $\mathbf{stut}_3 \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau})$ then $T_3 \wedge ((p_3 \wedge p'_3) \vee \mathbf{stut}_3)$ is equal to $\mathbf{stut}_3 \vee \bigvee_{\tau} (\epsilon = \mathbf{a}_{\tau} \wedge trans_{\tau} \wedge p_3 \wedge p'_3)$.

$$\begin{aligned} - & \quad \tau_{3,1} \wedge p_3 \wedge p'_3 \\ \rightarrow & \quad (\tau_{2,1}) [\bar{f}/X_2] \end{aligned}$$

The user read request at the third level corresponds to the user read request at the second level.

4.6 Fourth Step: Error Recovery Layer

$$\begin{aligned} - & \quad \tau_{3,2} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read request to the detection layer i at the third level corresponds to the stutter step at the second level.

$$\begin{aligned} - & \quad \tau_{3,3} \wedge p_3 \wedge p'_3 \\ & \rightarrow (\tau_{2,2}) \left[\bar{f}/X_2 \right] \end{aligned}$$

The read request to physical disk i at the third level corresponds to the read request to the physical disk at the second level.

$$\begin{aligned} - & \quad \tau_{3,4} \wedge p_3 \wedge p'_3 \\ & \rightarrow (\tau_{2,3}) \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the physical disk i at the third level corresponds to the read response of the physical disk at the second level because no errors are detected.

$$\begin{aligned} - & \quad \tau_{3,5} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the physical disk i at the third level corresponds to the stutter step at the second level because a control system error is detected.

$$\begin{aligned} - & \quad \tau_{3,6} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the physical disk i at the third level corresponds to the stutter step at the second level because a disk surface error is detected.

$$\begin{aligned} - & \quad \tau_{3,7} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the detection i at the third level corresponds to the stutter step at the second level.

$$\begin{aligned} - & \quad \tau_{3,8} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the detection i at the third level corresponds to the stutter step at the second level.

$$\begin{aligned} - & \quad \tau_{3,9} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the detection i at the third level corresponds to the stutter step at the second level.

$$\begin{aligned} - & \quad \tau_{3,10} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The read response of the detection i at the third level corresponds to the stutter step at the second level.

- For $j = 11, \dots, 18$

$$\begin{aligned} & \quad \tau_{3,j} \wedge p_3 \wedge p'_3 \\ & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right] \end{aligned}$$

The correction step at the third level corresponds to the stutter step at the second level.

$$\begin{aligned}
 - & \quad \tau_{3,19} \wedge p_3 \wedge p'_3 \\
 & \rightarrow (\tau_{2,4}) \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The read response to the user at the third level corresponds to read response to the user at the second level.

$$\begin{aligned}
 - & \quad \tau_{3,20} \wedge p_3 \wedge p'_3 \\
 & \rightarrow (\tau_{2,4}) \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write request of the user at the third level corresponds to the write request of the user at the second level.

- For $j = 21, 25$

$$\begin{aligned}
 & \tau_{3,j} \wedge p_3 \wedge p'_3 \\
 & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write request to the detection layer i at the third level corresponds to the stutter step at the second level.

- For $j = 22, 26$

$$\begin{aligned}
 & \tau_{3,j} \wedge p_3 \wedge p'_3 \\
 & \rightarrow (\tau_{2,6}) \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write request to the physical disk i at the third level corresponds to the write request to the physical disk at the second level.

- For $j = 23, 27$

$$\begin{aligned}
 & \tau_{3,j} \wedge p_3 \wedge p'_3 \\
 & \rightarrow (\tau_{2,7}) \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write response of the physical disk i at the third level corresponds to the write response of the physical disk at the second level.

- For $j = 24, 28$

$$\begin{aligned}
 & \tau_{3,j} \wedge p_3 \wedge p'_3 \\
 & \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write response of the detection layer i at the third level corresponds to the stutter step at the second level.

$$\begin{aligned}
 - & \quad \tau_{3,29} \wedge p_3 \wedge p'_3 \\
 & \rightarrow (\tau_{2,8}) \left[\bar{f}/X_2 \right]
 \end{aligned}$$

The write response to the user at the third level corresponds to the write response to the user at the second level.

$$- \quad \mathbf{stut}_3 \rightarrow \mathbf{stut}_2 \left[\bar{f}/X_2 \right]$$

4.6 Fourth Step: Error Recovery Layer

(c) Let $SF_3 = \{\tau_{3,i} \mid i \in \{1, \dots, 29\}\}$ then

$$L_3 \triangleq \bigwedge_{\tau \in SF_3} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau)$$

. Let $SF_2 = \{\tau_{2,i} \mid i \in \{1, \dots, 8\}\}$ then

$$L_2 \triangleq \bigwedge_{\tau \in SF_2} (\Box \Diamond En(\tau) \rightarrow \Box \Diamond \tau).$$

Then the following holds

$$\mathcal{S}_3 \cap Hist(W_3) \models L_3 \rightarrow L_2 [\bar{f}/X_3]$$

So

$$\mathcal{S}_3 \cap Hist(W_3) \models L_2 [\bar{f}/X_2]$$

holds.

Bibliography

- [Acz83] P. Aczel. On an inference rule for parallel composition, 1983. Unpublished, University of Manchester.
- [AFK88] K. R. Apt, N. Francez, and S. Katz. Appraising fairness in languages for distributed programming. *Distributed Computing*, 2(4):226–241, 1988.
- [AL91] M. Abadi and L. Lamport. The existence of refinement mappings. *Theoretical Computer Science*, 82(2):253–284, May 1991.
- [AL93a] M. Abadi and L. Lamport. Composing specifications. *ACM Transactions on Programming Languages and Systems*, 15(1):73–132, 1993.
- [AL93b] M. Abadi and L. Lamport. Conjoining specifications. Technical Report 118, Digital Systems Research Center, 1993.
- [AS85] B. Alpern and F.B. Schneider. Defining liveness. *Information Processing Letters*, 21(4):181–185, 1985.
- [AS87] B. Alpern and F. Schneider. Proving boolean combinations of deterministic properties. In *Proceedings of the second symposium on logic in computer science*, pages 131–137. IEEE, June 1987.
- [BKP86] H. Barringer, R. Kuiper, and A. Pnueli. A really abstract concurrent model and its temporal semantics. In *Proc. 13th ACM Symp. Princ. of Prog. Lang.*, pages 173–183, 1986.
- [Bur82] J.P. Burgess. Axioms for tense logic, i. “since” and “until”. *Notre Dame Journal of Formal Logic*, 23(4):367–374, October 1982.
- [Bur84] J.P. Burgess. Basic tense logic. In D. Gabbay and F. Guentner, editors, *Handbook of Philosophical Logic.*, volume II, pages 89–133. Reidel Publishers, 1984.
- [CC94] A. Cau and P. Collette. Parallel composition of assumption-commitment specifications: a unifying approach for shared variable and distributed message passing concurrency. To appear in *Acta Informatica*, 1994.
- [CdR93a] A. Cau and W.-P. de Roever. Specifying fault tolerance within stark’s formalism. In *Proc. of the Twenty-Third International Symposium on Fault-Tolerant Computing*, pages 392–401. IEEE, 1993.

- [CdR93b] A. Cau and W.-P. de Roever. Using relative refinement for fault tolerance. In *Proceedings of FME'93 symposium: industrial strength formal methods*, 1993.
- [CKdR92] A. Cau, R. Kuiper, and W.-P. de Roever. Formalising Dijkstra's development strategy within Stark's formalism. In C. B. Jones, R. C. Shaw, and T. Denvir, editors, *Proc. 5th. BCS-FACS Refinement Workshop*, 1992.
- [Cri85] F. Cristian. A rigorous approach to fault-tolerant programming. *IEEE Transactions on Software Engineering*, 11(1):23–31, 1985.
- [Dij79] E.W. Dijkstra. A tutorial on the split binary semaphore, 1979. EWD 703.
- [DK90] E. Diepstraten and R. Kuiper. Abadi & Lamport and Stark: towards a proof theory for stuttering, dense domains and refinements mappings. In *LNCS 430:Proc. of the REX Workshop on Stepwise Refinement of Distributed Systems, Models, Formalisms, Correctness*, pages 208–238. Springer-Verlag, 1990.
- [Hoa84] C.A.R. Hoare. *Communicating Sequential Processes*. Prentice-Hall, London, 1984.
- [Jon83] C.B. Jones. Tentative steps towards a development method for interfering programs. *ACM Transactions on Programming Languages and Systems*, 5(4):596–619, 1983.
- [KMP93] Y. Kesten, Z. Manna, and A. Pnueli. Temporal verification of simulation and refinement. In J.W. de Bakker, W.-P. de Roever, and G. Rozenberg, editors, *LNCS 803: A Decade of Concurrency, Reflections and Perspectives*, pages 273–346. Springer-Verlag, 1993.
- [LA90] P.A. Lee and T. Anderson. *Fault Tolerance Principles and Practice*, volume 3 of *Dependable Computing and Fault-Tolerant Systems*. Springer-Verlag, second, revised edition, 1990.
- [Lam] L. Lamport. TLA - Temporal Logic of Actions. The hypertext page on TLA: www.research.digital.com/SRC/personal/Leslie.Lamport/tla/tla.html.
- [Lam83] L. Lamport. What good is temporal logic. In R.E.A. Manson, editor, *Information Processing 83: Proc. of the IFIP 9th World Congress*, pages 657–668. Elsevier Science Publishers, North Holland, 1983.
- [Lam89] L. Lamport. A simple approach to specifying concurrent systems. *Communications of the ACM*, 32(1):32–45, January 1989.
- [Lam91] L. Lamport. The temporal logic of actions. Technical Report 79, Digital Systems Research Center, 1991.

BIBLIOGRAPHY

- [Lam94] L. Lamport. The temporal logic of actions. To appear in ACM TOPLAS, July? 1994.
- [LGdR79] S. Lee, S. Gerhart, and W.-P. de Roever. The evolution of list-copying algorithms and the need for structured program verification. In *Proc. of 6th POPL*, 1979.
- [MC81] J. Misra and K.M. Chandy. Proofs of networks of processes. *IEEE Transactions on Software Engineering*, 7(4):417–426, July 1981.
- [MP89] Z. Manna and A. Pnueli. An exercise in the verification of multi-process programs. Technical report, Stanford University, 1989.
- [PJ91] P.K. Pandya and M. Joseph. P-a logic - a compositional proof system for distributed programs. *Distributed Computing*, 5:37–54, 1991.
- [Pnu85] A. Pnueli. In transition from global to modular temporal reasoning about programs. In *NATO ASI Series F 13: Logics and models of concurrent systems*, pages 123–144. Springer-Verlag, 1985.
- [PWT90] P.R.H. Place, W.G. Wood, and M. Tudball. Survey of formal specification techniques for reactive systems. Technical Report, 1990.
- [Sch91] H. Schepers. Terminology and Paradigms for Fault Tolerance. Computing Science Notes 91/08 of the Department of Mathematics and Computing Science Eindhoven University of Technology, 1991.
- [Sta84] E.W. Stark. *Foundations of a Theory of Specification for Distributed Systems*. PhD thesis, Massachusetts Inst. of Technology, 1984. Available as Report No. MIT/LCS/TR-342.
- [Sta85] E.W. Stark. A Proof Technique for Rely/Guarantee Properties. In *LNCS 206: Fifth Conference on Foundations of Software Technology and Theoretical Computer Science*, pages 369–391. Springer-Verlag, 1985.
- [Sta88] E.W. Stark. Proving entailment between conceptual state specifications. *Theoretical Computer Science*, 56:135–154, 1988.
- [Sti88] C. Stirling. A generalization of owicki-gries hoare logic for a concurrent while language. *Theoretical Computer Science*, 58:347–359, 1988.
- [Stø91] K. Stølen. A method for the development of totally correct shared-state parallel programs. In J.C.M. Baeten and J.F. Groote, editors, *LNCS 527: Proceedings of Concur '91*, pages 510–525. Springer-Verlag, 1991.
- [WD88] J.C.P. Woodcock and B. Dickinson. Using vdm with rely and guarantee-conditions. In R. Bloomfield, L. Marshall, and R. Jones, editors, *LNCS 328: Proceedings of VDM '88, The Way Ahead*, pages 434–458. Springer-Verlag, 1988.

- [Wri87] M. Wriedt. Allgemeine Topologie I. Technical report, Christian-Albrechts-Universität zu Kiel, 1987. Vorlesungsskript ausgearbeitet von M. Jäger.
- [XCC94] Q. Xu, A. Cau, and P. Collette. On unifying assumption–commitment style proof rules for concurrency. In B. Jonsson and J. Parrow, editors, *LNCS 836: CONCUR'94: Concurrency Theory*, pages 267–282. Springer-Verlag, 1994.
- [ZCdR92] J. Zwiers, J. Coenen, and W.-P. de Roever. A note on compositional refinement. In C. B. Jones, R. C. Shaw, and T. Denvir, editors, *5th Refinement Workshop*, Workshops in Computing, pages 342–366, London, January 1992. BCS-FACS, Springer Verlag.
- [ZdBdR84] J. Zwiers, A. de Bruin, and W.-P. de Roever. A proof system for partial correctness of dynamic networks of processes. In *LNCS 164: Proc. of the Conference on Logics of Programs 1983*, pages 513–527. Springer-Verlag, 1984.
- [ZdRvEB84] J. Zwiers, W.-P. de Roever, and P. van Emde Boas. Compositionality and concurrent networks: soundness and completeness of a proof system. Technical report, University of Nijmegen, 1984. Technical Report 57.

Appendix A

Proofs of Dense Model Theorems

A.1 Proof of Theorem 1

Theorem 1 (Relationship between histories and infinite sequences)

Let $h \in \mathcal{H}/\simeq_h$ then $(sel_i)_{i \geq 0} \in SEQ/\simeq_s$ where sel_i is as follows:
if $nn(h) < \infty$:

$$\begin{aligned} sel_{2^*i+1} &= h(i) & 0 \leq i < nn(h) \\ sel_{2^*i} &= \lim_{i \leftarrow t_1} h(t_1) & 0 \leq i \leq nn(h) \\ sel_{2^*i+1} &= \lim_{k \leftarrow t_1} h(t_1) & k = nn(h) \wedge k \leq i \\ sel_{2^*i} &= \lim_{k \leftarrow t_1} h(t_1) & k = nn(h) \wedge k \leq i \end{aligned}$$

if $nn(h) = \infty$:

$$\begin{aligned} sel_{2^*i+1} &= h(i) & 0 \leq i \\ sel_{2^*i} &= \lim_{i \leftarrow t_1} h(t_1) & 0 \leq i \end{aligned}$$

Let $seq = (sel_i)_{i \geq 0} \in SEQ/\simeq_s$ then $h \in \mathcal{H}/\simeq_h$ where h is as follows:
if $ns(seq) < \infty$:

$$\begin{aligned} h(0) &= sel_0 \\ h(t) &= sel_{2^*t-1} & t \in \mathbb{N} \wedge 0 < t \leq ns(seq) \\ h(t) &= sel_{2^*t} & t \in \mathbb{N} \wedge t > ns(seq) \\ h(t) &= sel_{2^*i} & i < t < i + 1 \end{aligned}$$

if $ns(seq) = \infty$:

$$\begin{aligned} h(0) &= sel_0 \\ h(t) &= sel_{2^*t-1} & t \in \mathbb{N} \\ h(t) &= sel_{2^*i} & i < t < i + 1 \end{aligned}$$

Proof 15 Let $h \in \mathcal{H}/\simeq_h$ then h is of the form $h_1 \circ di(h_1)$ for some $h_1 \in \mathcal{H}$. According to Def. 10 h is then of the form that at discrete points the non-stutter steps and at all other points the stutter steps occur. The construction of seq above is such that at odd points the

non-stutter steps (or λ -steps if number of non-stutter steps is finite) and at even points the λ -step occur, i.e., a sequence from SEQ / \simeq_s .

Let $seq = (sel_i)_{i \geq 0} \in SEQ / \simeq_s$ then, according to Def. 11, seq is such that at odd points non-stutter steps occur (or λ -steps if the number of non-stutter steps is finite) and at the even points λ steps. The construction of h above is such that at discrete points greater than zero the non-stutter steps occur (or λ steps if the number of non-stutter steps is finite) and at all other points the λ steps, i.e. a history from \mathcal{H} / \simeq_h .

A.2 Proof of Lemma 1

Lemma 1

Given machine $M = (B, I, T)$ then

$Comp(M)$ is a safety set.

Proof 16 One has to prove that $Comp(M)$ is **closed**, i.e., $\mathcal{H} \setminus Comp(M)$ is an open set, i.e., $\mathcal{H} \setminus Comp(M) \in \tau_d$ (τ_d is the topological space defined in Def. 14).

$$\begin{aligned}
 & \mathcal{H} \setminus Comp(M) \in \tau_d \\
 = & \quad \% \text{ Def. 14 } \tau_d \\
 & \forall h : \exists \varepsilon > 0 : \forall h_1 : \\
 & \quad (h \in \mathcal{H} \setminus Comp(M) \wedge d(h, h_1) < \varepsilon) \rightarrow h_1 \in \mathcal{H} \setminus Comp(M) \\
 = & \quad \% \text{ Contraposition} \\
 & \forall h : \exists \varepsilon > 0 : \forall h_1 : h_1 \in Comp(M) \wedge d(h, h_1) < \varepsilon \rightarrow h \in Comp(M) \\
 = & \quad \% \text{ Def. 14 } d(h, h_1) \\
 & \forall h : \forall t : \forall h_1 : (h \downarrow_t = h_1 \downarrow_t \wedge h_1 \in Comp(M)) \rightarrow h \in Comp(M) \\
 \leftarrow & \quad \% \text{ Pred. Calc.} \\
 & \forall h : \forall h_1 : \forall t : (h \downarrow_t = h_1 \downarrow_t \wedge h_1 \in Comp(M)) \rightarrow h \in Comp(M) \\
 \leftarrow & \quad \% \text{ Pred. Calc.} \\
 & \forall h : \forall h_1 : (h = h_1 \wedge h_1 \in Comp(M)) \rightarrow h \in Comp(M) \\
 \leftarrow & \quad \% \text{ Pred. Calc.} \\
 & \text{true}
 \end{aligned}$$

A.3 Proof of Theorem 2

Theorem 2

Let $rexp$ be a rigid expression, exp be an expression, $evexp$ an event expression and p a temporal formula then

- a $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models rexp = (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models rexp)$
- b $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models exp = (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models exp)$
- c $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models evexp = (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models evexp)$
- d $\forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models p \text{ iff } (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models p)$

A.3 Proof of Theorem 2

Proof 17

$$a \quad \forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{rexp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp})$$

The proof proceeds by induction on the structure of rexp

- $\text{rexp} = \mu$:

$$\begin{aligned} & (h_0, t) \models \mu \\ = & \quad \% \quad h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mu \end{aligned}$$

- $\text{rexp} = n$:

$$\begin{aligned} & (h_0, t) \models n \\ = & \quad \% \quad \text{Def. 20} \\ & \theta_{h_0}(0)(n) \\ = & \quad \% \quad h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0), \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\ & \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(n) \\ = & \quad \% \quad \text{Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models n \end{aligned}$$

- $\text{exp} = n'$:

$$\begin{aligned} & (h_0, t) \models n' \\ = & \quad \% \quad \text{Def. 20} \\ & \theta_{h_0}(0)(n) \\ = & \quad \% \quad h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0), \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\ & \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(n) \\ = & \quad \% \quad \text{Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models n' \end{aligned}$$

- $\text{exp} = \text{`}n$:

$$\begin{aligned} & (h_0, t) \models \text{`}n \\ = & \quad \% \quad \text{Def. 20} \\ & \theta_{h_0}(0)(n) \\ = & \quad \% \quad h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0), \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\ & \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(n) \\ = & \quad \% \quad \text{Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{`}n \end{aligned}$$

- $\text{rexp} = \text{rexp}_1 + \text{rexp}_2$:

$$\begin{aligned} & (h_0, t) \models \text{rexp}_1 + \text{rexp}_2 \\ = & \quad \% \quad \text{Def. 20} \\ & (h_0, t) \models \text{rexp}_1 + (h_0, t) \models \text{rexp}_2 \\ = & \quad \% \quad \text{Induction} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp}_1 + (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp}_2 \\ = & \quad \% \quad \text{Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp}_1 + \text{rexp}_2 \end{aligned}$$

$$b \quad \forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{exp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{exp})$$

The proof proceeds by induction on structure of exp :

- $\text{exp} = \text{rexp}$:

$$\begin{aligned} & (h_0, t) \models \text{rexp} \\ = & \quad \% \text{ Theorem 2a} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{rexp} \end{aligned}$$

- $\text{exp} = \mathbf{v}$:

$$\begin{aligned} & (h_0, t) \models \mathbf{v} \\ = & \quad \% \text{ Def. 20} \\ & \theta_{h_0}(t)(\mathbf{v}) \\ = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\ & \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t))(\mathbf{v}) \\ = & \quad \% \text{ Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{v} \end{aligned}$$

- $\text{exp} = \mathbf{v}'$:

$$\begin{aligned} & (h_0, t) \models \mathbf{v}' \\ = & \quad \% \text{ Def. 20} \\ & \lim_{t \leftarrow t_1} \theta_{h_0}(t_1)(\mathbf{v}') \\ = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\ & \lim_{t \leftarrow t_1} \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(\mathbf{v}') \\ = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\ & \lim_{\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t) \leftarrow t_2} \theta_{h_1}(t_2)(\mathbf{v}') \\ = & \quad \% \text{ Def. 20} \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{v}' \end{aligned}$$

- $\text{exp} = \mathbf{v}$:

$$t = 0$$

$$\begin{aligned} & (h_0, 0) \models \mathbf{v} \\ = & \quad \% \text{ Def. 20} \\ & \theta_{h_0}(0)(\mathbf{v}) \\ = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\ & \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(\mathbf{v}) \\ = & \quad \% \text{ Def. 20, } \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\ & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0)) \models \mathbf{v} \end{aligned}$$

A.3 Proof of Theorem 2

$t > 0$

$$\begin{aligned}
& (h_0, t) \models \mathbf{v} \\
= & \quad \% \text{ Def. 20} \\
& \lim_{t_1 \rightarrow t} \theta_{h_0}(t_1)(\mathbf{v}) \\
= & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \lim_{t_1 \rightarrow t} \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(\mathbf{v}) \\
= & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
& \lim_{t_2 \rightarrow \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)} \theta_{h_1}(t_2)(\mathbf{v}) \\
= & \quad \% \text{ Def. 20} \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{v}
\end{aligned}$$

• $exp = x$:

$$\begin{aligned}
& (h_0, t) \models x \\
= & \quad \% \text{ Def. 20} \\
& \theta_{h_0}(t)(x) \\
= & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t))(x) \\
= & \quad \% \text{ Def. 20} \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models x
\end{aligned}$$

• $exp = x'$:

$$\begin{aligned}
& (h_0, t) \models x' \\
= & \quad \% \text{ Def. 20} \\
& \lim_{t \leftarrow t_1} \theta_{h_0}(t_1)(x) \\
= & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \lim_{t \leftarrow t_1} \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(x) \\
= & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
& \lim_{\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t) \leftarrow t_2} \theta_{h_1}(t_2)(x) \\
= & \quad \% \text{ Def. 20} \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models x'
\end{aligned}$$

• $exp = \mathbf{x}$:

$$\begin{aligned}
& (h_0, 0) \models \mathbf{x} \\
= & \quad \% \text{ Def. 20} \\
& \theta_{h_0}(0)(\mathbf{x}) \\
= & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(\mathbf{x}) \\
= & \quad \% \text{ Def. 20, } \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0)) \models \mathbf{x}
\end{aligned}$$

$t > 0$

$$\begin{aligned}
 & (h_0, t) \models \text{x} \\
 = & \quad \% \text{ Def. 20} \\
 & \lim_{t_1 \rightarrow t} \theta_{h_0}(t_1)(x) \\
 = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \lim_{t_1 \rightarrow t} \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(x) \\
 = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
 & \lim_{t_2 \rightarrow \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)} \theta_{h_1}(t_2)(x) \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{x}
 \end{aligned}$$

• $\text{exp} = \text{exp}_1 + \text{exp}_2$:

$$\begin{aligned}
 & (h_0, t) \models \text{exp}_1 + \text{exp}_2 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_0, t) \models \text{exp}_1 + (h_0, t) \models \text{exp}_2 \\
 = & \quad \% \text{ Induction} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{exp}_1 + (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{exp}_2 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{exp}_1 + \text{exp}_2
 \end{aligned}$$

$c \quad \forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models \text{evexp} = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \text{evexp})$

The proof proceeds by induction on structure of evexp :

• $\text{evexp} = \mathbf{a}?$:

$$\begin{aligned}
 & (h_0, t) \models \mathbf{a}? \\
 = & \quad \% \text{ Def. 20} \\
 & \mathbf{a}? \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{a}?
 \end{aligned}$$

• $\text{evexp} = \mathbf{a}!$:

$$\begin{aligned}
 & (h_0, t) \models \mathbf{a}! \\
 = & \quad \% \text{ Def. 20} \\
 & \mathbf{a}! \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{a}!
 \end{aligned}$$

A.3 Proof of Theorem 2

- $evexp = \mathbf{i}$:

$$\begin{aligned}
 & (h_0, t) \models \mathbf{i} \\
 = & \quad \% \text{ Def. 20} \\
 & \mathbf{i} \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{i}
 \end{aligned}$$

- $evexp = \mathbf{e}$:

$$\begin{aligned}
 & (h_0, t) \models \mathbf{e} \\
 = & \quad \% \text{ Def. 20} \\
 & \mathbf{e} \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{e}
 \end{aligned}$$

- $evexp = \lambda$:

$$\begin{aligned}
 & (h_0, t) \models \lambda \\
 = & \quad \% \text{ Def. 20} \\
 & \lambda \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \lambda
 \end{aligned}$$

- $evexp = \epsilon$:

$$\begin{aligned}
 & (h_0, t) \models \epsilon \\
 = & \quad \% \text{ Def. 20} \\
 & \psi_{h_0}(t)(\epsilon) \\
 = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \psi_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t))(\epsilon) \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \epsilon
 \end{aligned}$$

- $evexp = \epsilon'$:

$$\begin{aligned}
 & (h_0, t) \models \epsilon' \\
 = & \quad \% \text{ Def. 20} \\
 & \lim_{t \leftarrow t_1} \psi_{h_0}(t_1)(\epsilon) \\
 = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \lim_{t \leftarrow t_1} \psi_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(\epsilon) \\
 = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
 & \lim_{\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t) \leftarrow t_2} \psi_{h_1}(t_2)(\epsilon) \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \epsilon'
 \end{aligned}$$

- $evexp = \epsilon$:

$$t = 0$$

$$\begin{aligned}
 & (h_0, 0) \models \epsilon \\
 = & \quad \% \text{ Def. 20} \\
 & \psi_{h_0}(0)(\epsilon) \\
 = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \psi_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(0))(\epsilon) \\
 = & \quad \% \text{ Def. 20, } \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0) = 0 \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(0)) \models \epsilon
 \end{aligned}$$

$$t > 0$$

$$\begin{aligned}
 & (h_0, t) \models \epsilon \\
 = & \quad \% \text{ Def. 20} \\
 & \lim_{t_1 \rightarrow t} \psi_{h_0}(t_1)(\epsilon) \\
 = & \quad \% h_0 = h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \lim_{t_1 \rightarrow t} \theta_{h_1}(\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1))(\epsilon) \\
 = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
 & \lim_{t_2 \rightarrow \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)} \psi_{h_1}(t_2)(\epsilon) \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \epsilon
 \end{aligned}$$

$$d \quad \forall t, h_0, h_1 : h_0 \simeq_{\theta_h} h_1 \rightarrow ((h_0, t) \models p \text{ iff } (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p)$$

The proof proceeds by induction on structure of p :

- $p = \mathbf{true}$:

$$\begin{aligned}
 & (h_0, t) \models \mathbf{true} \\
 = & \quad \% \text{ Def. 20} \\
 & \mathbf{true} \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \mathbf{true}
 \end{aligned}$$

- $p = (exp_1 = exp_2)$:

$$\begin{aligned}
 & (h_0, t) \models exp_1 = exp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_0, t) \models exp_1 = (h_0, t) \models exp_2 \\
 = & \quad \% \text{ Theorem 2b} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models exp_1 = (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models exp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models exp_1 = exp_2
 \end{aligned}$$

A.3 Proof of Theorem 2

- $p = (exp_1 < exp_2)$:

$$\begin{aligned}
& (h_0, t) \models exp_1 < exp_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_0, t) \models exp_1 <?(h_0, t) \models exp_2 \\
= & \quad \% \text{ Theorem 2b} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models exp_1 < (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models exp_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models exp_1 < exp_2
\end{aligned}$$

- $p = (evexp_1 = evexp_2)$:

$$\begin{aligned}
& (h_0, t) \models evexp_1 = evexp_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_0, t) \models evexp_1 = (h_0, t) \models evexp_2 \\
= & \quad \% \text{ Theorem 2c} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models evexp_1 = (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models evexp_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models evexp_1 = evexp_2
\end{aligned}$$

- $p = \neg p_0$:

$$\begin{aligned}
& (h_0, t) \models \neg p_0 \\
= & \quad \% \text{ Def. 20} \\
& \text{not } (h_0, t) \models p_0 \\
= & \quad \% \text{ Induction} \\
& \text{not } (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models p_0 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models \neg p_0
\end{aligned}$$

- $p = p_1 \vee p_2$:

$$\begin{aligned}
& (h_0, t) \models p_1 \vee p_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_0, t) \models p_1 \text{ or } (h_0, t) \models p_2 \\
= & \quad \% \text{ Induction} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models p_1 \text{ or } (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models p_2 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, di(h_1) \circ di^{-1}(h_0)(t)) \models p_1 \vee p_2
\end{aligned}$$

- $p = p_1 \widehat{\mathcal{U}} p_2$:

$$\begin{aligned}
 & (h_0, t) \models p_1 \widehat{\mathcal{U}} p_2 \\
 = & \quad \% \text{ Def. 20} \\
 & \exists t_0 > t : (h_0, t_0) \models p_2 \text{ and } \forall t_1 \in (t, t_0) : (h_0, t_1) \models p_1 \\
 = & \quad \% \text{ Induction} \\
 & \exists t_0 > t : (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_0)) \models p_2 \\
 & \quad \text{and } \forall t_1 \in (t, t_0) : (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1)) \models p_1 \\
 = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_0), t_3 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
 & \exists t_2 > \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t) : (h_1, t_2) \models p_2 \\
 & \quad \text{and } \forall t_3 \in (\text{di}(h_1) \circ \text{di}^{-1}(h_0)(t), t_2) : (h_1, t_3) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p_1 \widehat{\mathcal{U}} p_2
 \end{aligned}$$

- $p = p_1 \widehat{\mathcal{S}} p_2$:

$$\begin{aligned}
 & (h_0, t) \models p_1 \widehat{\mathcal{S}} p_2 \\
 = & \quad \% \text{ Def. 20} \\
 & \exists t_0 < t : (h_0, t_0) \models p_2 \text{ and } \forall t_1 \in (t_0, t) : (h_0, t_1) \models p_1 \\
 = & \quad \% \text{ Induction} \\
 & \exists t_0 < t : (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_0)) \models p_2 \\
 & \quad \text{and } \forall t_1 \in (t_0, t) : (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1)) \models p_1 \\
 = & \quad \% t_2 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_0), t_3 = \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t_1) \\
 & \exists t_2 < \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t) : (h_1, t_2) \models p_2 \\
 & \quad \text{and } \forall t_3 \in (t_2, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) : (h_1, t_3) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p_1 \widehat{\mathcal{S}} p_2
 \end{aligned}$$

- $p = \exists x.p_0$:

$$\begin{aligned}
 & (h_0, t) \models \exists x.p_0 \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_2 : h_2 \text{ x-variant of } h_0 \wedge (h_2, t) \models p_0 \\
 = & \quad \% h_1 = h_0 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \exists h_2 : h_2 \text{ x-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \wedge (h_2, t) \models p_0 \\
 = & \quad \% h_3 = h_1 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1), \\
 & \quad h_2 \text{ x-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
 & \quad \Leftrightarrow h_2 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1) \text{ x-variant of } h_1 \\
 & \exists h_3 : h_3 \text{ x-variant of } h_1 \wedge (h_3, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p_0 \\
 = & \quad \% \text{ Def. 20} \\
 & (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \exists x.p_0
 \end{aligned}$$

A.4 Proof of Lemma 2

- $p = \exists \epsilon.p_0$:

$$\begin{aligned}
& (h_0, t) \models \exists \epsilon.p_0 \\
= & \quad \% \text{ Def. 20} \\
& \exists h_2 : h_2 \ \epsilon\text{-variant of } h_0 \wedge (h_2, t) \models p_0 \\
= & \quad \% h_1 = h_0 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \exists h_2 : h_2 \ \epsilon\text{-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \wedge (h_2, t) \models p_0 \\
= & \quad \% h_3 = h_1 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1), \\
& \quad h_2 \ \epsilon\text{-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \quad \leftrightarrow h_2 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1) \ \epsilon\text{-variant of } h_1 \\
& \exists h_3 : h_3 \ \epsilon\text{-variant of } h_1 \wedge (h_3, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p_0 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \exists \epsilon.p_0
\end{aligned}$$

- $p = \exists n.p_0$:

$$\begin{aligned}
& (h_0, t) \models \exists n.p_0 \\
= & \quad \% \text{ Def. 20} \\
& \exists h_2 : h_2 \ n\text{-variant of } h_0 \wedge (h_2, t) \models p_0 \\
= & \quad \% h_1 = h_0 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \exists h_2 : h_2 \ n\text{-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \wedge (h_2, t) \models p_0 \\
= & \quad \% h_3 = h_1 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1), \\
& \quad h_2 \ n\text{-variant of } h_1 \circ \text{di}(h_1) \circ \text{di}^{-1}(h_0) \\
& \quad \leftrightarrow h_2 \circ \text{di}(h_0) \circ \text{di}^{-1}(h_1) \ \epsilon\text{-variant of } h_1 \\
& \exists h_3 : h_3 \ n\text{-variant of } h_1 \wedge (h_3, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models p_0 \\
= & \quad \% \text{ Def. 20} \\
& (h_1, \text{di}(h_1) \circ \text{di}^{-1}(h_0)(t)) \models \exists n.p_0
\end{aligned}$$

A.4 Proof of Lemma 2

Lemma 2

Let exp_0 be an expression, exp be a state expression, $w \in \mathfrak{W} \cup \mathfrak{X}$, $rexp$ be a state rigid expression, $n \in \mathfrak{N}$, $evexp_0$ an event expression, $evexp$ a state event expression, $\epsilon \in \mathfrak{E}$, and p a temporal formula. Then the following holds:

- $(h, t) \models exp_0[exp/w] = ((h : w \rightsquigarrow exp), t) \models exp_0$
- $(h, t) \models exp_0[rexp/n] = ((h : n \rightsquigarrow rexp), t) \models exp_0$
- $(h, t) \models evexp_0[evexp/\epsilon] = ((h : \epsilon \rightsquigarrow evexp), t) \models evexp_0$
- $(h, t) \models p[exp/w]$ iff $((h : w \rightsquigarrow exp), t) \models p$
- $(h, t) \models p[rexp/n]$ iff $((h : n \rightsquigarrow rexp), t) \models p$
- $(h, t) \models p[evexp/\epsilon]$ iff $((h : \epsilon \rightsquigarrow evexp), t) \models p$

Proof 18

- $(h, t) \models exp_0[exp/w] = ((h : w \rightsquigarrow exp), t) \models exp_0$

Proof by induction on the structure of exp_0 :

- $exp_0 = rexp$:

$$\begin{aligned}
 & (h, t) \models rexp[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models rexp \\
 = & \quad \% \text{ Def. 26, } w \notin varrexp \\
 & ((h : w \rightsquigarrow exp), t) \models rexp
 \end{aligned}$$

- $exp_0 = \mathbf{v}$:

$$\mathbf{v} \equiv w$$

$$\begin{aligned}
 & (h, t) \models \mathbf{v}[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models exp \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \mathbf{v}
 \end{aligned}$$

$$\mathbf{v} \not\equiv w$$

$$\begin{aligned}
 & (h, t) \models \mathbf{v}[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models \mathbf{v} \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \mathbf{v}
 \end{aligned}$$

- $exp_0 = \mathbf{v}'$:

$$\mathbf{v} \equiv w$$

$$\begin{aligned}
 & (h, t) \models \mathbf{v}'[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models exp' \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \mathbf{v}'
 \end{aligned}$$

$$\mathbf{v} \not\equiv w$$

$$\begin{aligned}
 & (h, t) \models \mathbf{v}'[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models \mathbf{v}' \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \mathbf{v}'
 \end{aligned}$$

- $exp_0 = \backslash \mathbf{v}$:

$$\mathbf{v} \equiv w$$

$$\begin{aligned}
 & (h, t) \models \backslash \mathbf{v}[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models \backslash exp \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \backslash \mathbf{v}
 \end{aligned}$$

A.4 Proof of Lemma 2

$v \neq w$

$$\begin{aligned}
 & (h, t) \models v[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models v \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models v
 \end{aligned}$$

• $exp_0 = x$:

$x \equiv w$

$$\begin{aligned}
 & (h, t) \models x[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models exp \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models x
 \end{aligned}$$

$x \neq w$

$$\begin{aligned}
 & (h, t) \models x[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models x \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models x
 \end{aligned}$$

• $exp_0 = x'$:

$x \equiv w$

$$\begin{aligned}
 & (h, t) \models x'[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models exp' \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models x'
 \end{aligned}$$

$x \neq w$

$$\begin{aligned}
 & (h, t) \models x'[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models x' \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models x'
 \end{aligned}$$

• $exp_0 = \backslash x$:

$x \equiv w$

$$\begin{aligned}
 & (h, t) \models \backslash x[exp/w] \\
 = & \quad \% \text{ Def. 22} \\
 & (h, t) \models \backslash exp \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : w \rightsquigarrow exp), t) \models \backslash x
 \end{aligned}$$

$$x \neq w$$

$$\begin{aligned} & (h, t) \models x [exp/w] \\ = & \quad \% \text{ Def. 22} \\ & (h, t) \models x \\ = & \quad \% \text{ Def. 26} \\ & ((h : w \rightsquigarrow exp), t) \models x \end{aligned}$$

$$\bullet \ exp_0 \equiv exp_1 + exp_2$$

$$\begin{aligned} & (h, t) \models (exp_1 + exp_2) [exp/w] \\ = & \quad \% \text{ Def. 22} \\ & (h, t) \models exp_1 [exp/w] + exp_2 [exp/w] \\ = & \quad \% \text{ Def. 20} \\ & (h, t) \models exp_1 [exp/w] + (h, t) \models exp_2 [exp/w] \\ = & \quad \% \text{ Induction} \\ & ((h : w \rightsquigarrow exp), t) \models exp_1 + ((h : w \rightsquigarrow exp), t) \models exp_2 \\ = & \quad \% \text{ Def. 20} \\ & ((h : w \rightsquigarrow exp), t) \models exp_1 + exp_2 \end{aligned}$$

$$b \ (h, t) \models exp_0 [rexp/n] = ((h : n \rightsquigarrow rexp), t) \models exp_0$$

Proof by induction on structure of exp_0

$$\bullet \ exp_0 = \mu:$$

$$\begin{aligned} & (h, t) \models \mu [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models \mu \\ = & \quad \% \text{ Def. 26 and 20} \\ & ((h : n \rightsquigarrow rexp), t) \models \mu \end{aligned}$$

$$\bullet \ exp_0 = n_0:$$

$$n_0 \equiv n$$

$$\begin{aligned} & (h, t) \models n_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models rexp \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models n_0 \end{aligned}$$

$$n_0 \neq n$$

$$\begin{aligned} & (h, t) \models n_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models n_0 \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models n_0 \end{aligned}$$

A.4 Proof of Lemma 2

- $exp_0 = n'_0$:

$$n_0 \equiv n$$

$$\begin{aligned} & (h, t) \models n'_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models rexp' \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models n'_0 \end{aligned}$$

$$n_0 \not\equiv n$$

$$\begin{aligned} & (h, t) \models n'_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models n'_0 \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models n'_0 \end{aligned}$$

- $exp_0 = \backslash n_0$:

$$n_0 \equiv n$$

$$\begin{aligned} & (h, t) \models \backslash n_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models \backslash rexp \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models \backslash n_0 \end{aligned}$$

$$n_0 \not\equiv n$$

$$\begin{aligned} & (h, t) \models \backslash n_0 [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models \backslash n_0 \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models \backslash n_0 \end{aligned}$$

- $exp_0 = w$:

$$\begin{aligned} & (h, t) \models w [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models w \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models w \end{aligned}$$

- $exp_0 = w'$:

$$\begin{aligned} & (h, t) \models w' [rexp/n] \\ = & \quad \% \text{ Def. 23} \\ & (h, t) \models w' \\ = & \quad \% \text{ Def. 26} \\ & ((h : n \rightsquigarrow rexp), t) \models w' \end{aligned}$$

- $exp_0 = \backslash w$:

$$\begin{aligned}
 & (h, t) \models \backslash w [rexp/n] \\
 = & \quad \% \text{ Def. 23} \\
 & (h, t) \models \backslash w \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : n \rightsquigarrow rexp), t) \models \backslash w
 \end{aligned}$$

- $exp_0 \equiv exp_1 + exp_2$

$$\begin{aligned}
 & (h, t) \models (exp_1 + exp_2) [rexp/n] \\
 = & \quad \% \text{ Def. 23} \\
 & (h, t) \models exp_1 [rexp/n] + exp_2 [rexp/n] \\
 = & \quad \% \text{ Def. 20} \\
 & (h, t) \models exp_1 [rexp/n] + (h, t) \models exp_2 [rexp/n] \\
 = & \quad \% \text{ Induction} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 + ((h : n \rightsquigarrow rexp), t) \models exp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 + exp_2
 \end{aligned}$$

$$c \quad (h, t) \models evexp_0 [evexp/\epsilon] = ((h : \epsilon \rightsquigarrow evexp), t) \models evexp_0$$

Proof by induction on structure of evexp₀:

- $evexp_0 = \lambda$:

$$\begin{aligned}
 & (h, t) \models \lambda [evexp/\epsilon] \\
 = & \quad \% \text{ Def. 24} \\
 & (h, t) \models \lambda \\
 = & \quad \% \text{ Def. 26 and 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models \lambda
 \end{aligned}$$

- $evexp_0 = \mathbf{a}?$:

$$\begin{aligned}
 & (h, t) \models \mathbf{a}? [evexp/\epsilon] \\
 = & \quad \% \text{ Def. 24} \\
 & (h, t) \models \mathbf{a}? \\
 = & \quad \% \text{ Def. 26 and 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models \mathbf{a}?
 \end{aligned}$$

- $evexp_0 = \mathbf{a}!$:

$$\begin{aligned}
 & (h, t) \models \mathbf{a}! [evexp/\epsilon] \\
 = & \quad \% \text{ Def. 24} \\
 & (h, t) \models \mathbf{a}! \\
 = & \quad \% \text{ Def. 26 and 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models \mathbf{a}!
 \end{aligned}$$

A.4 Proof of Lemma 2

- $evexp_0 = \mathbf{i}$:

$$\begin{aligned}
& (h, t) \models \mathbf{i}[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models \mathbf{i} \\
= & \quad \% \text{ Def. 26 and 20} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \mathbf{i}
\end{aligned}$$

- $evexp_0 = \mathbf{e}$:

$$\begin{aligned}
& (h, t) \models \mathbf{e}[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models \mathbf{e} \\
= & \quad \% \text{ Def. 26 and 20} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \mathbf{e}
\end{aligned}$$

- $evexp_0 = \epsilon_0$:

$$\epsilon_0 \equiv \epsilon$$

$$\begin{aligned}
& (h, t) \models \epsilon_0[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models evexp \\
= & \quad \% \text{ Def. 26} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon_0
\end{aligned}$$

$$\epsilon_0 \not\equiv \epsilon$$

$$\begin{aligned}
& (h, t) \models \epsilon_0[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models \epsilon_0 \\
= & \quad \% \text{ Def. 26} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon_0
\end{aligned}$$

- $evexp_0 = \epsilon'_0$:

$$\epsilon_0 \equiv \epsilon$$

$$\begin{aligned}
& (h, t) \models \epsilon'_0[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models evexp' \\
= & \quad \% \text{ Def. 26} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon'_0
\end{aligned}$$

$$\epsilon_0 \not\equiv \epsilon$$

$$\begin{aligned}
& (h, t) \models \epsilon'_0[evexp/\epsilon] \\
= & \quad \% \text{ Def. 24} \\
& (h, t) \models \epsilon'_0 \\
= & \quad \% \text{ Def. 26} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon'_0
\end{aligned}$$

- $evexp_0 = \epsilon_0$:

$$\epsilon_0 \equiv \epsilon$$

$$\begin{aligned} & (h, t) \models \epsilon_0 [evexp/\epsilon] \\ = & \quad \% \text{ Def. 24} \\ & (h, t) \models evexp \\ = & \quad \% \text{ Def. 26} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon_0 \end{aligned}$$

$$\epsilon_0 \not\equiv \epsilon$$

$$\begin{aligned} & (h, t) \models \epsilon_0 [evexp/\epsilon] \\ = & \quad \% \text{ Def. 24} \\ & (h, t) \models \epsilon_0 \\ = & \quad \% \text{ Def. 26} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models \epsilon_0 \end{aligned}$$

$$d \quad (h, t) \models p [exp/w] \text{ iff } ((h : w \rightsquigarrow exp), t) \models p$$

Proof by induction on structure of p :

- $p = \mathbf{true}$:

$$\begin{aligned} & (h, t) \models \mathbf{true} [exp/w] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models \mathbf{true} \\ = & \quad \% \text{ Def. 26} \\ & ((h : w \rightsquigarrow exp), t) \models \mathbf{true} \end{aligned}$$

- $p = (exp_1 = exp_2)$:

$$\begin{aligned} & (h, t) \models (exp_1 = exp_2) [exp/w] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models exp_1 [exp/w] = exp_2 [exp/w] \\ = & \quad \% \text{ Def. 20} \\ & (h, t) \models exp_1 [exp/w] = (h, t) \models exp_2 [exp/w] \\ = & \quad \% \text{ Lemma 2a} \\ & ((h : w \rightsquigarrow exp), t) \models exp_1 = ((h : w \rightsquigarrow exp), t) \models exp_2 \\ = & \quad \% \text{ Def. 20} \\ & ((h : w \rightsquigarrow exp), t) \models exp_1 = exp_2 \end{aligned}$$

A.4 Proof of Lemma 2

- $p = (exp_1 < exp_2)$:

$$\begin{aligned}
& (h, t) \models (exp_1 < exp_2)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models exp_1[exp/w] < exp_2[exp/w] \\
= & \quad \% \text{ Def. 20} \\
& (h, t) \models exp_1[exp/w] < (h, t) \models exp_2[exp/w] \\
= & \quad \% \text{ Lemma refsu.lea} \\
& ((h : w \rightsquigarrow exp), t) \models exp_1 < ((h : w \rightsquigarrow exp), t) \models exp_2 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models exp_1 < exp_2
\end{aligned}$$

- $p = (evexp_1 = evexp_2)$:

$$\begin{aligned}
& (h, t) \models (evexp_1 = evexp_2)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models evexp_1 = evexp_2 \\
= & \quad \% \text{ Def. 20} \\
& (h, t) \models evexp_1 = (h, t) \models evexp_2 \\
= & \quad \% \text{ Def. 26} \\
& ((h : w \rightsquigarrow exp), t) \models evexp_1 = ((h : w \rightsquigarrow exp), t) \models evexp_2 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models evexp_1 = evexp_2
\end{aligned}$$

- $p = \neg p_1$:

$$\begin{aligned}
& (h, t) \models (\neg p_1)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \neg(p_1[exp/w]) \\
= & \quad \% \text{ Def. 20} \\
& \text{not } (h, t) \models p_1[exp/w] \\
= & \quad \% \text{ Induction} \\
& \text{not } ((h : w \rightsquigarrow exp), t) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models \neg p_1
\end{aligned}$$

- $p = p_1 \vee p_2$:

$$\begin{aligned}
& (h, t) \models (p_1 \vee p_2)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models (p_1[exp/w] \vee p_2[exp/w]) \\
= & \quad \% \text{ Def. 20} \\
& (h, t) \models p_1[exp/w] \text{ or } (h, t) \models p_2[exp/w] \\
= & \quad \% \text{ Induction} \\
& ((h : w \rightsquigarrow exp), t) \models p_1 \text{ or } ((h : w \rightsquigarrow exp), t) \models p_2 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models p_1 \vee p_2
\end{aligned}$$

- $p = p_1 \widehat{\mathcal{U}} p_2$:

$$\begin{aligned}
 & (h, t) \models (p_1 \widehat{\mathcal{U}} p_2)[exp/w] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models (p_1[exp/w] \widehat{\mathcal{U}} p_2[exp/w]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists t_0 > t : (h, t_0) \models p_2[exp/w] \text{ and } \forall t_1 \in (t, t_0) : (h, t_1) \models p_1[exp/w] \\
 = & \quad \% \text{ Induction} \\
 & \exists t_0 > t : ((h : w \rightsquigarrow exp), t_0) \models p_2 \\
 & \text{and } \forall t_1 \in (t, t_0) : ((h : w \rightsquigarrow exp), t_1) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : w \rightsquigarrow exp), t) \models p_1 \widehat{\mathcal{U}} p_2
 \end{aligned}$$

- $p = p_1 \widehat{\mathcal{S}} p_2$:

$$\begin{aligned}
 & (h, t) \models (p_1 \widehat{\mathcal{S}} p_2)[exp/w] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models (p_1[exp/w] \widehat{\mathcal{S}} p_2[exp/w]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists t_0 < t : (h, t_0) \models p_2[exp/w] \text{ and } \forall t_1 \in (t_0, t) : (h, t_1) \models p_1[exp/w] \\
 = & \quad \% \text{ Induction} \\
 & \exists t_0 < t : ((h : w \rightsquigarrow exp), t_0) \models p_2 \\
 & \text{and } \forall t_1 \in (t_0, t) : ((h : w \rightsquigarrow exp), t_1) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : w \rightsquigarrow exp), t) \models p_1 \widehat{\mathcal{S}} p_2
 \end{aligned}$$

- $p = \exists x.p_1$ for $x \notin \text{var}(exp) \cup \{w\}$:

$$\begin{aligned}
 & (h, t) \models (\exists x.p_1)[exp/w] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \exists x.(p_1[exp/w]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \text{ x-variant of } h \text{ and } (h_1, t) \models p_1[exp/w] \\
 = & \quad \% \text{ Induction} \\
 & \exists h_1 : h_1 \text{ x-variant of } h \text{ and } ((h_1 : w \rightsquigarrow exp), t) \models p_1 \\
 = & \quad \% h_2 = (h_1 : w \rightsquigarrow exp), h_3 = (h : w \rightsquigarrow exp) \\
 & \quad h_1 \text{ x-variant of } h \text{ iff } h_2 \text{ x-variant of } h_3 \\
 & \exists h_2 : h_2 \text{ x-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : w \rightsquigarrow exp), t) \models \exists x.p_1
 \end{aligned}$$

A.4 Proof of Lemma 2

- $p = \exists\epsilon.p_1$:

$$\begin{aligned}
& (h, t) \models (\exists\epsilon.p_1)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \exists\epsilon.(p_1[exp/w]) \\
= & \quad \% \text{ Def. 20} \\
& \exists h_1 : h_1 \text{ } \epsilon\text{-variant of } h \text{ and } (h_1, t) \models p_1[exp/w] \\
= & \quad \% \text{ Induction} \\
& \exists h_1 : h_1 \text{ } \epsilon\text{-variant of } h \text{ and } ((h_1 : w \rightsquigarrow exp), t) \models p_1 \\
= & \quad \% h_2 = (h_1 : w \rightsquigarrow exp), h_3 = (h : w \rightsquigarrow exp) \\
& \quad h_1 \text{ } \epsilon\text{-variant of } h \text{ iff } h_2 \text{ } \epsilon\text{-variant of } h_3 \\
& \exists h_2 : h_2 \text{ } \epsilon\text{-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models \exists\epsilon.p_1
\end{aligned}$$

- $p = \exists n.p_1$:

$$\begin{aligned}
& (h, t) \models (\exists n.p_1)[exp/w] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \exists n.(p_1[exp/w]) \\
= & \quad \% \text{ Def. 20} \\
& \exists h_1 : h_1 \text{ } n\text{-variant of } h \text{ and } (h_1, t) \models p_1[exp/w] \\
= & \quad \% \text{ Induction} \\
& \exists h_1 : h_1 \text{ } n\text{-variant of } h \text{ and } ((h_1 : w \rightsquigarrow exp), t) \models p_1 \\
= & \quad \% h_2 = (h_1 : w \rightsquigarrow exp), h_3 = (h : w \rightsquigarrow exp) \\
& \quad h_1 \text{ } n\text{-variant of } h \text{ iff } h_2 \text{ } n\text{-variant of } h_3 \\
& \exists h_2 : h_2 \text{ } n\text{-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : w \rightsquigarrow exp), t) \models \exists n.p_1
\end{aligned}$$

$$e \quad (h, t) \models p[rexp/n] \text{ iff } ((h : n \rightsquigarrow rexp), t) \models p$$

Proof by induction on structure of p :

- $p = \mathbf{true}$:

$$\begin{aligned}
& (h, t) \models \mathbf{true}[rexp/n] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \mathbf{true} \\
= & \quad \% \text{ Def. 26} \\
& ((h : n \rightsquigarrow rexp), t) \models \mathbf{true}
\end{aligned}$$

- $p = (exp_1 = exp_2)$:

$$\begin{aligned}
 & (h, t) \models (exp_1 = exp_2) [rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models exp_1 [rexp/n] = exp_2 [rexp/n] \\
 = & \quad \% \text{ Def. 20} \\
 & (h, t) \models exp_1 [rexp/n] = (h, t) \models exp_2 [rexp/n] \\
 = & \quad \% \text{ Lemma 2b} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 = ((h : n \rightsquigarrow rexp), t) \models exp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 = exp_2
 \end{aligned}$$

- $p = (exp_1 < exp_2)$:

$$\begin{aligned}
 & (h, t) \models (exp_1 < exp_2) [rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models exp_1 [rexp/n] < exp_2 [rexp/n] \\
 = & \quad \% \text{ Def. 20} \\
 & (h, t) \models exp_1 [rexp/n] < (h, t) \models exp_2 [rexp/n] \\
 = & \quad \% \text{ Lemma 2b} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 < ((h : n \rightsquigarrow rexp), t) \models exp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models exp_1 < exp_2
 \end{aligned}$$

- $p = (evexp_1 = evexp_2)$:

$$\begin{aligned}
 & (h, t) \models (evexp_1 = evexp_2) [rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models evexp_1 = evexp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & (h, t) \models evexp_1 = (h, t) \models evexp_2 \\
 = & \quad \% \text{ Def. 26} \\
 & ((h : n \rightsquigarrow rexp), t) \models evexp_1 = ((h : n \rightsquigarrow rexp), t) \models evexp_2 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models evexp_1 = evexp_2
 \end{aligned}$$

- $p = \neg p_1$:

$$\begin{aligned}
 & (h, t) \models (\neg p_1) [rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \neg(p_1 [rexp/n]) \\
 = & \quad \% \text{ Def. 20} \\
 & \text{not } (h, t) \models p_1 [rexp/n] \\
 = & \quad \% \text{ Induction} \\
 & \text{not } ((h : n \rightsquigarrow rexp), t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models \neg p_1
 \end{aligned}$$

A.4 Proof of Lemma 2

- $p = p_1 \vee p_2$:

$$\begin{aligned}
& (h, t) \models (p_1 \vee p_2)[rexp/n] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models (p_1[rexp/n] \vee p_2[rexp/n]) \\
= & \quad \% \text{ Def. 20} \\
& (h, t) \models p_1[rexp/n] \text{ or } (h, t) \models p_2[rexp/n] \\
= & \quad \% \text{ Induction} \\
& ((h : n \rightsquigarrow rexp), t) \models p_1 \text{ or } ((h : n \rightsquigarrow rexp), t) \models p_2 \\
= & \quad \% \text{ Def. 20} \\
& ((h : n \rightsquigarrow rexp), t) \models p_1 \vee p_2
\end{aligned}$$

- $p = p_1 \hat{\mathcal{U}} p_2$:

$$\begin{aligned}
& (h, t) \models (p_1 \hat{\mathcal{U}} p_2)[rexp/n] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models (p_1[rexp/n] \hat{\mathcal{U}} p_2[rexp/n]) \\
= & \quad \% \text{ Def. 20} \\
& \exists t_0 > t : (h, t_0) \models p_2[rexp/n] \text{ and } \forall t_1 \in (t, t_0) : (h, t_1) \models p_1[rexp/n] \\
= & \quad \% \text{ Induction} \\
& \exists t_0 > t : ((h : n \rightsquigarrow rexp), t_0) \models p_2 \\
& \text{and } \forall t_1 \in (t, t_0) : ((h : n \rightsquigarrow rexp), t_1) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : n \rightsquigarrow rexp), t) \models p_1 \hat{\mathcal{U}} p_2
\end{aligned}$$

- $p = p_1 \hat{\mathcal{S}} p_2$:

$$\begin{aligned}
& (h, t) \models (p_1 \hat{\mathcal{S}} p_2)[rexp/n] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models (p_1[rexp/n] \hat{\mathcal{S}} p_2[rexp/n]) \\
= & \quad \% \text{ Def. 20} \\
& \exists t_0 < t : (h, t_0) \models p_2[rexp/n] \text{ and } \forall t_1 \in (t_0, t) : (h, t_1) \models p_1[rexp/n] \\
= & \quad \% \text{ Induction} \\
& \exists t_0 < t : ((h : n \rightsquigarrow rexp), t_0) \models p_2 \\
& \text{and } \forall t_1 \in (t_0, t) : ((h : n \rightsquigarrow rexp), t_1) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : n \rightsquigarrow rexp), t) \models p_1 \hat{\mathcal{S}} p_2
\end{aligned}$$

- $p = \exists x.p_1$:

$$\begin{aligned}
 & (h, t) \models (\exists x.p_1)[rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \exists x.(p_1[rexp/n]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \text{ x-variant of } h \text{ and } (h_1, t) \models p_1[rexp/n] \\
 = & \quad \% \text{ Induction} \\
 & \exists h_1 : h_1 \text{ x-variant of } h \text{ and } ((h_1 : n \rightsquigarrow rexp), t) \models p_1 \\
 = & \quad \% h_2 = (h_1 : n \rightsquigarrow rexp), h_3 = (h : n \rightsquigarrow rexp) \\
 & \quad h_1 \text{ x-variant of } h \text{ iff } h_2 \text{ x-variant of } h_3 \\
 & \exists h_2 : h_2 \text{ x-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models \exists x.p_1
 \end{aligned}$$

- $p = \exists \epsilon.p_1$:

$$\begin{aligned}
 & (h, t) \models (\exists \epsilon.p_1)[rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \exists \epsilon.(p_1[rexp/n]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \text{ } \epsilon\text{-variant of } h \text{ and } (h_1, t) \models p_1[rexp/n] \\
 = & \quad \% \text{ Induction} \\
 & \exists h_1 : h_1 \text{ } \epsilon\text{-variant of } h \text{ and } ((h_1 : n \rightsquigarrow rexp), t) \models p_1 \\
 = & \quad \% h_2 = (h_1 : n \rightsquigarrow rexp), h_3 = (h : n \rightsquigarrow rexp) \\
 & \quad h_1 \text{ } \epsilon\text{-variant of } h \text{ iff } h_2 \text{ } \epsilon\text{-variant of } h_3 \\
 & \exists h_2 : h_2 \text{ } \epsilon\text{-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models \exists \epsilon.p_1
 \end{aligned}$$

- $p = \exists n_0.p_1$: for $n_0 \notin \text{varrexp} \cup \{n\}$

$$\begin{aligned}
 & (h, t) \models (\exists n_0.p_1)[rexp/n] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \exists n_0.(p_1[rexp/n]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \text{ } n_0\text{-variant of } h \text{ and } (h_1, t) \models p_1[rexp/n] \\
 = & \quad \% \text{ Induction} \\
 & \exists h_1 : h_1 \text{ } n_0\text{-variant of } h \text{ and } ((h_1 : n \rightsquigarrow rexp), t) \models p_1 \\
 = & \quad \% h_2 = (h_1 : n \rightsquigarrow rexp), h_3 = (h : n \rightsquigarrow rexp) \\
 & \quad h_1 \text{ } n_0\text{-variant of } h \text{ iff } h_2 \text{ } n_0\text{-variant of } h_3 \\
 & \exists h_2 : h_2 \text{ } n_0\text{-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : n \rightsquigarrow rexp), t) \models \exists n_0.p_1
 \end{aligned}$$

A.4 Proof of Lemma 2

$$f \ h, t) \models p [evexp/\epsilon] \text{ iff } ((h : \epsilon \rightsquigarrow evexp), t) \models p$$

Proof by induction on structure of p:

- $p = \mathbf{true}$:

$$\begin{aligned} & (h, t) \models \mathbf{true} [evexp/\epsilon] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models \mathbf{true} \\ = & \quad \% \text{ Def. 26} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models \mathbf{true} \end{aligned}$$

- $p = (exp_1 = exp_2)$:

$$\begin{aligned} & (h, t) \models (exp_1 = exp_2) [evexp/\epsilon] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models exp_1 = exp_2 \\ = & \quad \% \text{ Def. 20} \\ & (h, t) \models exp_1 = (h, t) \models exp_2 \\ = & \quad \% \text{ Def. 26} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models exp_1 = ((h : \epsilon \rightsquigarrow evexp), t) \models exp_2 \\ = & \quad \% \text{ Def. 20} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models exp_1 = exp_2 \end{aligned}$$

- $p = (exp_1 < exp_2)$:

$$\begin{aligned} & (h, t) \models (exp_1 < exp_2) [evexp/\epsilon] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models exp_1 < exp_2 \\ = & \quad \% \text{ Def. 20} \\ & (h, t) \models exp_1 < (h, t) \models exp_2 \\ = & \quad \% \text{ Def. 26} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models exp_1 < ((h : \epsilon \rightsquigarrow evexp), t) \models exp_2 \\ = & \quad \% \text{ Def. 20} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models exp_1 < exp_2 \end{aligned}$$

- $p = (evexp_1 = evexp_2)$:

$$\begin{aligned} & (h, t) \models (evexp_1 = evexp_2) [evexp/\epsilon] \\ = & \quad \% \text{ Def. 25} \\ & (h, t) \models evexp_1 [evexp_1/\epsilon] = evexp_2 [evexp_1/\epsilon] \\ = & \quad \% \text{ Def. 20} \\ & (h, t) \models evexp_1 [evexp_1/\epsilon] = (h, t) \models evexp_2 [evexp_2/\epsilon] \\ = & \quad \% \text{ Lemma 2c} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models evexp_1 = ((h : \epsilon \rightsquigarrow evexp), t) \models evexp_2 \\ = & \quad \% \text{ Def. 20} \\ & ((h : \epsilon \rightsquigarrow evexp), t) \models evexp_1 = evexp_2 \end{aligned}$$

- $p = \neg p_1$:

$$\begin{aligned}
 & (h, t) \models (\neg p_1)[evexp/\epsilon] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \neg(p_1[evexp/\epsilon]) \\
 = & \quad \% \text{ Def. 20} \\
 & \text{not } (h, t) \models p_1[evexp/\epsilon] \\
 = & \quad \% \text{ Induction} \\
 & \text{not } ((h : \epsilon \rightsquigarrow evexp), t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models \neg p_1
 \end{aligned}$$

- $p = p_1 \vee p_2$:

$$\begin{aligned}
 & (h, t) \models (p_1 \vee p_2)[evexp/\epsilon] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models (p_1[evexp/\epsilon] \vee p_2[evexp/\epsilon]) \\
 = & \quad \% \text{ Def. 20} \\
 & (h, t) \models p_1[evexp/\epsilon] \text{ or } (h, t) \models p_2[evexp/\epsilon] \\
 = & \quad \% \text{ Induction} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models p_1 \text{ or } ((h : \epsilon \rightsquigarrow evexp), t) \models p_2 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models p_1 \vee p_2
 \end{aligned}$$

- $p = p_1 \hat{\mathcal{U}} p_2$:

$$\begin{aligned}
 & (h, t) \models (p_1 \hat{\mathcal{U}} p_2)[evexp/\epsilon] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models (p_1[evexp/\epsilon] \hat{\mathcal{U}} p_2[evexp/\epsilon]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists t_0 > t : (h, t_0) \models p_2[evexp/\epsilon] \text{ and } \forall t_1 \in (t, t_0) : (h, t_1) \models p_1[evexp/\epsilon] \\
 = & \quad \% \text{ Induction} \\
 & \exists t_0 > t : ((h : \epsilon \rightsquigarrow evexp), t_0) \models p_2 \\
 & \text{and } \forall t_1 \in (t, t_0) : ((h : \epsilon \rightsquigarrow evexp), t_1) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models p_1 \hat{\mathcal{U}} p_2
 \end{aligned}$$

A.4 Proof of Lemma 2

- $p = p_1 \widehat{\mathcal{S}} p_2$:

$$\begin{aligned}
& (h, t) \models (p_1 \widehat{\mathcal{S}} p_2) [evexp/\epsilon] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models (p_1 [evexp/\epsilon] \widehat{\mathcal{S}} p_2 [evexp/\epsilon]) \\
= & \quad \% \text{ Def. 20} \\
& \exists t_0 < t : (h, t_0) \models p_2 [evexp/\epsilon] \text{ and } \forall t_1 \in (t_0, t) : (h, t_1) \models p_1 [evexp/\epsilon] \\
= & \quad \% \text{ Induction} \\
& \exists t_0 < t : ((h : \epsilon \rightsquigarrow evexp), t_0) \models p_2 \\
& \text{and } \forall t_1 \in (t_0, t) : ((h : \epsilon \rightsquigarrow evexp), t_1) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models p_1 \widehat{\mathcal{S}} p_2
\end{aligned}$$

- $p = \exists x.p_1$:

$$\begin{aligned}
& (h, t) \models (\exists x.p_1) [evexp/\epsilon] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \exists x.(p_1 [evexp/\epsilon]) \\
= & \quad \% \text{ Def. 20} \\
& \exists h_1 : h_1 \text{ x-variant of } h \text{ and } (h_1, t) \models p_1 [evexp/\epsilon] \\
= & \quad \% \text{ Induction} \\
& \exists h_1 : h_1 \text{ x-variant of } h \text{ and } ((h_1 : \epsilon \rightsquigarrow evexp), t) \models p_1 \\
= & \quad \% h_2 = (h_1 : \epsilon \rightsquigarrow evexp), h_3 = (h : \epsilon \rightsquigarrow evexp) \\
& \quad h_1 \text{ x-variant of } h \text{ iff } h_2 \text{ x-variant of } h_3 \\
& \exists h_2 : h_2 \text{ x-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \exists x.p_1
\end{aligned}$$

- $p = \exists \epsilon_0.p_1$: for $\epsilon_0 \notin \text{var}(evexp) \cup \{\epsilon\}$:

$$\begin{aligned}
& (h, t) \models (\exists \epsilon_0.p_1) [evexp/\epsilon] \\
= & \quad \% \text{ Def. 25} \\
& (h, t) \models \exists \epsilon_0.(p_1 [evexp/\epsilon]) \\
= & \quad \% \text{ Def. 20} \\
& \exists h_1 : h_1 \text{ } \epsilon_0\text{-variant of } h \text{ and } (h_1, t) \models p_1 [evexp/\epsilon] \\
= & \quad \% \text{ Induction} \\
& \exists h_1 : h_1 \text{ } \epsilon_0\text{-variant of } h \text{ and } ((h_1 : \epsilon \rightsquigarrow evexp), t) \models p_1 \\
= & \quad \% h_2 = (h_1 : \epsilon \rightsquigarrow evexp), h_3 = (h : \epsilon \rightsquigarrow evexp) \\
& \quad h_1 \text{ } \epsilon_0\text{-variant of } h \text{ iff } h_2 \text{ } \epsilon_0\text{-variant of } h_3 \\
& \exists h_2 : h_2 \text{ } \epsilon_0\text{-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
= & \quad \% \text{ Def. 20} \\
& ((h : \epsilon \rightsquigarrow evexp), t) \models \exists \epsilon_0.p_1
\end{aligned}$$

- $p = \exists n_0.p_1$:

$$\begin{aligned}
 & (h, t) \models (\exists n.p_1)[evexp/\epsilon] \\
 = & \quad \% \text{ Def. 25} \\
 & (h, t) \models \exists n.(p_1[evexp/\epsilon]) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \text{ n-variant of } h \text{ and } (h_1, t) \models p_1[evexp/\epsilon] \\
 = & \quad \% \text{ Induction} \\
 & \exists h_1 : h_1 \text{ n-variant of } h \text{ and } ((h_1 : \epsilon \rightsquigarrow evexp), t) \models p_1 \\
 = & \quad \% h_2 = (h_1 : \epsilon \rightsquigarrow evexp), h_3 = (h : \epsilon \rightsquigarrow evexp) \\
 & \quad h_1 \text{ n-variant of } h \text{ iff } h_2 \text{ n-variant of } h_3 \\
 & \exists h_2 : h_2 \text{ n-variant of } h_3 \text{ and } (h_2, t) \models p_1 \\
 = & \quad \% \text{ Def. 20} \\
 & ((h : \epsilon \rightsquigarrow evexp), t) \models \exists n.p_1
 \end{aligned}$$

A.5 Proof of Lemma 3

Lemma 3

Given a machine in DTL $(B, I \wedge \Box T)$ then there exists a semantic machine $M = (B, I, T)$ such that $Comp(M) = Hist(I \wedge \Box T)$.

Proof 19

Let $I \triangleq \{\sigma \in \Sigma \mid \exists h : \sigma = \theta_h(0) \wedge h \models I\}$ and
 let $T \triangleq \{\langle \delta, \sigma_0, \sigma_1 \rangle \in \Delta \times \Sigma^2 \mid \exists h : \exists t : Step_h(t) = \langle \delta, \sigma_0, \sigma_1 \rangle \wedge Step_h(t) \notin STU \wedge h \models \Box T\}$
 then $Hist(I \wedge \Box T) = Comp(M)$. *Proof:*

$$\begin{aligned}
 & Hist(I \wedge \Box T) \\
 = & \quad \% \text{ Def. 21} \\
 & \{h \in \mathcal{H} \mid h \models I \wedge \Box T\} \\
 = & \quad \% \text{ Def. 20} \\
 & \{h \in \mathcal{H} \mid h \models I \wedge \forall t : (h, t) \models T\} \\
 = & \quad \% \text{ Def. 28, def. of } I \text{ and } T \\
 & \{h \in \mathcal{H} \mid \theta_h(0) \in I \wedge \forall t : Step_h(t) \in STU \vee Step_h(t) \in T\} \\
 = & \quad \% \text{ Def. 16} \\
 & Comp(M)
 \end{aligned}$$

A.6 Proof of Lemma 4

Lemma 4

Given DTL machine specification of a system $(B, I \wedge \Box T \wedge L)$ then there exists a semantic machine specification $\mathcal{S} = (B, Comp(M) \cap L)$ such that $Comp(M) \cap L = Hist(I \wedge \Box T \wedge L)$.

Proof 20

Let $I \triangleq \{\sigma \in \Sigma \mid \exists h : \sigma = \theta_h(0) \wedge h \models I\}$ and
 let $T \triangleq \{\langle \delta, \sigma_0, \sigma_1 \rangle \in \Delta \times \Sigma^2 \mid \exists h : \exists t : Step_h(t) = \langle \delta, \sigma_0, \sigma_1 \rangle \wedge Step_h(t) \notin STU \wedge h \models \Box T\}$

A.7 Proof of Lemma 5

and

let $L \triangleq \text{Hist}(L)$ then $\text{Hist}(I \wedge \Box T \wedge L) = \text{Comp}(M) \cap L$. Because of machine closedness $cl(\text{Comp}(M) \cap \text{Hist}(L)) = \text{Comp}(M)$. Proof:

$$\begin{aligned}
& \text{Hist}(I \wedge \Box T \wedge L) \\
= & \quad \% \text{ Def. 21} \\
& \{h \in \mathcal{H} \mid h \models I \wedge \Box T\} \cap \text{Hist}(L) \\
= & \quad \% \text{ Def. 20} \\
& \{h \in \mathcal{H} \mid h \models I \wedge \forall t : (h, t) \models T\} \cap \text{Hist}(L) \\
= & \quad \% \text{ Def. 28, def. of } I, T \text{ and } L \\
& \{h \in \mathcal{H} \mid \theta_h(0) \in I \wedge \forall t : \text{Step}_h(t) \in \text{STU} \vee \text{Step}_h(t) \in T\} \cap L \\
= & \quad \% \text{ Def. 16} \\
& \text{Comp}(M) \cap L
\end{aligned}$$

A.7 Proof of Lemma 5

Lemma 5 (Properties of \mathcal{O} and \otimes)

Given systems (B_1, H_0) , (B_1, H_1) , (B_2, H_2) and (B_2, H_3) then

- (a) $H_0 \subseteq H_1$ implies $H_0 \otimes H_2 \subseteq H_1 \otimes H_2$
- (b) $\mathcal{O}_{X_{12}}(H_1 \otimes H_2) = \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2)$
- (c) $H_0 \subseteq H_1$ implies $\mathcal{O}_{X_1}(H_0) \subseteq \mathcal{O}_{X_1}(H_1)$
- (d) $(H_0 \cap H_1) \otimes (H_2 \cap H_3) \subseteq (H_0 \otimes H_2) \cap (H_1 \otimes H_3)$

Proof 21

- (a) $H_0 \subseteq H_1$ implies $H_0 \otimes H_2 \subseteq H_1 \otimes H_2$

$$\begin{aligned}
& h \in H_0 \otimes H_2 \\
= & \quad \% \text{ Def. 32} \\
& \exists h_1 \in H_0, h_2 \in H_2. \otimes (h, h_1, h_2) \\
\rightarrow & \quad \% H_0 \subseteq H_1 \\
& \exists h_1 \in H_1, h_2 \in H_2. \otimes (h, h_1, h_2) \\
= & \quad \% \text{ Def. 32} \\
& h \in H_1 \otimes H_2
\end{aligned}$$

- (b) $\mathcal{O}_{X_{12}}(H_1 \otimes H_2) = \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2)$

$$\begin{aligned}
 & h \in \mathcal{O}_{X_{12}}(H_1 \otimes H_2) \\
 = & \quad \% \text{ Def. 30} \\
 & \exists h_3 \in H_1 \otimes H_2 : h \text{ } X_1 \cup X_2\text{-variant of } h_3 \\
 = & \quad \% \text{ Def. 32} \\
 & \exists h_1, h_2, h_3 : h_1 \in \text{Obs}_1 \wedge h_2 \in \text{Obs}_2 \wedge \otimes(h_3, h_1, h_2) \\
 & \wedge h \text{ } X_1 \cup X_2\text{-variant of } h_3 \\
 = & \quad \% \psi = \psi_3, \psi_4 = \psi_1, \psi_5 = \psi_2 \\
 & \quad \theta_1^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus (X_1 \cup X_2)} = \theta_3^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus (X_1 \cup X_2)}, \\
 & \quad \theta_4^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus X_1} = \theta_1^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus X_1}, \\
 & \quad \theta_5^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus X_2} = \theta_2^2|_{(\mathfrak{X} \cup \mathfrak{X}) \setminus X_2} \\
 & \quad \theta_3 = \theta_1 = \theta_2, \theta = \theta_4 = \theta_5 \\
 & \exists h_1, h_2, h_4, h_5 : h_1 \in H_1 \wedge h_2 \in H_2 \wedge \otimes(h, h_4, h_5) \\
 & h_4 \text{ } X_1\text{-variant of } h_1 \wedge h_5 \text{ } X_2\text{-variant of } h_2 \\
 = & \quad \% \text{ Def. 30} \\
 & \exists h_4 \in \mathcal{O}_{X_1}(H_1), h_5 \in \mathcal{O}_{X_2}(H_2) : \otimes(h, h_4, h_5) \\
 = & \quad \% \text{ Def. 32} \\
 & h \in \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2)
 \end{aligned}$$

(c) $H_0 \subseteq H_1$ implies $\mathcal{O}_{X_1}(H_0) \subseteq \mathcal{O}_{X_1}(H_1)$

$$\begin{aligned}
 & h \in \mathcal{O}_{X_1}(H_0) \\
 = & \quad \% \text{ Def. 30} \\
 & \exists h_1 : h_1 \in H_0 \wedge h \text{ } X_1\text{-variant of } h_1 \\
 \rightarrow & \quad \% H_0 \subseteq H_1 \\
 & \exists h_1 : h_1 \in H_1 \wedge h \text{ } X_1\text{-variant of } h_1 \\
 = & \quad \% \text{ Def. 30} \\
 & h \in \mathcal{O}_{X_1}(H_1)
 \end{aligned}$$

(d) $(H_0 \cap H_1) \otimes (H_2 \cap H_3) \subseteq (H_0 \otimes H_2) \cap (H_1 \otimes H_3)$

$$\begin{aligned}
 & h \in (H_0 \cap H_1) \otimes (H_2 \cap H_3) \\
 = & \quad \% \text{ Def. 32} \\
 & \exists h_1, h_2 : h_1 \in (H_0 \cap H_1) \wedge h_2 \in (H_2 \cap H_3) \wedge \otimes(h, h_1, h_2) \\
 \rightarrow & \quad \% \text{ Calculus} \\
 & \exists h_1, h_2 : h_1 \in H_0 \wedge h_2 \in H_2 \wedge \otimes(h, h_1, h_2) \\
 & \wedge \exists h_1, h_2 : h_1 \in H_1 \wedge h_2 \in H_3 \wedge \otimes(h, h_1, h_2) \\
 = & \quad \% \text{ Def. 32} \\
 & h \in H_0 \otimes H_2 \wedge h \in H_1 \otimes H_3
 \end{aligned}$$

A.8 Proof of Theorem 3

Theorem 3 (Compositional refinement)

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) such that $\mathfrak{D}(B_1) = \mathfrak{D}(B_3)$ and $\mathfrak{D}(B_2) = \mathfrak{D}(B_4)$ then $\mathcal{S}_1 \text{ ref } \mathcal{S}_3$ and $\mathcal{S}_2 \text{ ref } \mathcal{S}_4$ implies $\mathcal{S}_1 \parallel \mathcal{S}_2 \text{ ref } \mathcal{S}_3 \parallel \mathcal{S}_4$.

A.9 Proof of Lemma 6

Proof 22

$$\begin{aligned}
& \mathcal{S}_1 \parallel \mathcal{S}_2 \text{ ref } \mathcal{S}_3 \parallel \mathcal{S}_4 \\
= & \quad \% \text{ Def. 31, 32, } \mathfrak{D}(B_1) = \mathfrak{D}(B_3), \mathfrak{D}(B_2) = \mathfrak{D}(B_4) \\
& \mathcal{O}_{X_{12}}(H_1 \otimes H_2) \subseteq \mathcal{O}_{X_{34}}(H_3 \otimes H_4) \\
= & \quad \% \text{ Lemma 5(b)} \\
& \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2) \subseteq \mathcal{O}_{X_3}(H_3) \otimes \mathcal{O}_{X_4}(H_4) \\
= & \quad \% \mathcal{O}_{X_1}(H_1) \subseteq \mathcal{O}_{X_3}(H_3) \text{ with Lemma 5(a) gives} \\
& \quad \mathcal{O}_{X_1}(H_1) \otimes \mathcal{O}_{X_2}(H_2) \subseteq \mathcal{O}_{X_3}(H_3) \otimes \mathcal{O}_{X_2}(H_2) \\
& \quad \mathcal{O}_{X_2}(H_2) \subseteq \mathcal{O}_{X_4}(H_4) \text{ with Lemma 5(a) gives} \\
& \quad \mathcal{O}_{X_3}(H_3) \otimes \mathcal{O}_{X_2}(H_2) \subseteq \mathcal{O}_{X_3}(H_3) \otimes \mathcal{O}_{X_4}(H_4) \\
& \text{true}
\end{aligned}$$

A.9 Proof of Lemma 6

Lemma 6

Given DTL machine specification $\mathcal{S} = (B, I \wedge \Box T \wedge L)$ then $\mathcal{O}_X(\text{Hist}(I \wedge \Box T \wedge L)) = \text{Hist}((\exists X. (I \wedge \Box T \wedge L)))$

Proof 23

$$\begin{aligned}
& \text{Hist}((\exists X. (I \wedge \Box T \wedge L))) \\
= & \quad \% \text{ Def. 21} \\
& \{h \mid h \models (\exists X. (I \wedge \Box T \wedge L))\} \\
= & \quad \% \text{ Def. 20} \\
& \{h \mid \exists h_1 : h_1 \text{ X-variant of } h \wedge h_1 \models I \wedge \Box T \wedge L\} \\
= & \quad \% \text{ Def. 21} \\
& \{h \mid \exists h_1 : h_1 \text{ X-variant of } h \wedge h_1 \in \text{Hist}(I \wedge \Box T \wedge L)\} \\
= & \quad \% \text{ Def. 30} \\
& \mathcal{O}_X(\text{Hist}(I \wedge \Box T \wedge L))
\end{aligned}$$

A.10 Proof of Theorem 4

Theorem 4 (Refinement of machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ where $B_c \triangleq (B_c^P, (V_c, X_c))$ and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ where $B_a \triangleq (B_a^P, (V_a, X_a))$. Then \mathcal{S}_c refines \mathcal{S}_a denoted $\mathcal{S}_c \text{ ref } \mathcal{S}_a$ iff

$$\begin{aligned}
& \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\
& (\exists X_c. (I_c \wedge \Box T_c \wedge L_c)) \rightarrow (\exists X_a. (I_a \wedge \Box T_a \wedge L_a))
\end{aligned}$$

Proof 24

$$\begin{aligned}
 & \mathcal{S}_c \text{ ref } \mathcal{S}_a \\
 = & \quad \% \text{ Def. 31} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \mathcal{O}_{X_c}(\text{Hist}(I_c \wedge \Box T_c \wedge L_c)) \subseteq \mathcal{O}_{X_a}(\text{Hist}(I_a \wedge \Box T_a \wedge L_a)) \\
 = & \quad \% \text{ Lemma 6} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \text{Hist}((\exists X_c . (I_c \wedge \Box T_c \wedge L_c))) \subseteq \text{Hist}((\exists X_a . (I_a \wedge \Box T_a \wedge L_a))) \\
 = & \quad \% \text{ Def. 20 and 21} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \models (\exists X_c . (I_c \wedge \Box T_c \wedge L_c)) \rightarrow (\exists X_a . (I_a \wedge \Box T_a \wedge L_a))
 \end{aligned}$$

A.11 Proof of Theorem 5

Theorem 5 (Semantic merge is almost conjunction)

Given machine system specifications $(B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq ((\text{In}_i, \text{Out}_i), (V_i, X_i))$, for $i = 1, 2$ and composed machine system specification as in definition 35, i.e., (B, H) where $H \triangleq \exists \epsilon_1, \epsilon_2. B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2) [\epsilon_2/\epsilon]$ and $B \triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2))$ then

$$\text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \otimes \text{Hist}(I_2 \wedge \Box T_2 \wedge L_2) = \text{Hist}(H)$$

Proof 25

$$\begin{aligned}
 & h \in \text{Hist}(H) \\
 = & \quad \% \text{ Def. 20} \\
 & \exists h_1 : h_1 \{ \epsilon_1, \epsilon_2 \}\text{-variant of } h \wedge \\
 & h_1 \models_{B_1^A \odot_{B_2^A}} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2) [\epsilon_2/\epsilon] \\
 = & \quad \% \text{ Def. 20 and 25} \\
 & \exists h_1 : h_1 \{ \epsilon_1, \epsilon_2 \}\text{-variant of } h \wedge \\
 & h_1 \models_{B_1^A \odot_{B_2^A}} (\epsilon, \epsilon_1, \epsilon_2) \wedge \\
 & (h : \epsilon \rightsquigarrow \epsilon_1) \models I_1 \wedge \Box T_1 \wedge L_1 \wedge \\
 & (h : \epsilon \rightsquigarrow \epsilon_2) \models I_2 \wedge \Box T_2 \wedge L_2 \\
 = & \quad \% \theta_1 = \theta, h_3 = (h_1 : \epsilon \rightsquigarrow \epsilon_1), h_4 = (h_1 : \epsilon \rightsquigarrow \epsilon_2), \\
 & \quad \psi_1(t)(\epsilon) = \psi(t)(\epsilon), \psi_1(t)(\epsilon_1) = \psi_3(t)(\epsilon), \psi_1(t)(\epsilon_2) = \psi_4(t)(\epsilon) \\
 & \quad \text{i.e., } h_1 \{ \epsilon_1, \epsilon_2 \}\text{-variant of } h \wedge h_1 \models_{B_1^A \odot_{B_2^A}} (\epsilon, \epsilon_1, \epsilon_2) \text{ iff } \otimes (h, h_3, h_4) \\
 & \exists h_3, h_4 : h_3 \models I_1 \wedge \Box T_1 \wedge L_1 \wedge \\
 & h_4 \models I_2 \wedge \Box T_2 \wedge L_2 \wedge \\
 & \otimes (h, h_3, h_4) \\
 = & \quad \% \text{ Def. 21} \\
 & h \in \text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \otimes \text{Hist}(I_2 \wedge \Box T_2 \wedge L_2)
 \end{aligned}$$

A.12 Proof of Theorem 6

Theorem 6

Given machine specification $\mathcal{S} \triangleq (B, H)$ and given set of shared variables $V_1 \subseteq V$ then

$$Enc_{V_1}(Hist(H)) = Hist(H \wedge (\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1))$$

Proof 26

$$\begin{aligned} & h \in Enc_{V_1}(Hist(H)) \\ = & \quad \% \text{ Def. 33} \\ & h \in Hist(H) \wedge \forall t : \psi(t)(\epsilon) = \mathbf{e} \rightarrow \theta(t)|_{V_1}^1 = \lim_{t \leftarrow t_1} \theta(t_1)|_{V_1}^1 \\ = & \quad \% \text{ Semantics. of } (\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1) \\ & h \in Hist(H) \wedge h \in Hist(\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1) \\ = & \quad \% \text{ Calculus} \\ & h \in Hist(H \wedge (\epsilon = \mathbf{e} \Rightarrow V'_1 = V_1)) \end{aligned}$$

A.13 Proof of Lemma 7

Lemma 7

Given concrete system $\mathcal{S}_c \triangleq (B_c, H_c)$ and abstract system $\mathcal{S}_a \triangleq (B_a, H_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a refinement mapping from \mathcal{S}_c to \mathcal{S}_a , then $\mathcal{S}_c \text{ ref } \mathcal{S}_a$.

Proof 27

$$\begin{aligned} & \mathcal{S}_c \text{ ref } \mathcal{S}_a \\ = & \quad \% \text{ Def. 31} \\ & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\ & \mathcal{O}_{X_c}(H_c) \subseteq \mathcal{O}_{X_a}(H_a) \\ \leftarrow & \quad \% \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\ & \mathcal{O}_{X_c}(H_c) \subseteq \mathcal{O}_{X_a}(H_a) \\ \leftarrow & \quad \% \text{ Def. 37, i.e. } \mathcal{O}_{X_a}(f(H_c)) \subseteq \mathcal{O}_{X_a}(H_a), \mathcal{O}_{X_c}(H_c) = \mathcal{O}_{X_a}(f(H_c)) \\ & \text{true} \end{aligned}$$

A.14 Proof of Lemma 8

Lemma 8

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, Comp(M_c) \cap L_c)$ and given abstract machine specification $\mathcal{S}_a \triangleq (B_a, Comp(M_a) \cap L_a)$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a refinement mapping from \mathcal{S}_c to \mathcal{S}_a then $\mathcal{S}_c \text{ ref } \mathcal{S}_a$.

Proof 28

We first prove the following result:

For all $h_c \in \text{Comp}(M_c)$, there exists a $h_a \in \text{Comp}(M_a)$ s.t. for all $t \in \mathbb{R}^{\geq 0}$, $\langle \psi_c(t), \theta_c(t) \rangle = \langle \psi_a(t), \theta(t)_a \rangle$ and $f(\theta_c(t)) = \theta_a(t)$.

$$\begin{aligned}
 & h_c \in \text{Comp}(M_c) \\
 = & \quad \% \text{ Def. 16} \\
 & \theta_c(0) \in I_c \wedge \\
 & \forall t : \langle \psi_c(t), \theta_c(t), \lim_{t \leftarrow t_1} \theta_c(t_1) \rangle \in T_c \vee (\psi_c(t)(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\} \wedge \theta_c(t) = \lim_{t \leftarrow t_1} \theta_c(t_1)) \\
 \rightarrow & \quad \% \text{ Def. 38} \\
 & f(\theta_c(0)) \in I_a \\
 & \wedge \forall t : \langle \psi_a(t), f(\theta_c(t)), \lim_{t \leftarrow t_1} f(\theta_c(t_1)) \rangle \in T_a \vee \\
 & \quad (\psi_a(t)(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\} \wedge f(\theta_c(t)) = \lim_{t \leftarrow t_1} f(\theta_c(t_1))) \\
 = & \quad \% \text{ Def. 16} \\
 & h_a \in \text{Comp}(M_a) \wedge \forall t : \langle \psi_c(t), \theta_c(t) \rangle = \langle \psi_a(t), \theta(t)_a \rangle \wedge f(\theta_c(t)) = \theta_a(t)
 \end{aligned}$$

The proof of Lemma 8 is then as follows:

$$\begin{aligned}
 & \mathcal{S}_c \text{ ref } \mathcal{S}_a \\
 = & \quad \% \text{ Def. 31} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\
 & \mathcal{O}_{X_c}(\text{Comp}(M_c) \cap L_c) \subseteq \mathcal{O}_{X_a}(\text{Comp}(M_a) \cap L_a) \\
 = & \quad \% \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \mathcal{O}_{X_c}(\text{Comp}(M_c) \cap L_c) \subseteq \mathcal{O}_{X_a}(\text{Comp}(M_a) \cap L_a) \\
 \leftarrow & \quad \% \text{ From Def. 37, } f(\text{Comp}(M_c) \cap L_c) \subseteq L_a \\
 & \text{Property of } f, f(\text{Comp}(M_c) \cap L_c) \subseteq f(\text{Comp}(M_c)), \\
 & \text{by above result, } f(\text{Comp}(M_c)) \subseteq \text{Comp}(M_a), \\
 & \text{Resulting in , } f(\text{Comp}(M_c) \cap L_c) \subseteq \text{Comp}(M_a) \cap L_a \\
 & \text{From Lemma 5(c), } \mathcal{O}_{X_a}(f(\text{Comp}(M_c) \cap L_c)) \subseteq \mathcal{O}_{X_a}(\text{Comp}(M_a) \cap L_a), \\
 & \text{From Def. 37, } \mathcal{O}_{X_c}(\text{Comp}(M_c) \cap L_c) = \mathcal{O}_{X_a}(f(\text{Comp}(M_c) \cap L_c)) \\
 & \text{true}
 \end{aligned}$$

A.15 Proof of Theorem 7

Theorem 7 (Compositional relative refinement)

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and given set W_c constraining B_{12} (the basis of $\mathcal{S}_1 \parallel \mathcal{S}_2$). And given abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) and given set W_a constraining B_{34} (the basis of $\mathcal{S}_3 \parallel \mathcal{S}_4$). Then the following holds:

$$\begin{array}{l}
 H_1 \otimes H_2 \cap W_{c1} \otimes W_{c2} \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
 W_c \subseteq W_{c1} \otimes W_{c2} \\
 W_{a3} \otimes W_{a4} \subseteq W_a \\
 \mathcal{S}_1 \text{ }_{W_{c1}} \text{ ref } \text{}^{W_{a3}} \mathcal{S}_3 \\
 \mathcal{S}_2 \text{ }_{W_{c2}} \text{ ref } \text{}^{W_{a4}} \mathcal{S}_4 \\
 \hline
 \mathcal{S}_1 \parallel \mathcal{S}_2 \text{ }_{W_c} \text{ ref } \text{}^{W_a} \mathcal{S}_3 \parallel \mathcal{S}_4
 \end{array}$$

W_{c_i} constraining B_i ($i=1,2$)
 W_{a_j} constraining B_j ($j=3,4$)

A.16 Proof of Lemma 9

Proof 29

Assume agreement on the bases. Then according to Def. 39 and 40 we must infer from the assumptions that $\mathcal{O}_{X_{12}}(H_1 \otimes H_2 \cap W_c) \subseteq \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)$.

$$\begin{aligned}
& \mathcal{O}_{X_{12}}(H_1 \otimes H_2 \cap W_c) \\
\subseteq & \quad \% H_1 \otimes H_2 \cap W_{c1} \otimes W_{c2} \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
& \quad W_c \subseteq W_{c1} \otimes W_{c2} \\
& \quad \text{Lemma 5(c)} \\
& \mathcal{O}_{X_{12}}((H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2})) \\
\subseteq & \quad \% \mathcal{S}_1 \text{ } W_{c1} \text{ ref }^{W_{a3}} \mathcal{S}_3 \\
& \quad \mathcal{S}_2 \text{ } W_{c2} \text{ ref }^{W_{a4}} \mathcal{S}_4 \\
& \quad \text{Lemma 5(a), (b)} \\
& \mathcal{O}_{X_{34}}((H_3 \cap W_{a3}) \otimes (H_4 \cap W_{a4})) \\
\subseteq & \quad \% \text{ Lemma 5(c), (d)} \\
& \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_{a3} \otimes W_{a4}) \\
\subseteq & \quad \% W_{a3} \otimes W_{a4} \subseteq W_a \\
& \quad \text{Lemma 5(c)} \\
& \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)
\end{aligned}$$

A.16 Proof of Lemma 9

Lemma 9

Given systems $\mathcal{S}_i = (B_i, H_i)$ and sets W_i constraining B_i ($i = 1, 2$) with no restrictions on the event variables. Then the following holds:

$$(H_1 \cap W_1) \otimes (H_2 \cap W_2) = H_1 \otimes H_2 \cap W_1 \otimes W_2$$

Proof 30

From Lemma 5(d) we infer $(H_1 \cap W_1) \otimes (H_2 \cap W_2) \subseteq H_1 \otimes H_2 \cap W_1 \otimes W_2$ so we must prove $H_1 \otimes H_2 \cap W_1 \otimes W_2 \subseteq (H_1 \cap W_1) \otimes (H_2 \cap W_2)$.

$$\begin{aligned}
& h \in H_1 \otimes H_2 \cap W_1 \otimes W_2 \\
= & \quad \% \text{ Def.32} \\
& \exists h_1, h_2 : h_1 \in H_1 \wedge h_2 \in H_2 \wedge \otimes(h, h_1, h_2) \\
& \wedge \exists h_3, h_4 : h_3 \in W_1 \wedge h_4 \in W_2 \wedge \otimes(h, h_3, h_4) \\
\rightarrow & \quad \% W_i \text{ puts no restriction on } \epsilon \text{ variables, } \theta = \theta_3 = \theta_4 \\
& \exists h_1, h_2 : h_1 \in H_1 \wedge h_2 \in H_2 \wedge \otimes(h, h_1, h_2) \\
& \wedge h \in W_1 \wedge h \in W_2 \\
= & \quad \% \text{ Calc.} \\
& \exists h_1, h_2 : h_1 \in H_1 \wedge h_2 \in H_2 \wedge h \in W_1 \wedge h \in W_2 \wedge \otimes(h, h_1, h_2) \\
\rightarrow & \quad \% W_i \text{ puts no restriction on } \epsilon \text{ variables, } \theta = \theta_1 = \theta_2, \\
& \quad W_i \text{ constrains } B_i \\
& \exists h_1, h_2 : h_1 \in H_1 \cap W_1 \wedge h_2 \in H_2 \cap W_2 \wedge \otimes(h, h_1, h_2) \\
= & \quad \% \text{ Def. 32} \\
& h \in (H_1 \cap W_1) \otimes (H_2 \cap W_2)
\end{aligned}$$

A.17 Proof of Lemma 10

Lemma 10

Given concrete systems $\mathcal{S}_i = (B_i, H_i)$ ($i = 1, 2$) and given set W_c constraining B_{12} . And given abstract systems $\mathcal{S}_j = (B_j, H_j)$ ($j = 3, 4$) and given set W_a constraining B_{34} without restricting the ϵ variables. Then the following holds:

$$\begin{array}{l}
 H_1 \otimes H_2 \cap W_{c1} \otimes W_{c2} \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
 W_c \subseteq W_{c1} \otimes W_{c2} \\
 \mathcal{S}_1 \mathop{W_{c1}}_{\text{ref}} \mathop{W_a}^{\mathcal{S}_3} \\
 \mathcal{S}_2 \mathop{W_{c2}}_{\text{ref}} \mathop{W_a}^{\mathcal{S}_4} \\
 \hline
 \mathcal{S}_1 \parallel \mathcal{S}_2 \mathop{W_c}_{\text{ref}} \mathop{W_a}^{\mathcal{S}_3} \parallel \mathcal{S}_4
 \end{array}
 \quad W_{ci} \text{ constraining } B_i \ (i=1,2)$$

Proof 31

Assume agreement on the bases. Then according to Def. 39 and 40 we must infer from the assumptions that $\mathcal{O}_{X_{12}}(H_1 \otimes H_2 \cap W_c) \subseteq \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)$. We will first prove the following:

$$\mathcal{O}_{X_{34}}((H_3 \cap W_a) \otimes (H_4 \cap W_a)) \subseteq \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)$$

$$\begin{aligned}
 & h \in \mathcal{O}_{X_{34}}((H_3 \cap W_a) \otimes (H_4 \cap W_a)) \\
 = & \quad \% \text{ Def. 30} \\
 & \exists h_1 : h_1 \in (H_3 \cap W_a) \otimes (H_4 \cap W_a) \wedge h \text{ X}_{34}\text{-variant of } h_1 \\
 = & \quad \% \text{ Def. 32} \\
 & \exists h_1 : (\exists h_3, h_4 : h_3 \in (H_3 \cap W_a) \wedge h_4 \in (H_4 \cap W_a) \wedge \otimes(h_1, h_3, h_4)) \\
 & \wedge h \text{ X}_{34}\text{-variant of } h_1 \\
 \rightarrow & \quad \% \theta_1 = \theta_3 = \theta_4, W_a \text{ doesn't restrict the } \epsilon \text{ variables} \\
 & \exists h_1 : (\exists h_3, h_4 : h_3 \in H_3 \wedge h_4 \in H_4 \wedge h_1 \in W_a \wedge \otimes(h_1, h_3, h_4)) \\
 & \wedge h \text{ X}_{34}\text{-variant of } h_1 \\
 = & \quad \% \text{ Calc.} \\
 & \exists h_1 : (\exists h_3, h_4 : h_3 \in H_3 \wedge h_4 \in H_4 \wedge \otimes(h_1, h_3, h_4)) \\
 & \wedge h_1 \in W_a \wedge h \text{ X}_{34}\text{-variant of } h_1 \\
 = & \quad \% \text{ Def. 32} \\
 & \exists h_1 : h_1 \in H_3 \otimes H_4 \cap W_a \wedge h \text{ X}_{34}\text{-variant of } h_1 \\
 = & \quad \% \text{ Def. 30} \\
 & h \in \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)
 \end{aligned}$$

A.18 Proof of Lemma 11

The proof of the Lemma is then as follows:

$$\begin{aligned}
& \mathcal{O}_{X_{12}}(H_1 \otimes H_2 \cap W_c) \\
\subseteq & \quad \% H_1 \otimes H_2 \cap W_{c1} \otimes W_{c2} \subseteq (H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2}) \\
& \quad W_c \subseteq W_{c1} \otimes W_{c2} \\
& \quad \text{Lemma 5(c)} \\
& \mathcal{O}_{X_{12}}((H_1 \cap W_{c1}) \otimes (H_2 \cap W_{c2})) \\
\subseteq & \quad \% \mathcal{S}_1 \text{ } W_{c1} \text{ ref }^{W_a} \mathcal{S}_3 \\
& \quad \mathcal{S}_2 \text{ } W_{c2} \text{ ref }^{W_a} \mathcal{S}_4 \\
& \quad \text{Lemma 5(a), (b)} \\
& \mathcal{O}_{X_{34}}((H_3 \cap W_a) \otimes (H_4 \cap W_a)) \\
\subseteq & \quad \% \text{ Above result} \\
& \mathcal{O}_{X_{34}}(H_3 \otimes H_4 \cap W_a)
\end{aligned}$$

A.18 Proof of Lemma 11

Lemma 11

Given sets W_i ($i = 1, 2$) not restricting the ϵ variables then

$$W_1 \otimes W_2 = W_1 \cap W_2.$$

Proof 32

$$\begin{aligned}
& h \in W_1 \otimes W_2 \\
= & \quad \% \text{ Def. 32} \\
& \exists h_1, h_2 : h_1 \in W_1 \wedge h_2 \in W_2 \wedge \otimes(h, h_1, h_2) \\
= & \quad \% W_i \text{ don't restrict } \epsilon \text{ variables, i.e.} \\
& \quad \otimes(h, h_1, h_2) \leftrightarrow h = h_1 = h_2 \\
& \exists h_1, h_2 : h_1 \in W_1 \wedge h_2 \in W_2 \wedge h = h_1 = h_2 \\
= & \quad \% \text{ Calc.} \\
& h \in W_1 \cap W_2
\end{aligned}$$

A.19 Proof of Theorem 8

Theorem 8 (Relative refinement of DTL machine specifications)

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, I_c \wedge \Box T_c \wedge L_c)$ and DTL formula W_c over B_c and abstract machine specification $\mathcal{S}_a \triangleq (B_a, I_a \wedge \Box T_a \wedge L_a)$ and DTL formula W_a over B_a . Let $G_c \triangleq I_c \wedge \Box T_c \wedge L_c \wedge W_c$ and $G_a \triangleq I_a \wedge \Box T_a \wedge L_a \wedge W_a$. Then $\mathcal{S}_c \text{ Hist}(W_c) \text{ ref }^{Hist(W_a)} \mathcal{S}_a$ iff

$$\begin{aligned}
& \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\
& \models (\exists X_c . (G_c)) \rightarrow (\exists X_a . (G_a))
\end{aligned}$$

Proof 33

$$\begin{aligned}
 & \mathcal{S}_c \text{Hist}(W_c) \mathbf{ref}^{\text{Hist}(W_a)} \mathcal{S}_a \\
 = & \quad \% \text{ Def. 39} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \mathcal{O}_{X_c}(\text{Hist}(I_c \wedge \Box T_c \wedge L_c) \cap \text{Hist}(W_c)) \\
 & \subseteq \mathcal{O}_{X_a}(\text{Hist}(I_a \wedge \Box T_a \wedge L_a) \cap \text{Hist}(W_a)) \\
 = & \quad \% \text{ Def. 21 and 20} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \mathcal{O}_{X_c}(\text{Hist}(I_c \wedge \Box T_c \wedge L_c \wedge W_c)) \subseteq \mathcal{O}_{X_a}(\text{Hist}(I_a \wedge \Box T_a \wedge L_a \wedge W_a)) \\
 = & \quad \% \text{ Theorem 4} \\
 & \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
 & \models (\exists X_c. (I_c \wedge \Box T_c \wedge L_c \wedge W_c)) \rightarrow (\exists X_a. (I_a \wedge \Box T_a \wedge L_a \wedge W_a))
 \end{aligned}$$

A.20 Proof of Theorem 9

Theorem 9 (Relative composition corresponds to semantic merge)

Given machine system specifications $(B_i, I_i \wedge \Box T_i \wedge L_i)$ where $B_i \triangleq ((\text{In}_i, \text{Out}_i), (V_i, X_i))$, and given DTL formulae W_i over B_i for $i = 1, 2$ and let $\overline{W} \triangleq (\text{Hist}(W_c), \text{Hist}(W_a))$ and given the relative composed system as in Def. 41, i.e., (B, H) where $B \triangleq ((\text{In}_1 \setminus \text{Out}_2 \cup \text{In}_2 \setminus \text{Out}_1, \text{Out}_1 \setminus \text{In}_2 \cup \text{Out}_2 \setminus \text{In}_1), (V_1 \cup V_2, X_1 \cup X_2))$ and $H \triangleq \exists \epsilon_1, \epsilon_2. (B_1^A \odot_{B_2^A} (\epsilon, \epsilon_1, \epsilon_2) \wedge (I_1 \wedge \Box T_1 \wedge L_1 \wedge W_1) [\epsilon_1/\epsilon] \wedge (I_2 \wedge \Box T_2 \wedge L_2 \wedge W_2) [\epsilon_2/\epsilon])$ then

$$\text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \overline{W} \text{Hist}(I_2 \wedge \Box T_2 \wedge L_2) = \text{Hist}(H)$$

Proof 34

$$\begin{aligned}
 & \text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \overline{W} \text{Hist}(I_2 \wedge \Box T_2 \wedge L_2) \\
 = & \quad \% \text{ Def. 40} \\
 & (\text{Hist}(I_1 \wedge \Box T_1 \wedge L_1) \cap \text{Hist}(W_1)) \otimes (\text{Hist}(I_2 \wedge \Box T_2 \wedge L_2) \cap \text{Hist}(W_2)) \\
 = & \quad \% \text{ Def. 21 and 20} \\
 & (\text{Hist}(I_1 \wedge \Box T_1 \wedge L_1 \wedge W_1)) \otimes (\text{Hist}(I_2 \wedge \Box T_2 \wedge L_2 \wedge W_2)) \\
 = & \quad \% \text{ Theorem 5, def. of } H \\
 & \text{Hist}(H)
 \end{aligned}$$

A.21 Proof of Lemma 12

Lemma 12

Given machines $M \triangleq (B, I, T)$ and $M_1 \triangleq (B, I_1, T_1)$. Define machine M_2 as (B, I_2, T_2) where I_2 and T_2 are as follows:

- $I_2 \triangleq I \cap I_1$, and
- $T_2 \triangleq T \cap T_1$.

Then $\text{Comp}(M_2) = \text{Comp}(M) \cap \text{Comp}(M_1)$.

A.22 Proof of Lemma 13

Proof 35

$$\begin{aligned}
& h \in \text{Comp}(M) \cap \text{Comp}(M_1) \\
= & \quad \% \text{ Def. 16} \\
& \theta(0) \in I \wedge \theta(0) \in I_1 \\
& (\forall t : \text{Step}_h \in T \vee \text{Step}_h \in \text{STU}) \wedge (\forall t : \text{Step}_h \in T_1 \vee \text{Step}_h \in \text{STU}) \\
= & \quad \% \text{ Calculus} \\
& \theta(0) \in I \cap I_1 \\
& \forall t : \text{Step}_h \in T \cap T_2 \vee \text{Step}_h \in \text{STU} \\
= & \quad \% \text{ Def. 16} \\
& h \in \text{Comp}(M_2)
\end{aligned}$$

A.22 Proof of Lemma 13

Lemma 13

Given concrete machine specification $\mathcal{S}_c \triangleq (B_c, \text{Comp}(M_c) \cap L_c)$ and set $W_c = \text{Comp}(M_{c1}) \cap L_{c1}$, and given abstract machine specification $\mathcal{S}_a \triangleq (B_a, \text{Comp}(M_a) \cap L_a)$ and set $W_a = \text{Comp}(M_{a1}) \cap L_{a1}$ s.t. $\mathfrak{D}(B_c) = \mathfrak{D}(B_a)$. If there exists a relative refinement mapping from \mathcal{S}_c to \mathcal{S}_a then $\mathcal{S}_c \text{ }_{W_c} \text{ref}^{W_a} \mathcal{S}_a$.

Proof 36

We first prove the following result:

For all $h_c \in \text{Comp}(M_c) \cap \text{Comp}(M_{c1})$, there exists a $h_a \in \text{Comp}(M_a) \cap \text{Comp}(M_{a1})$ s.t. for all $t \in \mathbb{R}^{\geq 0}$, $\langle \psi_c(t), \theta_c(t) \rangle = \langle \psi_a(t), \theta(t)_a \rangle$ and $f(\theta_c(t)) = \theta_a(t)$.

$$\begin{aligned}
& h_c \in \text{Comp}(M_c) \cap \text{Comp}(M_{c1}) \\
= & \quad \% \text{ Def. 16} \\
& \theta_c(0) \in I_c \cap I_{c1} \wedge \\
& \forall t : \langle \psi_c(t), \theta_c(t), \lim_{t \leftarrow t_1} \theta_c(t_1) \rangle \in T_c \cap T_{c1} \vee (\psi_c(t)(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\} \wedge \theta_c(t) = \lim_{t \leftarrow t_1} \theta_c(t_1)) \\
\rightarrow & \quad \% \text{ Def. 42} \\
& f(\theta_c(0)) \in I_a \cap I_{a1} \\
& \wedge \forall t : \langle \psi_a(t), f(\theta_c(t)), \lim_{t \leftarrow t_1} f(\theta_c(t_1)) \rangle \in T_a \cap T_{a1} \vee \\
& \quad (\psi_a(t)(\epsilon) \in \{\lambda, \mathbf{i}, \mathbf{e}\} \wedge f(\theta_c(t)) = \lim_{t \leftarrow t_1} f(\theta_c(t_1))) \\
= & \quad \% \text{ Def. 16} \\
& h_a \in \text{Comp}(M_a) \cap \text{Comp}(M_{a1}) \\
& \wedge \forall t : \langle \psi_c(t), \theta_c(t) \rangle = \langle \psi_a(t), \theta(t)_a \rangle \wedge f(\theta_c(t)) = \theta_a(t)
\end{aligned}$$

The proof of Lemma 13 is then as follows:

$$\begin{aligned}
& \mathcal{S}_c \text{ } W_c \text{ ref }^{W_a} \mathcal{S}_a \\
= & \quad \% \text{ Def. 39, Def. } W_c \text{ and } W_a \\
& \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \text{ and} \\
& \mathcal{O}_{X_c}(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \\
& \subseteq \mathcal{O}_{X_a}(Comp(M_a) \cap Comp(M_{a1}) \cap L_a \cap L_{a1}) \\
= & \quad \% \mathfrak{D}(B_c) = \mathfrak{D}(B_a) \\
& \mathcal{O}_{X_c}(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \\
& \subseteq \mathcal{O}_{X_a}(Comp(M_a) \cap Comp(M_{a1}) \cap L_a \cap L_{a1}) \\
\leftarrow & \quad \% \text{ From Def. 42,} \\
& f(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \subseteq L_a \cap L_{a1} \\
& \text{Property of } f, \\
& f(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \subseteq f(Comp(M_c) \cap Comp(M_{c1})), \\
& \text{by above result,} \\
& f(Comp(M_c) \cap Comp(M_{c1})) \subseteq Comp(M_a) \cap Comp(M_{a1}), \\
& \text{Resulting in ,} \\
& f(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \\
& \subseteq Comp(M_a) \cap Comp(M_{a1}) \cap L_a \cap L_{a1} \\
& \text{From Lemma 5(c),} \\
& \mathcal{O}_{X_a}(f(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1})) \\
& \subseteq \mathcal{O}_{X_a}(Comp(M_a) \cap Comp(M_{a1}) \cap L_a \cap L_{a1}), \\
& \text{From Def. 42,} \\
& \mathcal{O}_{X_c}(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1}) \\
& = \mathcal{O}_{X_a}(f(Comp(M_c) \cap Comp(M_{c1}) \cap L_c \cap L_{c1})) \\
& \text{true}
\end{aligned}$$